



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_int\_base = 176

## PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573

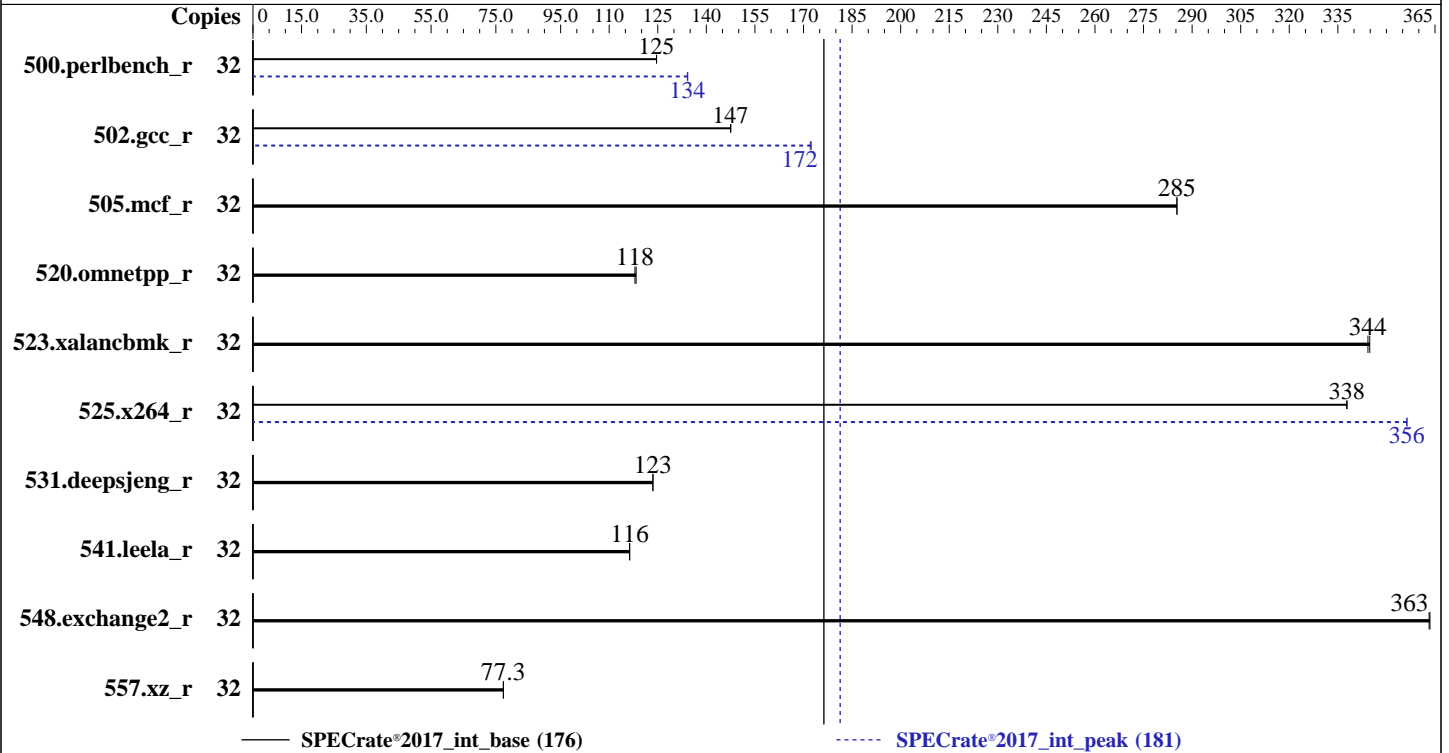
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 5415+  
 Max MHz: 4100  
 Nominal: 2900  
 Enabled: 16 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 22.5 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R, running at 4400)  
 Storage: 125 GB on tmpfs  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 1.0.1 released Feb-2023  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	409	125	<b>409</b>	<b>125</b>			32	379	134	<b>380</b>	<b>134</b>		
502.gcc_r	32	<b>307</b>	<b>147</b>	307	148			32	<b>263</b>	<b>172</b>	263	172		
505.mcf_r	32	<b>181</b>	<b>285</b>	181	285			32	<b>181</b>	<b>285</b>	181	285		
520.omnetpp_r	32	<b>356</b>	<b>118</b>	355	118			32	<b>356</b>	<b>118</b>	355	118		
523.xalancbmk_r	32	98.0	345	<b>98.1</b>	<b>344</b>			32	98.0	345	<b>98.1</b>	<b>344</b>		
525.x264_r	32	166	338	<b>166</b>	<b>338</b>			32	<b>157</b>	<b>356</b>	157	356		
531.deepsjeng_r	32	297	124	<b>297</b>	<b>123</b>			32	297	124	<b>297</b>	<b>123</b>		
541.leela_r	32	<b>456</b>	<b>116</b>	456	116			32	<b>456</b>	<b>116</b>	456	116		
548.exchange2_r	32	231	363	<b>231</b>	<b>363</b>			32	231	363	<b>231</b>	<b>363</b>		
557.xz_r	32	447	77.4	<b>447</b>	<b>77.3</b>			32	447	77.4	<b>447</b>	<b>77.3</b>		

SPECrate®2017\_int\_base = 176

SPECrate®2017\_int\_peak = 181

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/ia32:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/je5.0.1-32"
MALLOC_CONF = "retain:true"
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
 memory using Red Hat Enterprise Linux 8.4  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
`sync; echo 3> /proc/sys/vm/drop_caches`  
 runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`  
 jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

ADDC Setting : Disabled  
 DIMM Self Healing on  
 Uncorrectable Memory Error : Disabled  
 Virtualization Technology : Disabled  
 DCU Streamer Prefetcher : Disabled  
 LLC Prefetch : Disabled  
 Dead Line LLC Alloc : Disabled

System Profile : Custom  
 CPU Power Management : Maximum Performance  
 CLE : Disabled  
 C States : Autonomous  
 Memory Patrol Scrub : Disabled  
 Energy Efficiency Policy : Performance  
 PCI ASPM L1 Link  
 Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo  
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
 running on localhost Mon Mar 13 21:48:38 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. `uname -a`
2. `w`
3. Username
4. `ulimit -a`
5. `sysinfo process ancestry`
6. `/proc/cpuinfo`
7. `lscpu`
8. `numactl --hardware`

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

```

9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```

-----

```

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222/lp)
x86_64 x86_64 x86_64 GNU/Linux

```

-----

```

2. w
 21:48:38 up 5 min,  1 user,  load average: 5.24, 7.73, 3.67
USER  TTY      FROM             LOGIN@   IDLE   JCPU   PCPU   WHAT
root  tty1    -                21:43   5:10   1.04s  0.00s  /bin/bash ./dell-run-specrate.sh
--iterations 2 --output_format csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc

```

```

3. Username
From environment variable $USER:  root

```

-----

```

4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4124659
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4124659
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

```

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 34
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-specrate.sh --iterations 2 --output_format csv,html,pdf,txt -define

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Mar-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
/bin/bash ./dell-run-specrate.sh --iterations 2 --output_format csv,html,pdf,txt -define
Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2
--output_format csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--output_format csv,html,pdf,txt --define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc --nopower --runmode rate
--tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 5415+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000190
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 8
siblings       : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 128-143

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:  0-31
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) Gold 5415+
CPU family:            6
Model:                 143
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             2
Stepping:              8
BogoMIPS:              5800.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm  
abm 3dnowprefetch cpuid\_fault epb cat\_13 cat\_12 cdp\_13 invpcid\_single  
cdp\_12 ssbd mba ibrs ibpb stibp ibrs\_enhanced fsgsbase tsc\_adjust bmi1 hle  
avx2 smep bmi2 erms invpcid rtm cqm rdt\_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl  
xsavesopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local split\_lock\_detect avx\_vnni avx512\_bf16 wbnoinvd dtherm ida  
arat pln pts avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni vaes  
vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid  
bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize  
tsxldtrk pconfig arch\_lbr avx512\_fp16 amx\_tile flush\_l1d arch\_capabilities

L1d cache: 768 KiB (16 instances)  
L1i cache: 512 KiB (16 instances)  
L2 cache: 32 MiB (16 instances)  
L3 cache: 45 MiB (2 instances)  
NUMA node(s): 2  
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30  
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)  
node 0 cpus: 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30  
node 0 size: 515472 MB  
node 0 free: 505647 MB  
node 1 cpus: 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31  
node 1 size: 515717 MB  
node 1 free: 515331 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

9. /proc/meminfo

MemTotal: 1055937944 kB

10. who -r

run-level 3 Mar 13 21:43

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

Default Target Status  
multi-user running

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd bluetooth cron display-manager
firewalld getty@ haveged irqbalance iscsi issue-generator kbdsettings kdump kdump-early
klog libvirt lvm2-monitor nsd nvme-fc-boot-connections oracle postfix purge-kernels
rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
wickedd-nanny xencommons
enabled-runtime systemd-remount-fs
disabled accounts-daemon amavis apache2 apache2@ appstream-sync-cache autofs autoyast-initscripts
bgpd blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd
clamav-milter clamd console-getty cups cups-browsed ddclient debug-shell dhcpd dhcpd6
dhcrelay dhcrelay6 dirsrv@ dnsmasq ebttables exchange-bmc-os-info fetchmail freshclam gpm
grub2-once haveged-switch-root hwloc-dump-hwdata instsvcdrv ipmi ipmiev d iscsi-init iscsid
iscsiuio isisd issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask
man-db-create mariadb mariadb@ multipathd named nfs nfs-blkmap nfs-server nfsserver nmb
nvmf-autoconnect ospf6d ospfd ostree-remount racoon racoon-setkey radvd rarpd@ rdisc ripd
ripngd rpcbnd rpmconfigcheck rsyncd rtkit-daemon sapconf serial-getty@
smartd_generate_opts smb snmpd snmptrapd spamd spamd speech-dispatcherd squid srp_daemon
srp_daemon_port@ strongswan strongswan-starter svnserv sysstat
systemd-boot-check-no-failures systemd-network-generator systemd-nspawn@ systemd-sysext
systemd-time-wait-sync systemd-timesyncd tcsd udisks2 upower virtinterfaced virtnetworkd
virtnodedevd virtnwfilterd virtproxid virtqemud virtsecret d virtstoraged virtxend vsftpd
winbind xen-dom0-modules xen-init-dom0 xen-qemu-dom0-disk-backend xen-watchdog xenconsoled
xendomains xenstored zebra
indirect pcsd uuid virtlockd virtlogd wickedd
-----

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=35458c4c-5e83-48b0-808c-9f9263c843b9
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=320M,high
crashkernel=72M,low
-----

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
-----

```

```

-----
15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
-----

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_int\_base = 176

## PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Mar-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

### Platform Notes (Continued)

```

vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               20
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

```

```

-----
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 125G  4.3G 121G   4% /mnt/ramdisk

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:         Dell Inc.
Product:        PowerEdge HS5610
Product Family: PowerEdge
Serial:         DULAC01

```

```

-----
21. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
 13x 00AD00B300AD HMC94MEBRA121N 64 GB 2 rank 4800, configured at 4400
  3x 00AD069D00AD HMC94MEBRA112N 64 GB 2 rank 4800, configured at 4400

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)

```

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Platform Notes (Continued)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.0.1  
BIOS Date: 02/13/2023  
BIOS Revision: 1.0

## Compiler Version Notes

-----  
C | 502.gcc\_r(peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
C | 502.gcc\_r(peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
C++ | 520.omnetpp\_r(base, peak) 523.xalanbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
Fortran | 548.exchange2\_r(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icx

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_int\_base = 176

PowerEdge HS5610 (Intel Xeon Gold 5415+)

SPECrate®2017\_int\_peak = 181

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmallocc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-03-13 09:48:38-0400.

Report generated on 2024-01-29 17:35:09 by CPU2017 PDF formatter v6716.

Originally published on 2023-04-26.