



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

A+ Server 2124US-TNRP  
(H12DSU-iN , AMD EPYC 7543)

**SPECrate®2017\_fp\_base = 510**

**SPECrate®2017\_fp\_peak = 534**

CPU2017 License: 001176

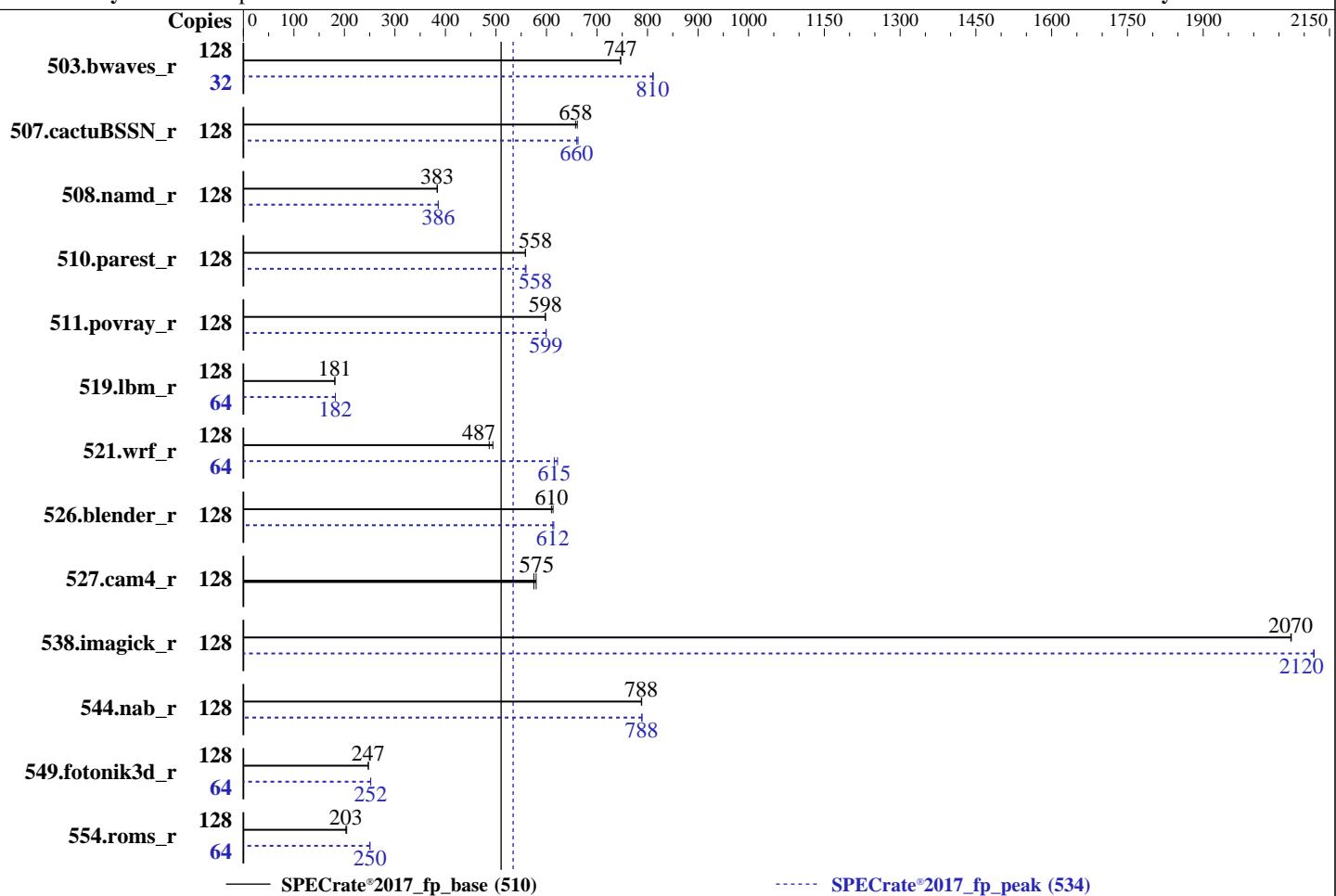
**Test Date:** Mar-2023

**Test Sponsor:** Supermicro

**Hardware Availability:** Mar-2021

**Tested by:** Supermicro

**Software Availability:** Feb-2023



### Hardware

CPU Name: AMD EPYC 7543  
Max MHz: 3700  
Nominal: 2800  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 2 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 512 KB I+D on chip per core  
L3: 256 MB I+D on chip per chip, 32 MB shared / 4 cores  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
Storage: 1 x 800 GB NVMe SSD  
Other: None

### Software

OS: Ubuntu 22.04.2 LTS  
Compiler: Kernel 5.15.0-67-generic  
Parallel: C/C++/Fortran: Version 3.2.0 of AOCC  
Firmware: No  
File System: Version 2.5 released Sep-2022  
System State: ext4  
Base Pointers: Run level 3 (multi-user)  
Peak Pointers: 64-bit  
Other: 64-bit  
Power Management: jemalloc: jemalloc memory allocator library v5.1.0  
BIOS and OS set to max performance at the cost of additional power usage.



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	<b>1719</b>	<b>747</b>	1719	747			32	395	811	<b>396</b>	<b>810</b>				
507.cactusBSSN_r	128	245	661	<b>246</b>	<b>658</b>			128	<b>246</b>	<b>660</b>	245	662				
508.namd_r	128	<b>317</b>	<b>383</b>	317	384			128	<b>315</b>	<b>386</b>	315	386				
510.parest_r	128	<b>600</b>	<b>558</b>	600	559			128	598	559	<b>600</b>	<b>558</b>				
511.povray_r	128	500	598	<b>500</b>	<b>598</b>			128	499	599	<b>499</b>	<b>599</b>				
519.lbm_r	128	744	181	<b>746</b>	<b>181</b>			64	370	182	<b>370</b>	<b>182</b>				
521.wrf_r	128	<b>589</b>	<b>487</b>	581	494			64	231	622	<b>233</b>	<b>615</b>				
526.blender_r	128	<b>320</b>	<b>610</b>	318	613			128	317	615	<b>318</b>	<b>612</b>				
527.cam4_r	128	<b>389</b>	<b>575</b>	386	580			128	<b>389</b>	<b>575</b>	386	580				
538.imagick_r	128	153	2070	<b>154</b>	<b>2070</b>			128	<b>150</b>	<b>2120</b>	150	2120				
544.nab_r	128	<b>273</b>	<b>788</b>	273	789			128	273	789	<b>273</b>	<b>788</b>				
549.fotonik3d_r	128	<b>2017</b>	<b>247</b>	2016	247			64	989	252	<b>990</b>	<b>252</b>				
554.roms_r	128	<b>1002</b>	<b>203</b>	994	205			64	406	250	<b>406</b>	<b>250</b>				

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.

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## Operating System Notes (Continued)

To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_rate_aocc320_milanx_A_lib/lib;/home/cpu2017/amd_rate_
    aocc320_milanx_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS Settings:

Determinism Control = Manual

Determinism Slider = Power

cTDP Control = Manual

cTDP = 240

Package Power Limit Control = Manual

Package Power Limit = 240

APBDIS = 1

NUMA Nodes Per Socket = NPS4

ACPI SRAT L3 cache As NUMA Domain = Enabled

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## Platform Notes (Continued)

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on as-2124us-tnrp-7543 Fri Mar 17 14:23:14 2023
```

SUT (System Under Test) info as seen by some common utilities.

---

### Table of contents

---

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.7)
  12. Failed units, from systemctl list-units --state=failed
  13. Services, from systemctl list-unit-files
  14. Linux kernel boot-time arguments, from /proc/cmdline
  15. cpupower frequency-info
  16. sysctl
  17. /sys/kernel/mm/transparent\_hugepage
  18. /sys/kernel/mm/transparent\_hugepage/khugepaged
  19. OS release
  20. Disk information
  21. /sys/devices/virtual/dmi/id
  22. dmidecode
  23. BIOS
- 

---

```
1. uname -a
Linux as-2124us-tnrp-7543 5.15.0-67-generic #74-Ubuntu SMP Wed Feb 22 14:14:39 UTC 2023 x86_64 x86_64
x86_64 GNU/Linux
```

---

```
2. w
14:23:14 up 4:28, 4 users, load average: 67.12, 111.81, 121.00
USER      TTY      FROM          LOGIN@    IDLE      JCPU      PCPU WHAT
lab       pts/0      -          10:00     4:22m   0.71s   0.01s  -bash
lab       pts/0      -          10:00     4:16m   1.46s   0.68s sudo su -
lab       pts/0      -          10:09     4:13m   0.08s   0.01s  -bash
```

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## Platform Notes (Continued)

```
lab      pts/1      -          10:09    4:13m  0.05s  0.05s sudo su -
```

---

### 3. Username

```
From environment variable $USER: root
From the command 'logname': lab
```

---

### 4. ulimit -a

```
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 2097152
process            4127157
nofiles             1024
vmemory(kbytes)     unlimited
locks               unlimited
rtprio              0
```

---

### 5. sysinfo process ancestry

```
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_rate_aocc320_milanx_A1.py
/bin/bash ./amd_rate_aocc320_milanx_A1.sh
runcpu --config amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 fprate
runcpu --configfile amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

---

### 6. /proc/cpuinfo

```
model name      : AMD EPYC 7543 32-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 1
stepping        : 1
```

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## Platform Notes (Continued)

```
microcode      : 0xa001173
bugs          : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 2560 4K pages
cpu cores     : 32
siblings       : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 64-127
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.2:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	48 bits physical, 48 bits virtual
Byte Order:	Little Endian
CPU(s):	128
On-line CPU(s) list:	0-127
Vendor ID:	AuthenticAMD
Model name:	AMD EPYC 7543 32-Core Processor
CPU family:	25
Model:	1
Thread(s) per core:	2
Core(s) per socket:	32
Socket(s):	2
Stepping:	1
Frequency boost:	enabled
CPU max MHz:	3737.8899
CPU min MHz:	1500.0000
BogoMIPS:	5599.86
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpf perf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock

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## Platform Notes (Continued)

```

nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold v_vmsave_vmload vgif v_spec_ctrl umip pku ospke vaes
vpclmulqdq rdpid overflow_recov succor smca fsrm
AMD-V
Virtualization:
L1d cache: 2 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 32 MiB (64 instances)
L3 cache: 512 MiB (16 instances)
NUMA node(s): 16
NUMA node0 CPU(s): 0-3,64-67
NUMA node1 CPU(s): 4-7,68-71
NUMA node2 CPU(s): 8-11,72-75
NUMA node3 CPU(s): 12-15,76-79
NUMA node4 CPU(s): 16-19,80-83
NUMA node5 CPU(s): 20-23,84-87
NUMA node6 CPU(s): 24-27,88-91
NUMA node7 CPU(s): 28-31,92-95
NUMA node8 CPU(s): 32-35,96-99
NUMA node9 CPU(s): 36-39,100-103
NUMA node10 CPU(s): 40-43,104-107
NUMA node11 CPU(s): 44-47,108-111
NUMA node12 CPU(s): 48-51,112-115
NUMA node13 CPU(s): 52-55,116-119
NUMA node14 CPU(s): 56-59,120-123
NUMA node15 CPU(s): 60-63,124-127
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling, PBRSB-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	2M	8	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	512K	32M	8	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

-----  
8. numactl --hardware

(Continued on next page)



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## Platform Notes (Continued)

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 16 nodes (0-15)
node 0 cpus: 0-3,64-67
node 0 size: 64316 MB
node 0 free: 62826 MB
node 1 cpus: 4-7,68-71
node 1 size: 64507 MB
node 1 free: 64021 MB
node 2 cpus: 8-11,72-75
node 2 size: 64509 MB
node 2 free: 63962 MB
node 3 cpus: 12-15,76-79
node 3 size: 64508 MB
node 3 free: 64067 MB
node 4 cpus: 16-19,80-83
node 4 size: 64509 MB
node 4 free: 64051 MB
node 5 cpus: 20-23,84-87
node 5 size: 64508 MB
node 5 free: 64053 MB
node 6 cpus: 24-27,88-91
node 6 size: 64509 MB
node 6 free: 64035 MB
node 7 cpus: 28-31,92-95
node 7 size: 64496 MB
node 7 free: 64029 MB
node 8 cpus: 32-35,96-99
node 8 size: 64509 MB
node 8 free: 64084 MB
node 9 cpus: 36-39,100-103
node 9 size: 64473 MB
node 9 free: 64035 MB
node 10 cpus: 40-43,104-107
node 10 size: 64509 MB
node 10 free: 64074 MB
node 11 cpus: 44-47,108-111
node 11 size: 64508 MB
node 11 free: 64084 MB
node 12 cpus: 48-51,112-115
node 12 size: 64509 MB
node 12 free: 64080 MB
node 13 cpus: 52-55,116-119
node 13 size: 64508 MB
node 13 free: 64078 MB
node 14 cpus: 56-59,120-123
node 14 size: 64509 MB
node 14 free: 64073 MB
```

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## Platform Notes (Continued)

node 15 cpus: 60-63,124-127

node 15 size: 64503 MB

node 15 free: 64072 MB

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0:	10	11	12	12	12	12	12	12	32	32	32	32	32	32	32	32
1:	11	10	12	12	12	12	12	12	32	32	32	32	32	32	32	32
2:	12	12	10	11	12	12	12	12	32	32	32	32	32	32	32	32
3:	12	12	11	10	12	12	12	12	32	32	32	32	32	32	32	32
4:	12	12	12	12	10	11	12	12	32	32	32	32	32	32	32	32
5:	12	12	12	12	11	10	12	12	32	32	32	32	32	32	32	32
6:	12	12	12	12	12	12	10	11	32	32	32	32	32	32	32	32
7:	12	12	12	12	12	12	11	10	32	32	32	32	32	32	32	32
8:	32	32	32	32	32	32	32	32	10	11	12	12	12	12	12	12
9:	32	32	32	32	32	32	32	32	11	10	12	12	12	12	12	12
10:	32	32	32	32	32	32	32	32	12	12	10	11	12	12	12	12
11:	32	32	32	32	32	32	32	32	12	12	11	10	12	12	12	12
12:	32	32	32	32	32	32	32	32	12	12	12	12	10	11	12	12
13:	32	32	32	32	32	32	32	32	12	12	12	12	11	10	12	12
14:	32	32	32	32	32	32	32	32	12	12	12	12	12	10	11	11
15:	32	32	32	32	32	32	32	32	12	12	12	12	12	11	10	10

9. /proc/meminfo

MemTotal: 1056669088 kB

10. who -r

run-level 3 Mar 17 09:57

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.7)

Default Target Status  
multi-user degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* systemd-networkd-wait-online.service	loaded	failed	Wait for Network to be Configured	

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db

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## Platform Notes (Continued)

```
setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
          systemd-timesyncd thermald ua-reboot-cmcs ubuntu-advantage udisks2 ufw vgaauth
enabled-runtime netplan-ovs-cleanups systemd-fsck-root systemd-remount-fs
disabled      apparmor console-getty debug-shell iscsid nftables rsync serial-getty@
               systemd-boot-check-no-failures systemd-network-generator systemd-sysext
               systemd-time-wait-sync upower
generated     apport
indirect       uuidd
masked        cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcs screen-cleanup sudo
               x11-common
```

---

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.15.0-67-generic  
root=UUID=63a5bd49-1a2b-4fc5-945b-bc07b22b6218  
ro

---

15. cpupower frequency-info  
analyzing CPU 0:  
 current policy: frequency should be within 1.50 GHz and 2.80 GHz.  
 The governor "performance" may decide which speed to use  
 within this range.  
boost state support:  
 Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 2800MHz

---

16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

A+ Server 2124US-TNRP  
(H12DSU-iN , AMD EPYC 7543)

SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

CPU2017 License: 001176

Test Date: Mar-2023

Test Sponsor: Supermicro

Hardware Availability: Mar-2021

Tested by: Supermicro

Software Availability: Feb-2023

## Platform Notes (Continued)

```
vm.swappiness           1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode    1
```

```
-----17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hugepage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

```
-----18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.2 LTS
```

```
-----20. Disk information
SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used  Avail Use% Mounted on
/dev/nvme0n1p2  ext4  732G  17G  678G  3%  /
```

```
-----21. /sys/devices/virtual/dmi/id
Vendor:      Supermicro
Product:     Super Server
Serial:      0123456789
```

```
-----22. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
```

Memory:

16x NO DIMM Unknown

(Continued on next page)



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## Platform Notes (Continued)

16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends Inc.  
BIOS Version: 2.5  
BIOS Date: 09/14/2022  
BIOS Revision: 5.22

## Compiler Version Notes

```
=====
C           | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
              | 544.nab_r(base, peak)
-----
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
-----

=====
C++          | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
-----

=====
C++, C       | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
```

(Continued on next page)



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SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====  
C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on  
LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

(Continued on next page)



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SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Compiler Version Notes (Continued)

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -D\_BOOL\_DEFINED -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64



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## Supermicro

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Test Sponsor: Supermicro

Tested by: Supermicro

SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Base Optimization Flags

C benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -fno
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -fno -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -Hz,1,0x1 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loop-fusion
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -fno -Wl,-mllvm -Wl,-region-vectorize
```

(Continued on next page)



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## Supermicro

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SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsv-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -z muldefs -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
```

(Continued on next page)



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SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

(Continued on next page)



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## Supermicro

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SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

Test Date: Mar-2023

Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

C++ benchmarks:

(Continued on next page)



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Hardware Availability: Mar-2021

Software Availability: Feb-2023

## Peak Optimization Flags (Continued)

```
508.namd_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc
```

```
510.parest_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc
```

Fortran benchmarks:

```
503.bwaves_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Kieee
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

A+ Server 2124US-TNRP  
(H12DSU-iN , AMD EPYC 7543)

SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

CPU2017 License: 001176

Test Date: Mar-2023

Test Sponsor: Supermicro

Hardware Availability: Mar-2021

Tested by: Supermicro

Software Availability: Feb-2023

## Peak Optimization Flags (Continued)

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-Hz,1,0x1 -mllvm -fuse-tile-inner-loop -lamdlibm
-ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Mrecursive -lamdlibm
-ljemalloc -lflang
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -lamdlibm -ljemalloc
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

A+ Server 2124US-TNRP  
(H12DSU-iN , AMD EPYC 7543)

SPECrate®2017\_fp\_base = 510

SPECrate®2017\_fp\_peak = 534

CPU2017 License: 001176

Test Date: Mar-2023

Test Sponsor: Supermicro

Hardware Availability: Mar-2021

Tested by: Supermicro

Software Availability: Feb-2023

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-finline-aggressive -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -Mrecursive -lamdlibm -ljemalloc
-lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revH.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revH.xml>

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