



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

CPU2017 License: 3

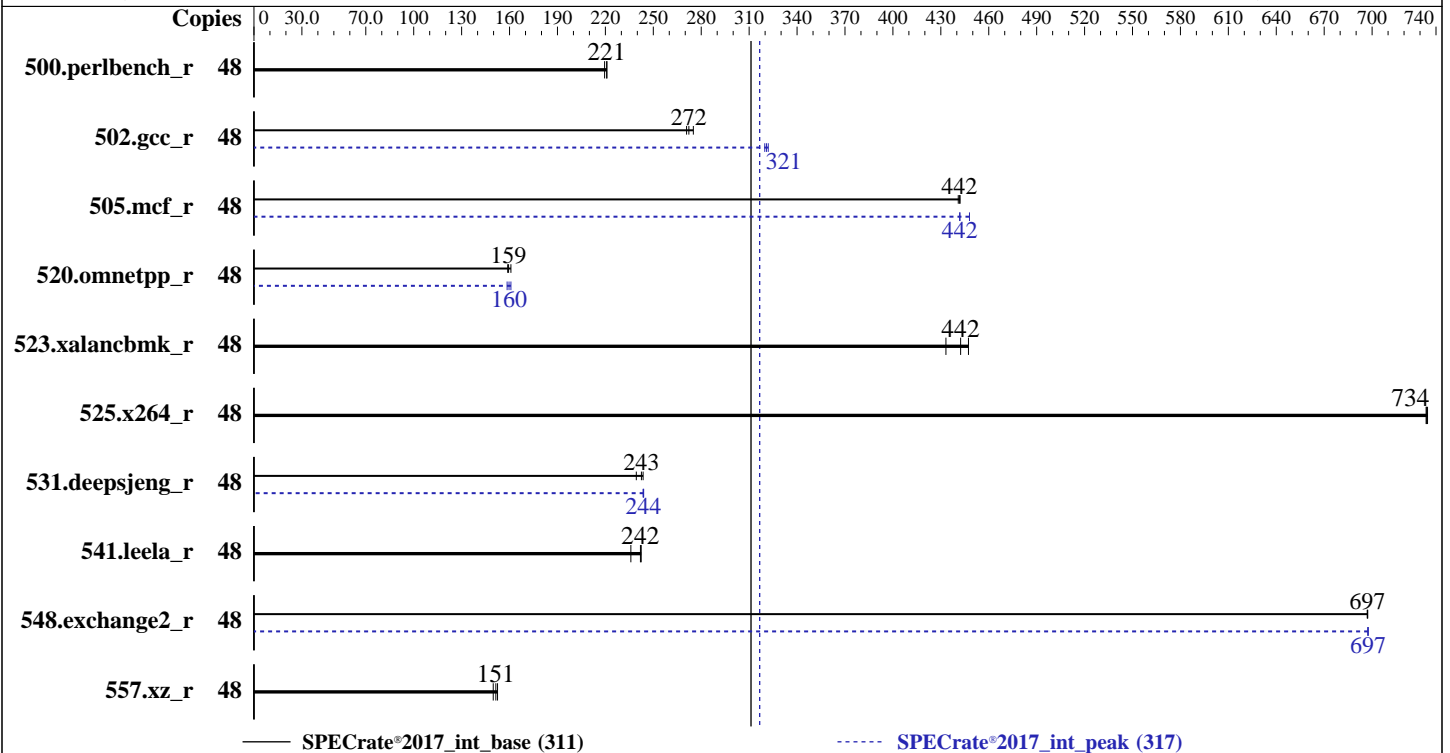
Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2023

Hardware Availability: Dec-2022

Software Availability: Nov-2022



### Hardware

CPU Name: AMD EPYC 9274F  
 Max MHz: 4300  
 Nominal: 4050  
 Enabled: 24 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 32 MB shared / 3 cores  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 1.6 TB NVMe SSD, RAID 0  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 Kernel 5.14.0-70.13.1.el9\_0.x86\_64  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: No  
 Firmware: HPE BIOS Version v1.12 11/24/2022 released  
 Nov-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jan-2023  
Hardware Availability: Dec-2022  
Software Availability: Nov-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	<b>346</b>	<b>221</b>	348	220	346	221	48	<b>346</b>	<b>221</b>	348	220	346	221
502.gcc_r	48	247	275	<b>250</b>	<b>272</b>	251	271	48	<b>212</b>	<b>321</b>	211	322	213	320
505.mcf_r	48	175	442	176	441	<b>176</b>	<b>442</b>	48	176	442	173	448	<b>175</b>	<b>442</b>
520.omnetpp_r	48	391	161	396	159	<b>395</b>	<b>159</b>	48	391	161	<b>394</b>	<b>160</b>	397	158
523.xalancbmk_r	48	113	447	<b>115</b>	<b>442</b>	117	433	48	113	447	<b>115</b>	<b>442</b>	117	433
525.x264_r	48	114	735	115	734	<b>115</b>	<b>734</b>	48	114	735	115	734	<b>115</b>	<b>734</b>
531.deepsjeng_r	48	226	244	230	239	<b>227</b>	<b>243</b>	48	225	244	226	244	<b>226</b>	<b>244</b>
541.leela_r	48	<b>329</b>	<b>242</b>	337	236	328	242	48	<b>329</b>	<b>242</b>	337	236	328	242
548.exchange2_r	48	<b>180</b>	<b>697</b>	180	697	180	697	48	<b>180</b>	<b>697</b>	180	697	180	697
557.xz_r	48	340	152	346	150	<b>342</b>	<b>151</b>	48	340	152	346	150	<b>342</b>	<b>151</b>

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2023

**Hardware Availability:** Dec-2022

**Software Availability:** Nov-2022

## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/home/CPU2017/amd\_rate\_aocc400\_genoa\_B\_lib/lib:/home/CPU2017/amd\_rate\_a  
occ400\_genoa\_B\_lib/lib32:"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration  
Workload Profile set to General Throughput Compute  
Determinism Control set to Manual  
Performance Determinism set to Power Deterministic  
Last-Level Cache (LLC) as NUMA Node set to Enabled  
NUMA memory domains per socket set to Four memory domains per socket  
Thermal Configuration set to Maximum Cooling  
ACPI CST C2 Latency set to 18 microseconds  
Workload Profile set to Custom  
Power Regulator set to OS Control Mode

The system ROM used for this result contains microcode version 0x0A10110e for the AMD EPYC 9nn4X family of processors. The reference code/AGESA version used in this ROM is version GenoaPI 1.0.0.1-L6

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2023

**Hardware Availability:** Dec-2022

**Software Availability:** Nov-2022

## Platform Notes (Continued)

sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Thu Apr 7 14:09:21 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 9274F 24-Core Processor

1 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 24

siblings : 48

physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30

From lscpu from util-linux 2.37.4:

Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Address sizes: 52 bits physical, 57 bits virtual  
Byte Order: Little Endian  
CPU(s): 48  
On-line CPU(s) list: 0-47  
Vendor ID: AuthenticAMD  
BIOS Vendor ID: Advanced Micro Devices, Inc.  
Model name: AMD EPYC 9274F 24-Core Processor  
BIOS Model name: AMD EPYC 9274F 24-Core Processor  
CPU family: 25  
Model: 17  
Thread(s) per core: 2  
Core(s) per socket: 24  
Socket(s): 1  
Stepping: 1  
Frequency boost: enabled  
CPU max MHz: 4303.1250  
CPU min MHz: 1500.0000  
BogoMIPS: 8088.23  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm constant\_tsc rep\_good nopl nonstop\_tsc cpuid extd\_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4\_1 sse4\_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_llc mwaitx cpb cat\_l3 cdp\_l3 invpcid\_single hw\_pstate ssbd mba ibrs

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2023  
**Hardware Availability:** Dec-2022  
**Software Availability:** Nov-2022

## Platform Notes (Continued)

ibpb stibp vmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt\_a avx512f  
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw  
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local avx512\_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd\_ppin arat npt  
lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif v\_spec\_ctrl avx512vbmi umip pku ospke  
avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57  
rdpid overflow\_recov succor smca fsrm flush\_lli

Virtualization:

AMD-V

L1d cache: 768 KiB (24 instances)

L1i cache: 768 KiB (24 instances)

L2 cache: 24 MiB (24 instances)

L3 cache: 256 MiB (8 instances)

NUMA node(s):

8

NUMA node0 CPU(s): 0-2,24-26

NUMA node1 CPU(s): 12-14,36-38

NUMA node2 CPU(s): 6-8,30-32

NUMA node3 CPU(s): 18-20,42-44

NUMA node4 CPU(s): 9-11,33-35

NUMA node5 CPU(s): 21-23,45-47

NUMA node6 CPU(s): 3-5,27-29

NUMA node7 CPU(s): 15-17,39-41

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP always-on, RSB filling

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	768K	8	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	8	Unified	2	2048	1	64
L3	32M	256M	16	Unified	3	32768	1	64

/proc/cpuinfo cache data  
cache size : 1024 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2023

**Hardware Availability:** Dec-2022

**Software Availability:** Nov-2022

## Platform Notes (Continued)

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 2 24 25 26
node 0 size: 48136 MB
node 0 free: 47828 MB
node 1 cpus: 12 13 14 36 37 38
node 1 size: 48382 MB
node 1 free: 48078 MB
node 2 cpus: 6 7 8 30 31 32
node 2 size: 48382 MB
node 2 free: 48018 MB
node 3 cpus: 18 19 20 42 43 44
node 3 size: 48382 MB
node 3 free: 48096 MB
node 4 cpus: 9 10 11 33 34 35
node 4 size: 48346 MB
node 4 free: 48105 MB
node 5 cpus: 21 22 23 45 46 47
node 5 size: 48382 MB
node 5 free: 48120 MB
node 6 cpus: 3 4 5 27 28 29
node 6 size: 48382 MB
node 6 free: 48115 MB
node 7 cpus: 15 16 17 39 40 41
node 7 size: 48334 MB
node 7 free: 48040 MB
node distances:
node  0  1  2  3  4  5  6  7
  0:  10  11  12  12  12  12  12  12
  1:  11  10  12  12  12  12  12  12
  2:  12  12  10  11  12  12  12  12
  3:  12  12  11  10  12  12  12  12
  4:  12  12  12  12  10  11  12  12
  5:  12  12  12  12  12  11  10  12  12
  6:  12  12  12  12  12  12  10  11
  7:  12  12  12  12  12  12  11  10

```

```

From /proc/meminfo
MemTotal:      396010920 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2023  
**Hardware Availability:** Dec-2022  
**Software Availability:** Nov-2022

## Platform Notes (Continued)

```
VERSION="9.0 (Plow)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="9.0"
PLATFORM_ID="platform:el9"
PRETTY_NAME="Red Hat Enterprise Linux 9.0 (Plow)"
ANSI_COLOR="0;31"
```

```
redhat-release: Red Hat Enterprise Linux release 9.0 (Plow)
system-release: Red Hat Enterprise Linux release 9.0 (Plow)
system-release-cpe: cpe:/o:redhat:enterprise_linux:9::baseos
```

```
uname -a:
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14
12:42:38 EDT 2022 x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Retpolines, IBPB:
conditional, IBRS_FW, STIBP:
always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
```

run-level 3 Apr 7 05:30

```
SPEC is set to: /home/CPU2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G   23G  796G   3% /home
```

```
From /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant DL345 Gen11
Product Family: ProLiant
Serial:          DL345G11-004
```

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2023  
**Hardware Availability:** Dec-2022  
**Software Availability:** Nov-2022

## Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Hynix HMC88MEBRA113N 32 GB 2 rank 4800

BIOS:

BIOS Vendor: HPE  
BIOS Version: 1.12  
BIOS Date: 11/24/2022  
BIOS Revision: 1.12  
Firmware Revision: 1.10

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 502.gcc\_r(peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
525.x264\_r(base, peak) 557.xz\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 502.gcc\_r(peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2023  
**Hardware Availability:** Dec-2022  
**Software Availability:** Nov-2022

## Compiler Version Notes (Continued)

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
=====

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak)  
| 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
=====

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
=====

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2023

**Hardware Availability:** Dec-2022

**Software Availability:** Nov-2022

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```



# SPEC CPU<sup>®</sup>2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate<sup>®</sup>2017\_int\_base = 311

SPECrate<sup>®</sup>2017\_int\_peak = 317

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2023

**Hardware Availability:** Dec-2022

**Software Availability:** Nov-2022

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2023

Hardware Availability: Dec-2022

Software Availability: Nov-2022

## Peak Optimization Flags (Continued)

```
502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

```
505.mcf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc
```

525.x264\_r: basepeak = yes

557.xz\_r: basepeak = yes

C++ benchmarks:

```
520.omnetpp_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

523.xalancbmk\_r: basepeak = yes

```
531.deepsjeng_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

541.leela\_r: basepeak = yes

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.05 GHz, AMD EPYC 9274F)

SPECrate®2017\_int\_base = 311

SPECrate®2017\_int\_peak = 317

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2023

Hardware Availability: Dec-2022

Software Availability: Nov-2022

## Peak Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fepilog-vectorization-of-inductions  
-mllvm -optimize-strided-mem-cost -floop-transform  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm  
-lflang -lamdalloc
```

## Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument
```

```
-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.html>

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.xml>

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2022-04-07 04:39:20-0400.

Report generated on 2023-03-02 11:20:00 by CPU2017 PDF formatter v6442.

Originally published on 2023-02-28.