SPEC CPU®2017 Integer Speed Result

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Nettrix
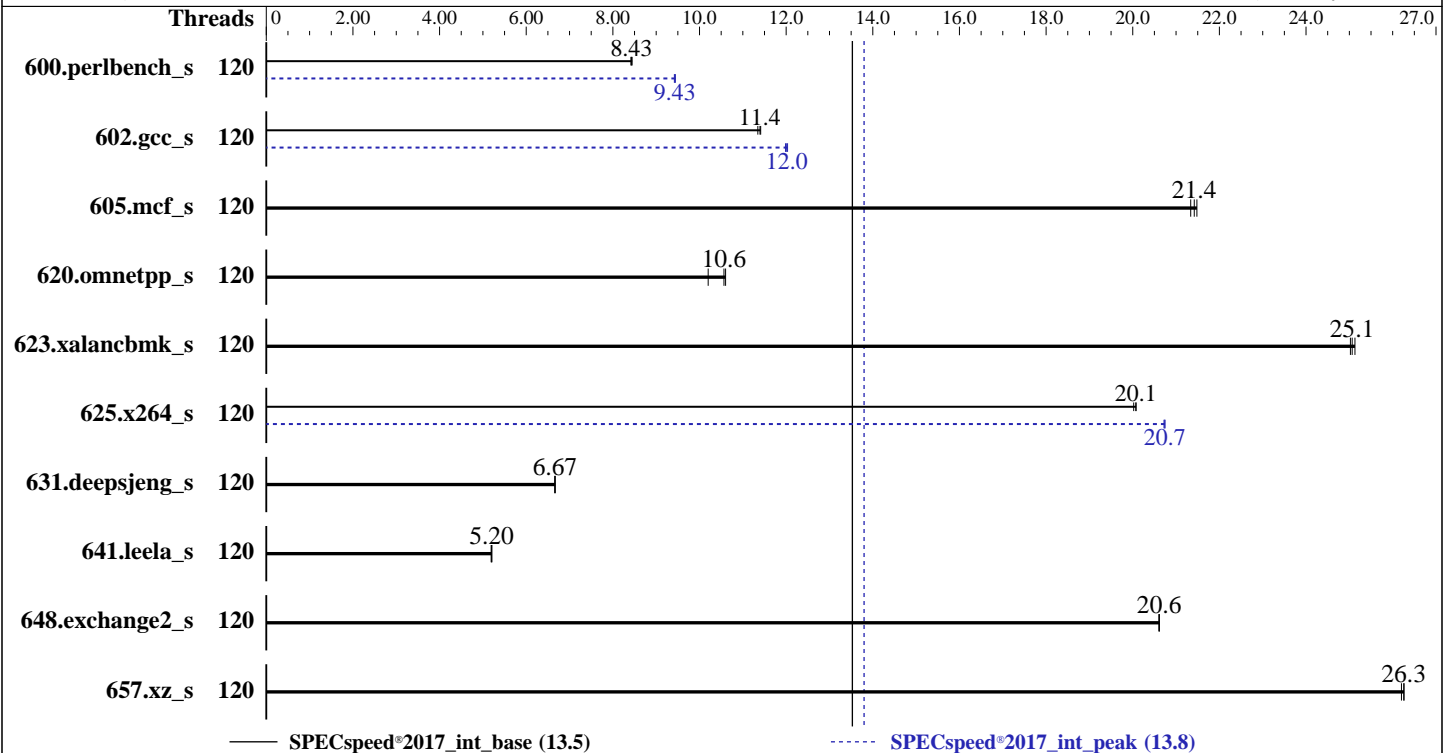
R620 G50 (Intel Xeon Platinum 8490H, 1.90 GHz)

$SPECspeed®2017\_int\_base = 13.5$

$SPECspeed®2017\_int\_peak = 13.8$

CPU2017 License: 6138　　　　　　　　　　　　　　　　**Test Date:** Dec-2022
Test Sponsor: Nettrix　　　　　　　　　　　　　　**Hardware Availability:** Jan-2023
Tested by: Nettrix　　　　　　　　　　　　　　　**Software Availability:** May-2022

Threads		Result
600.perlbench_s	120	8.43 (base) / 9.43 (peak)
602.gcc_s	120	11.4 (base) / 12.0 (peak)
605.mcf_s	120	21.4
620.omnetpp_s	120	10.6
623.xalancbmk_s	120	25.1
625.x264_s	120	20.1 (base) / 20.7 (peak)
631.deepsjeng_s	120	6.67
641.leela_s	120	5.20
648.exchange2_s	120	20.6
657.xz_s	120	26.3

SPECspeed®2017_int_base (13.5)　　　　SPECspeed®2017_int_peak (13.8)

Hardware

CPU Name: Intel Xeon Platinum 8490H
 Max MHz: 3500
 Nominal: 1900
Enabled: 120 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 112.5 MB I+D on chip per chip
 Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 4 TB SATA HDD, 7200 RPM
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP3
5.3.18-57-default
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler Build 20220316 for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler Build 20220316 for Linux;
Parallel: Yes
Firmware: Nettrix BIOS Version NNH1041018-U00-1 released Nov-2022
File System: xfs
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	120	**211**	**8.43**	211	8.42	210	8.44	120	**188**	**9.43**	188	9.42	188	9.44
602.gcc_s	120	**349**	**11.4**	351	11.3	349	11.4	120	331	12.0	**332**	**12.0**	332	12.0
605.mcf_s	120	221	21.3	**220**	**21.4**	220	21.5	120	221	21.3	**220**	**21.4**	220	21.5
620.omnetpp_s	120	160	10.2	**154**	**10.6**	154	10.6	120	160	10.2	**154**	**10.6**	154	10.6
623.xalancbmk_s	120	56.6	25.0	**56.5**	**25.1**	56.4	25.1	120	56.6	25.0	**56.5**	**25.1**	56.4	25.1
625.x264_s	120	88.1	20.0	**87.9**	**20.1**	87.9	20.1	120	**85.0**	**20.7**	85.0	20.7	85.1	20.7
631.deepsjeng_s	120	215	6.66	215	6.67	**215**	**6.67**	120	215	6.66	215	6.67	**215**	**6.67**
641.leela_s	120	329	5.19	328	5.20	**328**	**5.20**	120	329	5.19	328	5.20	**328**	**5.20**
648.exchange2_s	120	**143**	**20.6**	143	20.6	143	20.6	120	**143**	**20.6**	143	20.6	143	20.6
657.xz_s	120	**235**	**26.3**	235	26.3	236	26.2	120	**235**	**26.3**	235	26.3	236	26.2
SPECspeed®2017_int_base =		**13.5**												
SPECspeed®2017_int_peak =		**13.8**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
SPEC has ruled that the compiler used for this result was performing a compilation
that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s
benchmarks using a priori knowledge of the SPEC code and dataset to perform a
transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4
https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer
publish results using this optimization.

This result is left in the SPEC results database for historical reference.
```

Operating System Notes

```
 Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/lijq/lib/intel64:/home/lijq/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Redhat Enterprise Linux 8.0
 NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
   is mitigated in the system as tested and documented.
```

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CPU2017 License: 6138　　　　　　　　　　　　　　　　**Test Date:** Dec-2022
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Tested by: Nettrix　　　　　　　　　　　　　　　　**Software Availability:** May-2022

General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
  is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
  is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Configuration:
  Enable LP [Global] set to Single LP
  LLC Prefetch set to Enabled
  SNC (Sub NUMA) set to Disabled
  Patrol Scrub set to Disabled
  LLC dead line alloc set to Disabled
  XPT Prefetch set to Enabled
  KTI Prefetch set to Auto
  SR-IOV Support set to Disabled
  Energy Efficient Turbo set to Disabled


Sysinfo program /home/lijq/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Sat Jan  8 18:45:54 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8490H
      2  "physical id"s (chips)
      120 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 60
      siblings  : 60
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59

From lscpu from util-linux 2.36.2:
    Architecture:             x86_64
    CPU op-mode(s):           32-bit, 64-bit
    Byte Order:               Little Endian
    Address sizes:            52 bits physical, 57 bits virtual
    CPU(s):                   120
    On-line CPU(s) list:      0-119
    Thread(s) per core:       1
    Core(s) per socket:       60
    Socket(s):                2
    NUMA node(s):             2
```

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Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: May-2022

Platform Notes (Continued)

```
        Vendor ID:                GenuineIntel
        CPU family:               6
        Model:                    143
        Model name:               Intel(R) Xeon(R) Platinum 8490H
        Stepping:                 8
        Frequency boost:          enabled
        CPU MHz:                  800.000
        CPU max MHz:              1901.0000
        CPU min MHz:              800.0000
        BogoMIPS:                 3800.00
        Virtualization:           VT-x
        L1d cache:                5.6 MiB
        L1i cache:                3.8 MiB
        L2 cache:                 240 MiB
        L3 cache:                 225 MiB
        NUMA node0 CPU(s):        0-59
        NUMA node1 CPU(s):        60-119
        Vulnerability Itlb multihit:    Not affected
        Vulnerability L1tf:             Not affected
        Vulnerability Mds:              Not affected
        Vulnerability Meltdown:         Not affected
        Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
        prctl and seccomp
        Vulnerability Spectre v1:       Mitigation; usercopy/swapgs barriers and __user
        pointer sanitization
        Vulnerability Spectre v2:       Mitigation; Enhanced IBRS, IBPB conditional, RSB
        filling
        Vulnerability Srbds:            Not affected
        Vulnerability Tsx async abort:  Not affected
        Flags:                          fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
        pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx
        smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
        epb cat_l3 cat_l2 cdp_l3 invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp
        ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1
        hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
        split_lock_detect avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku
        ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
        avx512_vpopcntdq la57 rdpid cldemote movdiri movdir64b enqcmd fsrm md_clear
        serialize tsxldtrk pconfig avx512_fp16 flush_l1d arch_capabilities


    From lscpu --cache:
        NAME ONE-SIZE ALL-SIZE WAYS TYPE         LEVEL   SETS PHY-LINE COHERENCY-SIZE
        L1d       48K     5.6M   12 Data             1     64        1             64
        L1i       32K     3.8M    8 Instruction      1     64        1             64
        L2         2M     240M   16 Unified          2   2048        1             64
        L3     112.5M     225M   15 Unified          3 122880        1             64

    /proc/cpuinfo cache data
       cache size : 115200 KB

    From numactl --hardware
    WARNING: a numactl 'node' might or might not correspond to a physical chip.
      available: 2 nodes (0-1)
      node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
      28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56
```

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Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: May-2022

Platform Notes (Continued)

```
   57 58 59
 node 0 size: 515603 MB
 node 0 free: 508000 MB
 node 1 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84
 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109
 110 111 112 113 114 115 116 117 118 119
 node 1 size: 515786 MB
 node 1 free: 513625 MB
 node distances:
 node   0   1
   0:  10  21
   1:  21  10

From /proc/meminfo
   MemTotal:       1056144076 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
   ondemand

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP3"
      VERSION_ID="15.3"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP3"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp3"

uname -a:
   Linux localhost 5.3.18-57-default #1 SMP Wed Apr 28 10:54:41 UTC 2021 (ba3c2e9) x86_64
   x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                    Not affected
CVE-2018-3620 (L1 Terminal Fault):                 Not affected
Microarchitectural Data Sampling:                  Not affected
CVE-2017-5754 (Meltdown):                          Not affected
CVE-2018-3639 (Speculative Store Bypass):          Mitigation: Speculative Store
                                                   Bypass disabled via prctl and
                                                   seccomp
CVE-2017-5753 (Spectre variant 1):                 Mitigation: usercopy/swapgs
                                                   barriers and __user pointer
                                                   sanitization
CVE-2017-5715 (Spectre variant 2):                 Mitigation: Enhanced IBRS, IBPB:
                                                   conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):           Not affected

run-level 5 Jan 7 19:52

SPEC is set to: /home/lijq
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda3       xfs   2.7T  141G  2.5T   6% /home

From /sys/devices/virtual/dmi/id
```

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Platform Notes (Continued)

```
       Vendor:         Nettrix
       Product:        R620 G50
       Product Family: Rack
       Serial:         N/A

   Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
   interpret this section. The 'dmidecode' program reads system data which is "intended to
   allow hardware to be accurately determined", but the intent may not be met, as there are
   frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
     Memory:
       8x Hynix HMCG94MEBQA121N 64 GB 2 rank 4800
       8x Hynix HMCG94MEBQA123N 64 GB 2 rank 4800

   BIOS:
     BIOS Vendor:        American Megatrends International, LLC.
     BIOS Version:       NNH1041018-U00-1
     BIOS Date:          11/01/2022
     BIOS Revision:      5.29

   (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================================
C        | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
         | 657.xz_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
         | 641.leela_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
Fortran | 648.exchange2_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

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Base Compiler Invocation (Continued)

Fortran benchmarks:
`ifx`

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:
```
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:
```
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:
`icx`

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

(Continued on next page)

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Peak Optimization Flags (Continued)

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.html`
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.xml`
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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