



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_fp\_base = 585

xFusion 5885H V5 (Intel Xeon Platinum 8276L)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 6488

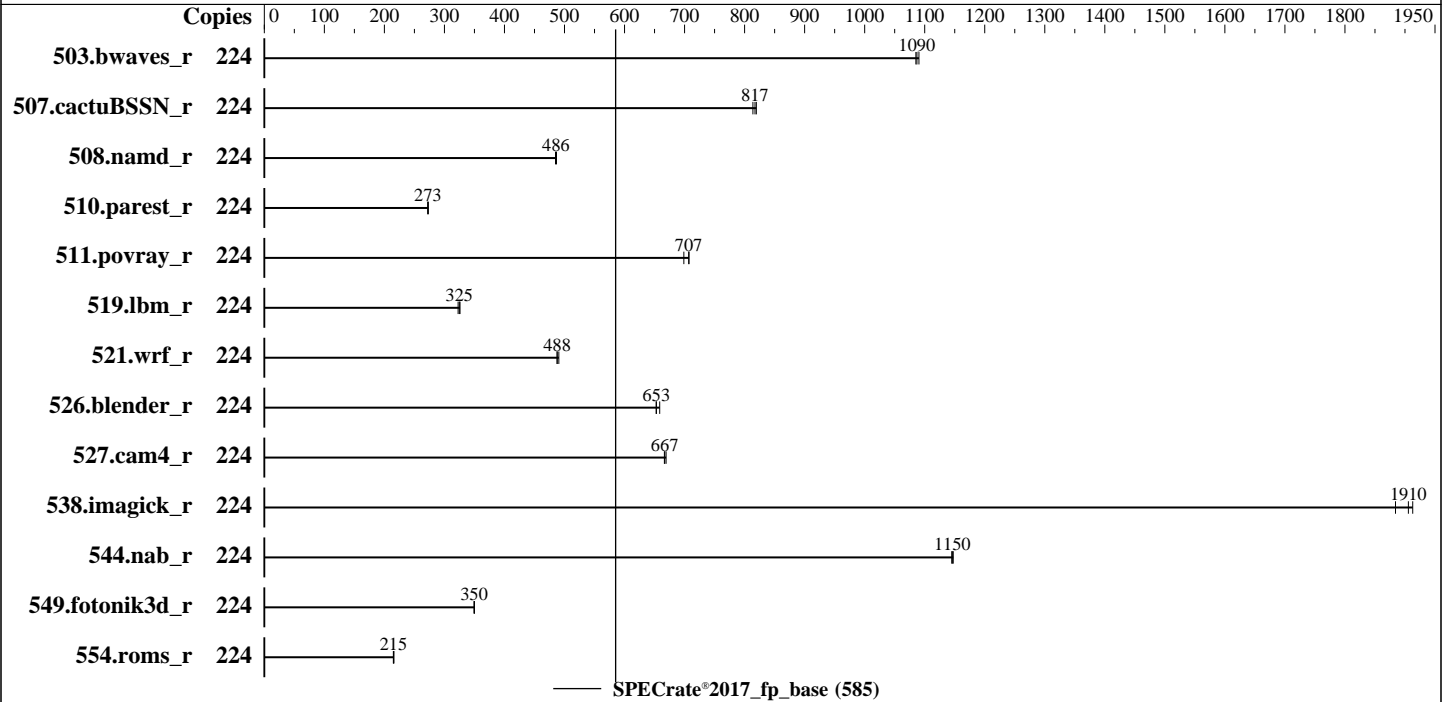
Test Sponsor: xFusion

Tested by: xFusion

Test Date: Mar-2022

Hardware Availability: Apr-2021

Software Availability: Jun-2021



### Hardware

CPU Name: Intel Xeon Platinum 8276L  
 Max MHz: 4000  
 Nominal: 2200  
 Enabled: 112 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 38.5 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx4 PC4-2933Y-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux  
 Parallel: No  
 Firmware: Version 8.23 Released Dec-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	224	2060	1090	<b>2066</b>	<b>1090</b>	2070	1090							
507.cactuBSSN_r	224	348	814	<b>347</b>	<b>817</b>	346	820							
508.namd_r	224	<b>438</b>	<b>486</b>	439	485	437	487							
510.parest_r	224	<b>2150</b>	<b>273</b>	2146	273	2152	272							
511.povray_r	224	739	707	748	699	<b>740</b>	<b>707</b>							
519.lbm_r	224	<b>727</b>	<b>325</b>	731	323	724	326							
521.wrf_r	224	1029	488	<b>1029</b>	<b>488</b>	1023	491							
526.blender_r	224	518	659	523	653	<b>522</b>	<b>653</b>							
527.cam4_r	224	585	669	<b>587</b>	<b>667</b>	588	666							
538.imagick_r	224	291	1910	296	1880	<b>292</b>	<b>1910</b>							
544.nab_r	224	329	1150	329	1150	<b>329</b>	<b>1150</b>							
549.fotonik3d_r	224	2499	349	2494	350	<b>2496</b>	<b>350</b>							
554.roms_r	224	1656	215	<b>1654</b>	<b>215</b>	1649	216							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:

(Continued on next page)



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### General Notes (Continued)

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

### Platform Notes

BIOS configuration:  
Power Policy Set to Performance  
SNC Set to Enabled  
IMC Interleaving set to 1-way interleave  
XPT Prefetch set to Enabled

Sysinfo program /spec2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Fri Mar 11 11:33:19 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8276L CPU @ 2.20GHZ
 4 "physical id"s (chips)
 224 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 56
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
```

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### Platform Notes (Continued)

From lscpu from util-linux 2.32.1:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 224
On-line CPU(s) list:   0-223
Thread(s) per core:    2
Core(s) per socket:    28
Socket(s):              4
NUMA node(s):          8
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Platinum 8276L CPU @ 2.20GHz
BIOS Model name:       Intel(R) Xeon(R) Platinum 8276L CPU @ 2.20GHz
Stepping:               7
CPU MHz:                2999.974
CPU max MHz:            4000.0000
CPU min MHz:            1000.0000
BogoMIPS:               4400.00
Virtualization:         VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               39424K
NUMA node0 CPU(s):     0-3,7-9,14-17,21-23,112-115,119-121,126-129,133-135
NUMA node1 CPU(s):     4-6,10-13,18-20,24-27,116-118,122-125,130-132,136-139
NUMA node2 CPU(s):     28-31,35-37,42-45,49-51,140-143,147-149,154-157,161-163
NUMA node3 CPU(s):     32-34,38-41,46-48,52-55,144-146,150-153,158-160,164-167
NUMA node4 CPU(s):     56-59,63-65,70-73,77-79,168-171,175-177,182-185,189-191
NUMA node5 CPU(s):     60-62,66-69,74-76,80-83,172-174,178-181,186-188,192-195
NUMA node6 CPU(s):     84-87,91-93,98-101,105-107,196-199,203-205,210-213,217-219
NUMA node7 CPU(s):     88-90,94-97,102-104,108-111,200-202,206-209,214-216,220-223
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_lld arch_capabilities

```

/proc/cpuinfo cache data

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### Platform Notes (Continued)

cache size : 39424 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0 1 2 3 7 8 9 14 15 16 17 21 22 23 112 113 114 115 119 120 121 126 127 128 129 133 134 135

node 0 size: 94938 MB

node 0 free: 81849 MB

node 1 cpus: 4 5 6 10 11 12 13 18 19 20 24 25 26 27 116 117 118 122 123 124 125 130 131 132 136 137 138 139

node 1 size: 96761 MB

node 1 free: 91215 MB

node 2 cpus: 28 29 30 31 35 36 37 42 43 44 45 49 50 51 140 141 142 143 147 148 149 154 155 156 157 161 162 163

node 2 size: 96761 MB

node 2 free: 91182 MB

node 3 cpus: 32 33 34 38 39 40 41 46 47 48 52 53 54 55 144 145 146 150 151 152 153 158 159 160 164 165 166 167

node 3 size: 96761 MB

node 3 free: 91216 MB

node 4 cpus: 56 57 58 59 63 64 65 70 71 72 73 77 78 79 168 169 170 171 175 176 177 182 183 184 185 189 190 191

node 4 size: 96761 MB

node 4 free: 89592 MB

node 5 cpus: 60 61 62 66 67 68 69 74 75 76 80 81 82 83 172 173 174 178 179 180 181 186 187 188 192 193 194 195

node 5 size: 96761 MB

node 5 free: 91077 MB

node 6 cpus: 84 85 86 87 91 92 93 98 99 100 101 105 106 107 196 197 198 199 203 204 205 210 211 212 213 217 218 219

node 6 size: 96724 MB

node 6 free: 91186 MB

node 7 cpus: 88 89 90 94 95 96 97 102 103 104 108 109 110 111 200 201 202 206 207 208 209 214 215 216 220 221 222 223

node 7 size: 96759 MB

node 7 free: 91212 MB

node distances:

node	0	1	2	3	4	5	6	7
0:	10	11	21	21	21	21	21	21
1:	11	10	21	21	21	21	21	21
2:	21	21	10	11	21	21	21	21
3:	21	21	11	10	21	21	21	21
4:	21	21	21	21	10	11	21	21
5:	21	21	21	21	11	10	21	21
6:	21	21	21	21	21	21	10	11
7:	21	21	21	21	21	21	11	10

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### Platform Notes (Continued)

From /proc/meminfo

MemTotal: 790765908 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/sbin/tuned-adm active

Current active profile: throughput-performance

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux"  
VERSION="8.4 (Ootpa)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="8.4"  
PLATFORM\_ID="platform:el8"  
PRETTY\_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"  
ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.4:ga

uname -a:

Linux localhost.localdomain 4.18.0-305.el8.x86\_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021 x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

KVM: Mitigation: Split huge pages

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):

Not affected

CVE-2019-11135 (TSX Asynchronous Abort):

Mitigation: TSX disabled

(Continued on next page)



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### Platform Notes (Continued)

run-level 3 Mar 11 07:02

SPEC is set to: /spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xf	859G	132G	728G	16%	/

From /sys/devices/virtual/dmi/id

Vendor: xFusion  
 Product: 5885H V5  
 Product Family: Purley  
 Serial: 1234567

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

48x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

BIOS:

BIOS Vendor: INSYDE Corp.  
 BIOS Version: 8.23  
 BIOS Date: 12/30/2021  
 BIOS Revision: 8.23

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----
```

```
=====
C++ | 508.namd_r(base) 510.parest_r(base)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----
=====
```

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### Compiler Version Notes (Continued)

C++, C | 511.povray\_r(base) 526.blender\_r(base)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
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Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

-----  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)

-----  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifort

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

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## Base Optimization Flags (Continued)

### C++ benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div  
-qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles  
-mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles  
-mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)  
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CSL-V1.1.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)  
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CSL-V1.1.xml>



# SPEC CPU®2017 Floating Point Rate Result

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**xFusion**

SPECrate®2017\_fp\_base = 585

xFusion 5885H V5 (Intel Xeon Platinum 8276L)

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Mar-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** Jun-2021

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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