SPEC CPU®2017 Integer Speed Result

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Inspur Corporation
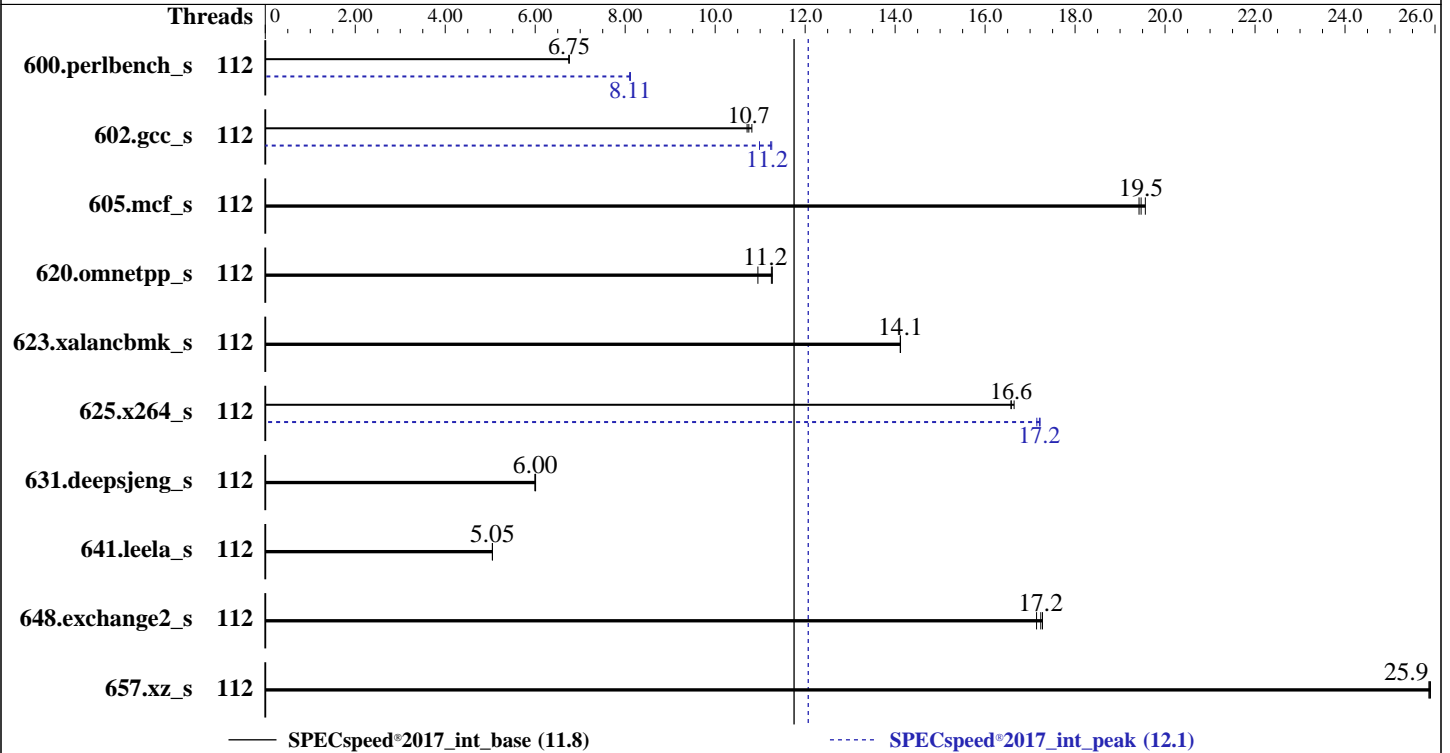
Inspur NF8260M5 (Intel Xeon Platinum 8276L)

SPECspeed®2017_int_base = 11.8

SPECspeed®2017_int_peak = 12.1

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Apr-2022
Hardware Availability: Apr-2019
Software Availability: Dec-2020

Threads

Benchmark	Threads	base	peak
600.perlbench_s	112	6.75	8.11
602.gcc_s	112	10.7	11.2
605.mcf_s	112	19.5	
620.omnetpp_s	112	11.2	
623.xalancbmk_s	112	14.1	
625.x264_s	112	16.6	17.2
631.deepsjeng_s	112	6.00	
641.leela_s	112	5.05	
648.exchange2_s	112	17.2	
657.xz_s	112	25.9	

——— SPECspeed®2017_int_base (11.8)   ------ SPECspeed®2017_int_peak (12.1)

Hardware

CPU Name: Intel Xeon Platinum 8276L
 Max MHz: 4000
 Nominal: 2200
Enabled: 112 cores, 4 chips
Orderable: 2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 38.5 MB I+D on chip per chip
 Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933V-R)
Storage: 1 x 600 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa)
4.18.0-240.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
C/C++: Version 2021.1 of Intel C/C++
Compiler Classic Build 20201112 for Linux;
Fortran: Version 2021.1 of Intel Fortran
Compiler Classic Build 20201112 for Linux
Parallel: Yes
Firmware: Version 4.1.16 released Aug-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost
of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	112	263	6.75	**263**	**6.75**	263	6.76	112	219	8.10	219	8.11	**219**	**8.11**
602.gcc_s	112	372	10.7	**370**	**10.7**	368	10.8	112	354	11.3	**354**	**11.2**	362	11.0
605.mcf_s	112	241	19.6	**243**	**19.5**	243	19.4	112	241	19.6	**243**	**19.5**	243	19.4
620.omnetpp_s	112	**145**	**11.2**	145	11.3	149	10.9	112	**145**	**11.2**	145	11.3	149	10.9
623.xalancbmk_s	112	100	14.1	**100**	**14.1**	100	14.1	112	100	14.1	**100**	**14.1**	100	14.1
625.x264_s	112	**106**	**16.6**	106	16.6	106	16.6	112	**103**	**17.2**	102	17.2	103	17.1
631.deepsjeng_s	112	**239**	**6.00**	239	5.99	238	6.01	112	**239**	**6.00**	239	5.99	238	6.01
641.leela_s	112	338	5.05	338	5.05	**338**	**5.05**	112	338	5.05	338	5.05	**338**	**5.05**
648.exchange2_s	112	170	17.3	**171**	**17.2**	171	17.1	112	170	17.3	**171**	**17.2**	171	17.1
657.xz_s	112	239	25.9	239	25.9	**239**	**25.9**	112	239	25.9	239	25.9	**239**	**25.9**
SPECspeed®2017_int_base =			**11.8**											
SPECspeed®2017_int_peak =			**12.1**											

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

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General Notes (Continued)

```
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or
https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS and OS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Sub NUMA Cluster (SNC) set to enable
Scaling_Governor set to Performance
Hyper Threading set to disabled


Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Wed Apr 13 10:09:09 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo


From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8276L CPU @ 2.20GHz
      4  "physical id"s (chips)
      112 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 28
      siblings  : 28
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
      physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
      physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30

From lscpu from util-linux 2.32.1:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      CPU(s):              112
```

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Platform Notes (Continued)

```
        On-line CPU(s) list: 0-111
        Thread(s) per core:  1
        Core(s) per socket:  28
        Socket(s):           4
        NUMA node(s):        4
        Vendor ID:           GenuineIntel
        CPU family:          6
        Model:               85
        Model name:          Intel(R) Xeon(R) Platinum 8276L CPU @ 2.20GHz
        Stepping:            7
        CPU MHz:             1000.518
        CPU max MHz:         4000.0000
        CPU min MHz:         1000.0000
        BogoMIPS:            4400.00
        Virtualization:      VT-x
        L1d cache:           32K
        L1i cache:           32K
        L2 cache:            1024K
        L3 cache:            39424K
        NUMA node0 CPU(s):   0-27
        NUMA node1 CPU(s):   28-55
        NUMA node2 CPU(s):   56-83
        NUMA node3 CPU(s):   84-111
        Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
    avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
    invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust
    bmi1 hle avx2 smep bmi2 erms invpcid cqm mpx rdt_a avx512f avx512dq rdseed adx smap
    clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
    cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts hwp
    hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d
    arch_capabilities

 /proc/cpuinfo cache data
    cache size : 39424 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
   node 0 size: 369804 MB
   node 0 free: 383043 MB
   node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
   53 54 55
```

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$SPECspeed^®2017\_int\_base = 11.8$

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$SPECspeed^®2017\_int\_peak = 12.1$

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Platform Notes (Continued)

```
   node 1 size: 371369 MB
   node 1 free: 386409 MB
   node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80
   81 82 83
   node 2 size: 372077 MB
   node 2 free: 386727 MB
   node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105
   106 107 108 109 110 111
   node 3 size: 373030 MB
   node 3 free: 386858 MB
   node distances:
   node   0   1   2   3
     0:  10  21  21  21
     1:  21  10  21  21
     2:  21  21  10  21
     3:  21  21  21  10

 From /proc/meminfo
    MemTotal:       1583918228 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sbin/tuned-adm active
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.3 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.3"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:
```

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Platform Notes (Continued)

```
CVE-2018-12207 (iTLB Multihit):              KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault):           Not affected
Microarchitectural Data Sampling:            Not affected
CVE-2017-5754 (Meltdown):                    Not affected
CVE-2018-3639 (Speculative Store Bypass):    Mitigation: Speculative Store
                                             Bypass disabled via prctl and
                                             seccomp
CVE-2017-5753 (Spectre variant 1):           Mitigation: usercopy/swapgs
                                             barriers and __user pointer
                                             sanitization
CVE-2017-5715 (Spectre variant 2):           Mitigation: Enhanced IBRS, IBPB:
                                             conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):     Mitigation: TSX disabled


run-level 3 Apr 12 10:17


SPEC is set to: /home/CPU2017
    Filesystem            Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   484G  144G  340G  30% /home


From /sys/devices/virtual/dmi/id
     Vendor:         Inspur
     Product:        NF8260M5
     Serial:         221140642


Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     48x Micron 18ASF4G72PDZ-2G9E1 32 GB 2 rank 2933


BIOS:
    BIOS Vendor:       American Megatrends Inc.
    BIOS Version:      4.1.16
    BIOS Date:         08/25/2021
    BIOS Revision:     5.14


(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C          | 600.perlbench_s(peak)
```

(Continued on next page)

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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
           | 625.x264_s(base, peak) 657.xz_s(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C          | 600.perlbench_s(peak)
--------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
           | 625.x264_s(base, peak) 657.xz_s(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C++        | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
           | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------


==========================================================================
Fortran    | 648.exchange2_s(base, peak)
--------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
```

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Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifort`

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:
```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
```
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icx
```

600.perlbench_s: `icc`

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

600.perlbench_s: `-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2)
`-xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4 -fno-strict-overflow`
`-mbranches-within-32B-boundaries`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

602.gcc_s: `-m64 -std=c11 -Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX512 -flto`
`-Ofast`(pass 1) `-O3 -ffast-math -qopt-mem-layout-trans=4`
`-mbranches-within-32B-boundaries`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

605.mcf_s: basepeak = yes

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Peak Optimization Flags (Continued)

625.x264_s: `-DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs`
`-xCORE-AVX512 -flto -O3 -ffast-math`
`-qopt-mem-layout-trans=4 -fno-alias`
`-mbranches-within-32B-boundaries`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

657.xz_s: `basepeak = yes`

C++ benchmarks:

620.omnetpp_s: `basepeak = yes`

623.xalancbmk_s: `basepeak = yes`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html`
`http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml`
`http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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