SPEC CPU®2017 Floating Point Speed Result

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Cisco Systems

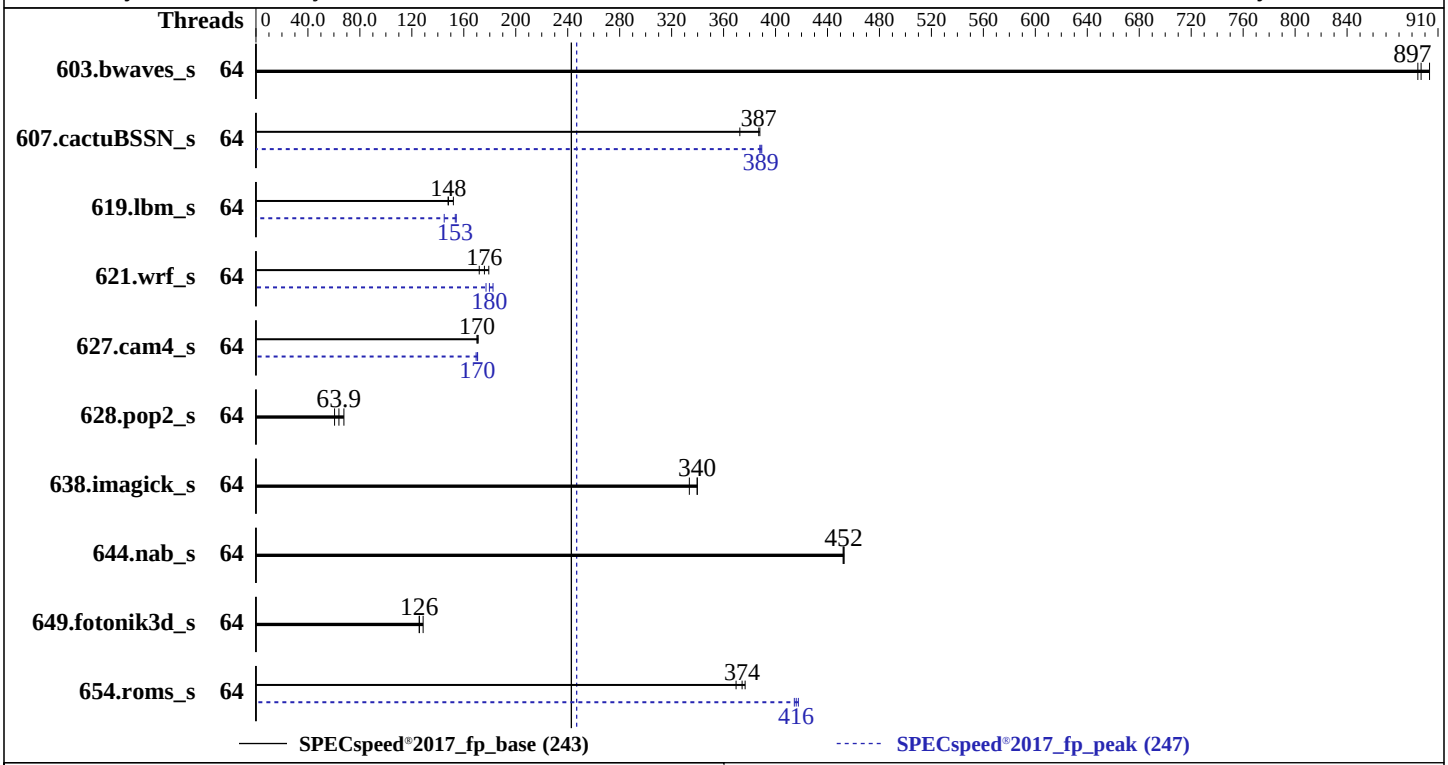
Cisco UCS C245 M6 (AMD EPYC 7573X)

SPECspeed®2017_fp_base = 243

SPECspeed®2017_fp_peak = 247

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Apr-2022
Hardware Availability: Mar-2022
Software Availability: Dec-2021



Hardware

CPU Name:	AMD EPYC 7573X
Max MHz:	3600
Nominal:	2800
Enabled:	64 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	768 MB I+D on chip per chip, 96 MB shared / 4 cores
Other:	None
Memory:	2 TB (16 x 128 GB 4Rx4 PC4-3200V-L)
Storage:	1 x 960 GB M.2 SSD SATA
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP2 (x86_64) kernel version 5.3.18-22-default
Compiler:	C/C++/Fortran: Version 3.2.0 of AOCC
Parallel:	Yes
Firmware:	Version 4.2.1.30 released Feb-2022
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc: jemalloc memory allocator library v5.1.0
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	65.3	903	**65.8**	**897**	66.0	894	64	65.3	903	**65.8**	**897**	66.0	894
607.cactuBSSN_s	64	43.0	388	44.7	373	**43.1**	**387**	64	42.8	389	43.0	388	**42.9**	**389**
619.lbm_s	64	**35.4**	**148**	35.4	148	34.5	152	64	**34.1**	**153**	36.1	145	34.0	154
621.wrf_s	64	73.8	179	**75.2**	**176**	76.9	172	64	74.7	177	**73.6**	**180**	72.5	183
627.cam4_s	64	51.8	171	52.1	170	**52.0**	**170**	64	**52.0**	**170**	52.3	169	51.9	171
628.pop2_s	64	**186**	**63.9**	175	67.7	196	60.5	64	**186**	**63.9**	175	67.7	196	60.5
638.imagick_s	64	42.5	340	**42.5**	**340**	43.2	334	64	42.5	340	**42.5**	**340**	43.2	334
644.nab_s	64	38.6	453	**38.6**	**452**	38.7	452	64	38.6	453	**38.6**	**452**	38.7	452
649.fotonik3d_s	64	70.8	129	**72.5**	**126**	72.5	126	64	70.8	129	**72.5**	**126**	72.5	126
654.roms_s	64	**42.1**	**374**	41.8	377	42.6	370	64	37.7	418	38.0	414	**37.9**	**416**

SPECspeed®2017_fp_base = 243

SPECspeed®2017_fp_peak = 247

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
To enable THP only on request for peak runs of 628.pop2_s:
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To disable THP for peak runs of 627.cam4_s, 649.fotonik3d_s, and 654.roms_s,
'echo never > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-63"
LD_LIBRARY_PATH =
     "/home/cpu2017/amd_speed_aocc320_milanx_A_lib/lib;/home/cpu2017/amd_spee
     d_aocc320_milanx_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "64"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:
GOMP_CPU_AFFINITY = "0-63"

Environment variables set by runcpu during the 619.lbm_s peak run:
GOMP_CPU_AFFINITY = "0-63"

Environment variables set by runcpu during the 621.wrf_s peak run:
GOMP_CPU_AFFINITY = "0-63"

Environment variables set by runcpu during the 627.cam4_s peak run:
GOMP_CPU_AFFINITY = "0-63"

Environment variables set by runcpu during the 654.roms_s peak run:
GOMP_CPU_AFFINITY = "0-63"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
```

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General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2
```

Platform Notes

```
BIOS Configuration
 SMT Mode set to Disabled
 NUMA nodes per socket set to NPS1
 ACPI SRAT L3 Cache As NUMA Domain set to Enabled
 DRAM Scrub Time set to Disabled
 Determinism Slider set to Power
 L1 Stream HW Prefetcher set to Enabled
 APBDIS set to 1

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on SPEC-SRV Mon Apr 18 22:01:44 2022

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7573X 32-Core Processor
       2  "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
       siblings  : 32
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31

 From lscpu from util-linux 2.33.1:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      Address sizes:       48 bits physical, 48 bits virtual
      CPU(s):              64
```

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Platform Notes (Continued)

```
On-line CPU(s) list: 0-63
Thread(s) per core:  1
Core(s) per socket:  32
Socket(s):           2
NUMA node(s):        16
Vendor ID:           AuthenticAMD
CPU family:          25
Model:               1
Model name:          AMD EPYC 7573X 32-Core Processor
Stepping:            2
CPU MHz:             1757.523
CPU max MHz:         2800.0000
CPU min MHz:         1500.0000
BogoMIPS:            5589.57
Virtualization:      AMD-V
L1d cache:           32K
L1i cache:           32K
L2 cache:            512K
L3 cache:            98304K
NUMA node0 CPU(s):   0-3
NUMA node1 CPU(s):   4-7
NUMA node2 CPU(s):   8-11
NUMA node3 CPU(s):   12-15
NUMA node4 CPU(s):   16-19
NUMA node5 CPU(s):   20-23
NUMA node6 CPU(s):   24-27
NUMA node7 CPU(s):   28-31
NUMA node8 CPU(s):   32-35
NUMA node9 CPU(s):   36-39
NUMA node10 CPU(s):  40-43
NUMA node11 CPU(s):  44-47
NUMA node12 CPU(s):  48-51
NUMA node13 CPU(s):  52-55
NUMA node14 CPU(s):  56-59
NUMA node15 CPU(s):  60-63
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand
lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase
bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_vmsave_vmload vgif
umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca
```

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Platform Notes (Continued)

```
 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 16 nodes (0-15)
   node 0 cpus: 0 1 2 3
   node 0 size: 128835 MB
   node 0 free: 128713 MB
   node 1 cpus: 4 5 6 7
   node 1 size: 129022 MB
   node 1 free: 128857 MB
   node 2 cpus: 8 9 10 11
   node 2 size: 128988 MB
   node 2 free: 128887 MB
   node 3 cpus: 12 13 14 15
   node 3 size: 129022 MB
   node 3 free: 128950 MB
   node 4 cpus: 16 17 18 19
   node 4 size: 129022 MB
   node 4 free: 128945 MB
   node 5 cpus: 20 21 22 23
   node 5 size: 129022 MB
   node 5 free: 128889 MB
   node 6 cpus: 24 25 26 27
   node 6 size: 129022 MB
   node 6 free: 128939 MB
   node 7 cpus: 28 29 30 31
   node 7 size: 116909 MB
   node 7 free: 116795 MB
   node 8 cpus: 32 33 34 35
   node 8 size: 129022 MB
   node 8 free: 128961 MB
   node 9 cpus: 36 37 38 39
   node 9 size: 129022 MB
   node 9 free: 128958 MB
   node 10 cpus: 40 41 42 43
   node 10 size: 129022 MB
   node 10 free: 128966 MB
   node 11 cpus: 44 45 46 47
   node 11 size: 129022 MB
   node 11 free: 128965 MB
   node 12 cpus: 48 49 50 51
   node 12 size: 129022 MB
   node 12 free: 128967 MB
   node 13 cpus: 52 53 54 55
```

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Platform Notes (Continued)

```
   node 13 size: 129022 MB
   node 13 free: 128961 MB
   node 14 cpus: 56 57 58 59
   node 14 size: 129022 MB
   node 14 free: 128962 MB
   node 15 cpus: 60 61 62 63
   node 15 size: 129017 MB
   node 15 free: 128964 MB
   node distances:
   node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
     0:  10  11  11  11  11  11  11  11  32  32  32  32  32  32  32  32
     1:  11  10  11  11  11  11  11  11  32  32  32  32  32  32  32  32
     2:  11  11  10  11  11  11  11  11  32  32  32  32  32  32  32  32
     3:  11  11  11  10  11  11  11  11  32  32  32  32  32  32  32  32
     4:  11  11  11  11  10  11  11  11  32  32  32  32  32  32  32  32
     5:  11  11  11  11  11  10  11  11  32  32  32  32  32  32  32  32
     6:  11  11  11  11  11  11  10  11  32  32  32  32  32  32  32  32
     7:  11  11  11  11  11  11  11  10  32  32  32  32  32  32  32  32
     8:  32  32  32  32  32  32  32  32  10  11  11  11  11  11  11  11
     9:  32  32  32  32  32  32  32  32  11  10  11  11  11  11  11  11
    10:  32  32  32  32  32  32  32  32  11  11  10  11  11  11  11  11
    11:  32  32  32  32  32  32  32  32  11  11  11  10  11  11  11  11
    12:  32  32  32  32  32  32  32  32  11  11  11  11  10  11  11  11
    13:  32  32  32  32  32  32  32  32  11  11  11  11  11  10  11  11
    14:  32  32  32  32  32  32  32  32  11  11  11  11  11  11  10  11
    15:  32  32  32  32  32  32  32  32  11  11  11  11  11  11  11  10

 From /proc/meminfo
    MemTotal:       2101272720 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="SLES"
       VERSION="15-SP2"
       VERSION_ID="15.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
       ID="sles"
       ID_LIKE="suse"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:15:sp2"

 uname -a:
```

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Platform Notes (Continued)

```
    Linux SPEC-SRV 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
    x86_64 x86_64 GNU/Linux

  Kernel self-reported vulnerability status:

  CVE-2018-12207 (iTLB Multihit):                        Not affected
  CVE-2018-3620 (L1 Terminal Fault):                     Not affected
  Microarchitectural Data Sampling:                      Not affected
  CVE-2017-5754 (Meltdown):                              Not affected
  CVE-2018-3639 (Speculative Store Bypass):              Mitigation: Speculative Store
                                                         Bypass disabled via prctl and
                                                         seccomp
  CVE-2017-5753 (Spectre variant 1):                     Mitigation: usercopy/swapgs
                                                         barriers and __user pointer
                                                         sanitization
  CVE-2017-5715 (Spectre variant 2):                     Mitigation: Full AMD retpoline,
                                                        IBPB: conditional, IBRS_FW, STIBP:
                                                         disabled, RSB filling
  CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
  CVE-2019-11135 (TSX Asynchronous Abort):               Not affected

  run-level 3 Apr 18 16:50

  SPEC is set to: /home/cpu2017
     Filesystem     Type   Size  Used Avail Use% Mounted on
     /dev/sda2      btrfs  222G   35G  187G  16% /home

  From /sys/devices/virtual/dmi/id
      Vendor:         Cisco Systems Inc
      Product:        UCSC-C245-M6SX
      Serial:         WZP25130VQH

  Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
  interpret this section. The 'dmidecode' program reads system data which is "intended to
  allow hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    Memory:
      16x 0xCE00 M386AAG40AM3-CWE 128 GB 4 rank 3200
      16x Unknown Unknown

  BIOS:
     BIOS Vendor:       Cisco Systems, Inc.
     BIOS Version:      C245M6.4.2.1.30.0221222139
     BIOS Date:         02/21/2022
     BIOS Revision:     5.22

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
                | 644.nab_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
                | 654.roms_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                | 628.pop2_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
```

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Compiler Version Notes (Continued)

```
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

Fortran benchmarks:
`flang`

Benchmarks using both Fortran and C:
`flang clang`

Benchmarks using Fortran, C, and C++:
`clang++ clang flang`

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`
607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64`
627.cam4_s: `-DSPEC_CASE_FLAG -DSPEC_LP64`
628.pop2_s: `-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`
649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:
-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument -Wno-return-type

Peak Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

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Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

654.roms_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc

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Peak Optimization Flags (Continued)

654.roms_s (continued):
-lflang

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Hz,1,0x1 -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

627.cam4_s: Same as 621.wrf_s

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -Mrecursive
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP -fopenmp=libomp
-lomp -lamdlibm -ljemalloc -lflang

Peak Other Flags

C benchmarks:
`-Wno-unused-command-line-argument -Wno-return-type`

Fortran benchmarks:
`-Wno-unused-command-line-argument -Wno-return-type`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument -Wno-return-type`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument -Wno-return-type`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html`
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v2-revD.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml`
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v2-revD.xml`

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