



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

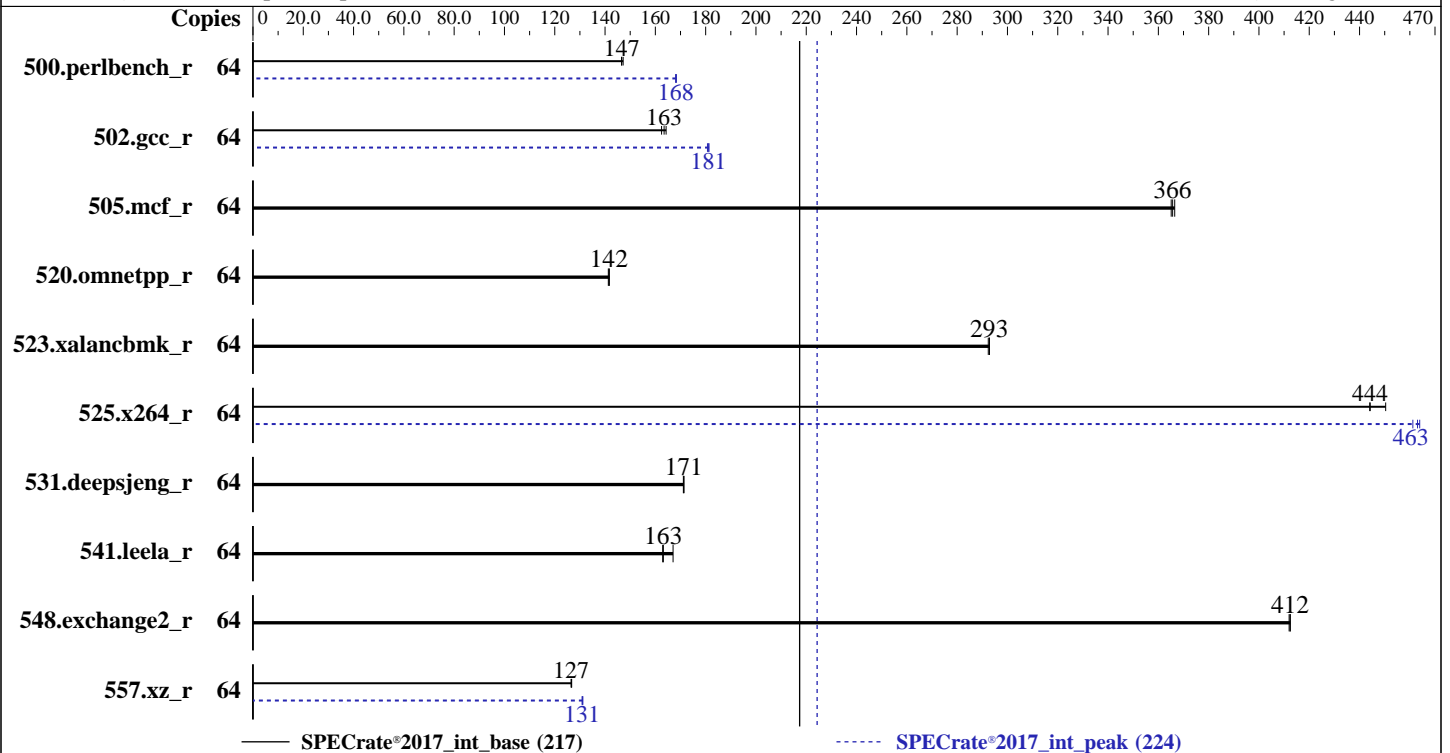
Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020



Hardware

CPU Name: Intel Xeon Gold 6226R
 Max MHz: 3900
 Nominal: 2900
 Enabled: 32 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 22 MB I+D on chip per chip
 Other: None
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
 Storage: 2 x 3.7 TB NVME SSD
 Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
 Compiler: C/C++: Version 19.1.2.275 of Intel C/C++ Compiler Build 20200604 for Linux; Fortran: Version 19.1.2.275 of Intel Fortran Compiler Build 20200623 for Linux;
 Parallel: No
 Firmware: Version 3.1.5 released May-2020
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	692	147	695	147	695	147	64	605	168	606	168	606	168
502.gcc_r	64	558	162	552	164	554	163	64	501	181	500	181	500	181
505.mcf_r	64	282	367	283	365	283	366	64	282	367	283	365	283	366
520.omnetpp_r	64	593	142	593	142	594	141	64	593	142	593	142	594	141
523.xalancbmk_r	64	231	293	231	292	231	293	64	231	293	231	292	231	293
525.x264_r	64	252	444	249	450	252	444	64	242	463	242	464	243	461
531.deepsjeng_r	64	428	171	428	171	429	171	64	428	171	428	171	429	171
541.leela_r	64	634	167	649	163	651	163	64	634	167	649	163	651	163
548.exchange2_r	64	407	412	407	412	407	412	64	407	412	407	412	407	412
557.xz_r	64	545	127	546	127	546	127	64	528	131	528	131	527	131

SPECrate®2017_int_base = 217

SPECrate®2017_int_peak = 224

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GVERNOR set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

General Notes (Continued)

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

```
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or
https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

BIOS configuration:

ENERGY_PERF_BIAS_CFG mode set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

CLE Support set to Disable

IMC (Integrated memory controller) Interleaving set to 1-way

Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011

running on localhost.localdomain Sat Dec 12 15:18:26 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
```

```
2 "physical id"s (chips)
```

```
64 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 16
```

```
siblings : 32
```

```
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

```
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

From lscpu:

```
Architecture: x86_64
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Dec-2020
Hardware Availability: Feb-2020
Software Availability: Aug-2020

Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              64
On-line CPU(s) list: 0-63
Thread(s) per core:  2
Core(s) per socket: 16
Socket(s):           2
NUMA node(s):        4
Vendor ID:           GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
Stepping:             7
CPU MHz:              1420.470
CPU max MHz:          3900.0000
CPU min MHz:          1200.0000
BogoMIPS:             5800.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:              1024K
L3 cache:              22528K
NUMA node0 CPU(s):   0-3,8-11,32-35,40-43
NUMA node1 CPU(s):   4-7,12-15,36-39,44-47
NUMA node2 CPU(s):   16-19,24-27,48-51,56-59
NUMA node3 CPU(s):   20-23,28-31,52-55,60-63
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

```

```
/proc/cpuinfo cache data
cache size : 22528 KB
```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 8 9 10 11 32 33 34 35 40 41 42 43
node 0 size: 95052 MB

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Platform Notes (Continued)

```

node 0 free: 94559 MB
node 1 cpus: 4 5 6 7 12 13 14 15 36 37 38 39 44 45 46 47
node 1 size: 96764 MB
node 1 free: 96397 MB
node 2 cpus: 16 17 18 19 24 25 26 27 48 49 50 51 56 57 58 59
node 2 size: 96764 MB
node 2 free: 96419 MB
node 3 cpus: 20 21 22 23 28 29 30 31 52 53 54 55 60 61 62 63
node 3 size: 96763 MB
node 3 free: 96304 MB
node distances:
node  0  1  2  3
  0: 10 11 21 21
  1: 11 10 21 21
  2: 21 21 10 11
  3: 21 21 11 10

```

From /proc/meminfo

```

MemTotal:      394592660 kB
HugePages_Total:       0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

```

uname -a:

```

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

itlb_multihit:                               KVM: Vulnerable
CVE-2018-3620 (L1 Terminal Fault):           Not affected
Microarchitectural Data Sampling:           Not affected
CVE-2017-5754 (Meltdown):                   Not affected
CVE-2018-3639 (Speculative Store Bypass):   Mitigation: Speculative Store Bypass disabled

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Dec-2020
Hardware Availability: Feb-2020
Software Availability: Aug-2020

Platform Notes (Continued)

via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
 CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
 tsx_async_abort: Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Dec 12 15:17

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	3.1T	85G	3.0T	3%	/home

From /sys/devices/virtual/dmi/id

BIOS: American Megatrends Inc. 3.1.5 05/12/2020
 Vendor: Inspur
 Product: NF5266M5
 Serial: 220984763

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
 12x Samsung M393A4K40DB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

```
=====
C      | 502.gcc_r(peak)
-----
Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275
Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----
```

```
=====
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
-----
Intel(R) C Compiler for applications running on Intel(R) 64, Version
19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Compiler Version Notes (Continued)

=====
C | 500.perlbench_r(peak) 557.xz_r(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.2.275 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 502.gcc_r(peak)

Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275
Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base, peak) 557.xz_r(base)

Intel(R) C Compiler for applications running on Intel(R) 64, Version
19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(peak) 557.xz_r(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.2.275 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 502.gcc_r(peak)

Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275
Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base, peak) 557.xz_r(base)

Intel(R) C Compiler for applications running on Intel(R) 64, Version

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Compiler Version Notes (Continued)

19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(peak) 557.xz_r(peak)
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.2.275 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
| 53l.deepsjeng_r(base, peak) 54l.leela_r(base, peak)
=====

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version
19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base, peak)
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.2.275 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Base Portability Flags (Continued)

```
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/ia32_lin
-std=gnu89
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

505.mcf_r: basepeak = yes
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Inspur Corporation

SPECrate®2017_int_base = 217

Inspur NF5266M5 (Intel Xeon Gold 6226R)

SPECrate®2017_int_peak = 224

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Dec-2020

Hardware Availability: Feb-2020

Software Availability: Aug-2020

Peak Optimization Flags (Continued)

```
525.x264_r: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc
```

```
557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.html

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.xml

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-12-12 02:18:25-0500.

Report generated on 2021-01-19 16:56:26 by CPU2017 PDF formatter v6255.

Originally published on 2021-01-19.