SPEC® CPU2017 Integer Rate Result

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Fujitsu
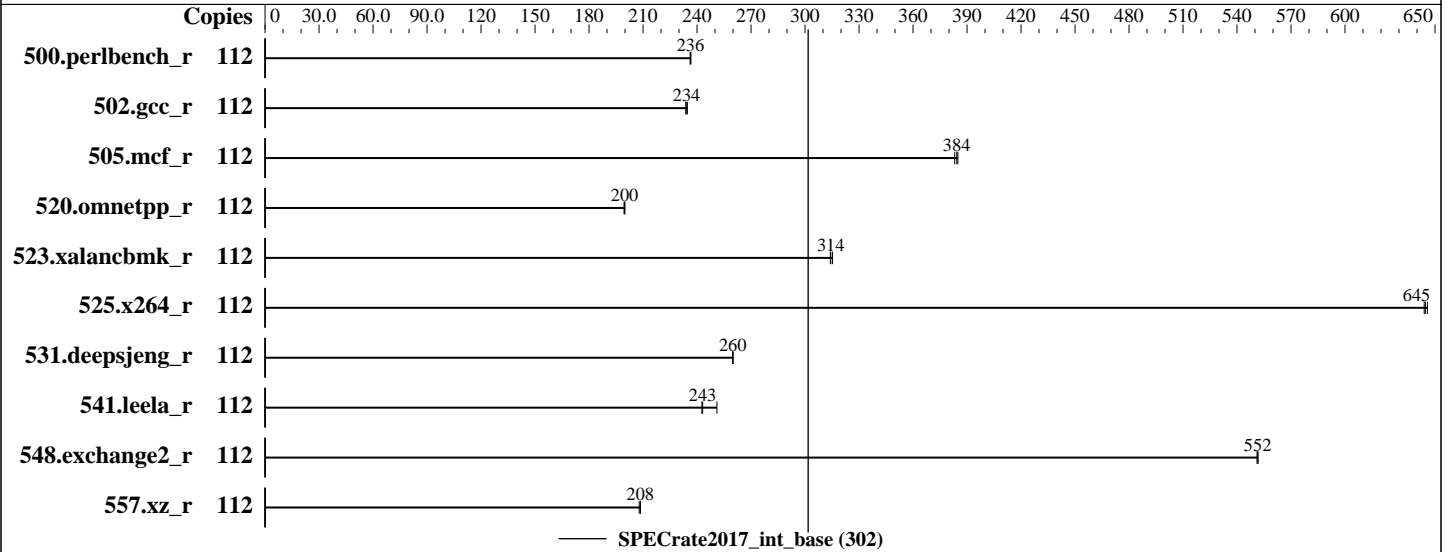
PRIMERGY RX2530 M5, Intel Xeon Platinum 8276, 2.20GHz

SPECrate2017_int_base =   302

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2019
Hardware Availability: May-2019
Software Availability: Feb-2019

Benchmark	Copies	Result
500.perlbench_r	112	236
502.gcc_r	112	234
505.mcf_r	112	384
520.omnetpp_r	112	200
523.xalancbmk_r	112	314
525.x264_r	112	645
531.deepsjeng_r	112	260
541.leela_r	112	243
548.exchange2_r	112	552
557.xz_r	112	208

Scale: 0 30.0 60.0 90.0 120 150 180 210 240 270 300 330 360 390 420 450 480 510 540 570 600 650

—— SPECrate2017_int_base (302)

Hardware

CPU Name: Intel Xeon Platinum 8276
 Max MHz.: 4000
 Nominal: 2200
Enabled: 56 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 38.5 MB I+D on chip per chip
 Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x SATA M.2 SSD, 240GB
Other: None

Software

OS: SUSE Linux Enterprise Server 15
  4.12.14-25.28-default
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++
  Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran
  Compiler Build 20181018 for Linux
Parallel: No
Firmware: Fujitsu BIOS Version V5.0.0.14 R1.8.0 for D3383-B1x. Released Jun-2019 tested as V5.0.0.14 R1.2.0 for D3383-B1x Feb-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	112	754	237	755	236	**754**	**236**							
502.gcc_r	112	676	235	679	234	**677**	**234**							
505.mcf_r	112	**471**	**384**	470	385	472	383							
520.omnetpp_r	112	**736**	**200**	735	200	737	199							
523.xalancbmk_r	112	375	315	377	314	**376**	**314**							
525.x264_r	112	305	644	304	646	**304**	**645**							
531.deepsjeng_r	112	**494**	**260**	494	260	494	260							
541.leela_r	112	739	251	764	243	**763**	**243**							
548.exchange2_r	112	532	552	532	551	**532**	**552**							
557.xz_r	112	582	208	**580**	**208**	580	209							
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-111

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.0.5/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

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Test Date: Mar-2019
Hardware Availability: May-2019
Software Availability: Feb-2019

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

```
BIOS configuration:
Patrol Scrub  = Disabled
DCU Ip Prefetcher = Disabled
DCU Streamer Prefetcher = Disabled
Fan Control = Full
Stale AtoS  = Enable
WR CRC feature Control = Disabled
Sysinfo program /home/Benchmark/speccpu2017-1.0.5/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on RX2530M5 Mon Mar 25 11:55:38 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8276 CPU @ 2.20GHz
      2  "physical id"s (chips)
      112 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 28
      siblings  : 56
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30

From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     CPU(s):              112
     On-line CPU(s) list: 0-111
     Thread(s) per core:  2
     Core(s) per socket:  28
     Socket(s):           2
     NUMA node(s):        4
     Vendor ID:           GenuineIntel
     CPU family:          6
     Model:               85
```

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Platform Notes (Continued)

```
        Model name:             Intel(R) Xeon(R) Platinum 8276 CPU @ 2.20GHz
        Stepping:               5
        CPU MHz:                2200.000
        CPU max MHz:            4000.0000
        CPU min MHz:            1000.0000
        BogoMIPS:               4400.00
        Virtualization:         VT-x
        L1d cache:              32K
        L1i cache:              32K
        L2 cache:               1024K
        L3 cache:               39424K
        NUMA node0 CPU(s):      0-3,7-9,14-17,21-23,56-59,63-65,70-73,77-79
        NUMA node1 CPU(s):      4-6,10-13,18-20,24-27,60-62,66-69,74-76,80-83
        NUMA node2 CPU(s):      28-31,35-37,42-45,49-51,84-87,91-93,98-101,105-107
        NUMA node3 CPU(s):      32-34,38-41,46-48,52-55,88-90,94-97,102-104,108-111
        Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
        pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
        aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
        xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
        avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
        invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
        fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
        avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
        dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke flush_l1d
        arch_capabilities

 /proc/cpuinfo cache data
    cache size : 39424 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 7 8 9 14 15 16 17 21 22 23 56 57 58 59 63 64 65 70 71 72 73 77 78
   79
   node 0 size: 191936 MB
   node 0 free: 191296 MB
   node 1 cpus: 4 5 6 10 11 12 13 18 19 20 24 25 26 27 60 61 62 66 67 68 69 74 75 76 80 81
   82 83
   node 1 size: 193530 MB
   node 1 free: 193235 MB
   node 2 cpus: 28 29 30 31 35 36 37 42 43 44 45 49 50 51 84 85 86 87 91 92 93 98 99 100
   101 105 106 107
   node 2 size: 193530 MB
   node 2 free: 193275 MB
   node 3 cpus: 32 33 34 38 39 40 41 46 47 48 52 53 54 55 88 89 90 94 95 96 97 102 103 104
```

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Platform Notes (Continued)

```
   108 109 110 111
   node 3 size: 193318 MB
   node 3 free: 193055 MB
   node distances:
   node   0   1   2   3
     0:  10  11  21  21
     1:  11  10  21  21
     2:  21  21  10  11
     3:  21  21  11  10

From /proc/meminfo
   MemTotal:       790851788 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15"
      VERSION_ID="15"
      PRETTY_NAME="SUSE Linux Enterprise Server 15"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
   Linux RX2530M5 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):           Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation,
IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Mar 25 11:45

SPEC is set to: /home/Benchmark/speccpu2017-1.0.5
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda5      xfs   191G   62G  130G  33% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

```
   BIOS FUJITSU // American Megatrends Inc. V5.0.0.14 R1.2.0 for D3383-B1x
       02/28/2019
   Memory:
    24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
  CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
       557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
       541.leela_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  FC  548.exchange2_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

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Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4`
`-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
`-lqkmalloc`

C++ benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4`
`-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
`-lqkmalloc`

Fortran benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`
`-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64`
`-lqkmalloc`

The flags files that were used to format this result can be browsed at
  http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html
  http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevA.html

---

You can also download the XML flags sources by saving the following links:
  http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml
  http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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