



# SPEC® CPU2017 Integer Speed Result

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## Fujitsu

PRIMEQUEST 3800B2, Intel Xeon Platinum 8280L,  
2.70GHz

SPECspeed2017\_int\_base = 10.5

SPECspeed2017\_int\_peak = Not Run

CPU2017 License: 19

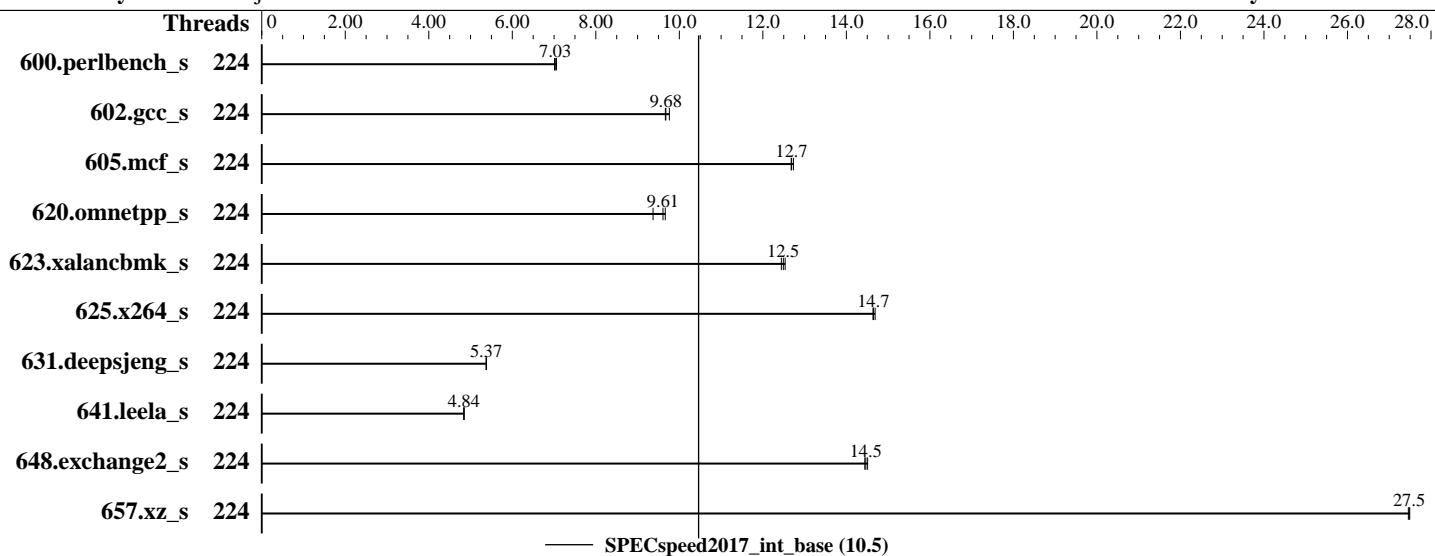
Test Date: Mar-2019

Test Sponsor: Fujitsu

Hardware Availability: Apr-2019

Tested by: Fujitsu

Software Availability: Feb-2019



### Hardware

CPU Name: Intel Xeon Platinum 8280L  
Max MHz.: 4000  
Nominal: 2700  
Enabled: 224 cores, 8 chips  
Orderable: 2,4,6,8 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 38.5 MB I+D on chip per chip  
Other: None  
Memory: 3 TB (96 x 32 GB 2Rx4 PC4-2933Y-R)  
Storage: 1 x SAS HDD, 600GB, 10.5K RPM, SAS HDD  
Other: None

### Software

OS: SUSE Linux Enterprise Server 15  
4.12.14-25.28-default  
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++  
Compiler for Linux;  
Fortran: Version 19.0.1.144 of Intel Fortran  
Compiler for Linux  
Parallel: Yes  
Firmware: Fujitsu BIOS Version V1.0.0.0 R1.21.0 for D3858-B1x. Released Jun-2019 tested as V1.0.0.0 R91.11.0 for D3858-B1x Mar-2019  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc memory allocator V5.0.1



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	224	<b>253</b>	<b>7.03</b>	253	7.01	252	7.06							
602.gcc_s	224	<b>411</b>	<b>9.68</b>	412	9.67	408	9.76							
605.mcf_s	224	371	12.7	<b>372</b>	<b>12.7</b>	372	12.7							
620.omnetpp_s	224	<b>170</b>	<b>9.61</b>	169	9.66	174	9.37							
623.xalancbmk_s	224	113	12.5	<b>113</b>	<b>12.5</b>	114	12.4							
625.x264_s	224	120	14.7	121	14.6	<b>120</b>	<b>14.7</b>							
631.deepsjeng_s	224	266	5.38	<b>267</b>	<b>5.37</b>	267	5.37							
641.leela_s	224	353	4.84	352	4.85	<b>352</b>	<b>4.84</b>							
648.exchange2_s	224	203	14.5	204	14.4	<b>203</b>	<b>14.5</b>							
657.xz_s	224	225	27.5	<b>225</b>	<b>27.5</b>	225	27.5							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Kernel Boot Parameter set with : nohz\_full=1-223

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/Benchmark/speccpu2017/lib/intel64"

LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/Benchmark/speccpu2017/je5.0.1-64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

jemalloc: sources available via jemalloc.net

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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## Platform Notes

BIOS configuration:

Fan Control = Full

Hyper-Threading = Disabled

LLC Deadline Alloc = Disabled

LLC Prefetcher = Enabled

Sub NUMA Clustering = Disabled

UPI Link L0p = Disabled

UPI Link L1 = Disabled

Sysinfo program /home/Benchmark/speccpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on linux-8r5c Sun Mar 31 10:25:56 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8280L CPU @ 2.70GHz
  8 "physical id"s (chips)
  224 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 28
  siblings : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 4: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 5: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 6: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
  physical 7: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Byte Order:           Little Endian
CPU(s):               224
On-line CPU(s) list: 0-223
```

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## Platform Notes (Continued)

Thread(s) per core: 1  
Core(s) per socket: 28  
Socket(s): 8  
NUMA node(s): 8  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Platinum 8280L CPU @ 2.70GHz  
Stepping: 6  
CPU MHz: 2700.000  
CPU max MHz: 4000.0000  
CPU min MHz: 1000.0000  
BogoMIPS: 5400.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 39424K  
NUMA node0 CPU(s): 0-27  
NUMA node1 CPU(s): 28-55  
NUMA node2 CPU(s): 56-83  
NUMA node3 CPU(s): 84-111  
NUMA node4 CPU(s): 112-139  
NUMA node5 CPU(s): 140-167  
NUMA node6 CPU(s): 168-195  
NUMA node7 CPU(s): 196-223  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid aperf fm perf pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb cat\_13 cdp\_13 invpcid\_single intel\_ppin ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpq rdt\_a avx512f avx512dq rdseed adx smap clflushopt clwb intel\_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local dtherm ida arat pln pts hwp hwp\_act\_window hwp\_epp hwp\_pkg\_req pkru ospke avx512\_vnni flush\_lll arch\_capabilities

/proc/cpuinfo cache data  
cache size : 39424 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27  
node 0 size: 385681 MB

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## Platform Notes (Continued)

```
node 0 free: 385146 MB
node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 387059 MB
node 1 free: 386888 MB
node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80
81 82 83
node 2 size: 387059 MB
node 2 free: 386838 MB
node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105
106 107 108 109 110 111
node 3 size: 387059 MB
node 3 free: 386884 MB
node 4 cpus: 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129
130 131 132 133 134 135 136 137 138 139
node 4 size: 387030 MB
node 4 free: 386857 MB
node 5 cpus: 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157
158 159 160 161 162 163 164 165 166 167
node 5 size: 387059 MB
node 5 free: 386884 MB
node 6 cpus: 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185
186 187 188 189 190 191 192 193 194 195
node 6 size: 387059 MB
node 6 free: 386777 MB
node 7 cpus: 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213
214 215 216 217 218 219 220 221 222 223
node 7 size: 386856 MB
node 7 free: 386625 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10  20  20  28  28  28  20  28
  1: 20  10  28  20  28  20  28  28
  2: 20  28  10  20  20  28  28  28
  3: 28  20  20  10  28  28  28  20
  4: 28  28  20  28  10  20  20  28
  5: 28  20  28  28  20  10  28  20
  6: 20  28  28  28  20  28  10  20
  7: 28  28  28  20  28  20  20  10
```

From /proc/meminfo

```
MemTotal:      3169144168 kB
HugePages_Total:        0
Hugepagesize:       2048 kB
```

From /etc/\*release\* /etc/\*version\*

os-release:

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## Platform Notes (Continued)

```
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"
```

```
uname -a:
Linux linux-8r5c 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB
filling
```

```
run-level 3 Mar 31 05:50
```

```
SPEC is set to: /home/Benchmark/speccpu2017
```

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	142G	35G	108G	25%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS FUJITSU V1.0.0.0 R91.11.0 for D3858-B1x 03/15/2019
```

Memory:

```
80x Micron 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933
16x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933
```

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)
657.xz_s(base)
=====
```

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

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```
=====
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
      641.leela_s(base)
=====
```

```
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
=====
```

```
=====
FC 648.exchange2_s(base)
=====
```

```
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
=====
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```



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## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-L/usr/local/jet5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64  
-lqkmalloc
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevA.2019-04-16.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevA.2019-04-16.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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