



# SPEC® CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

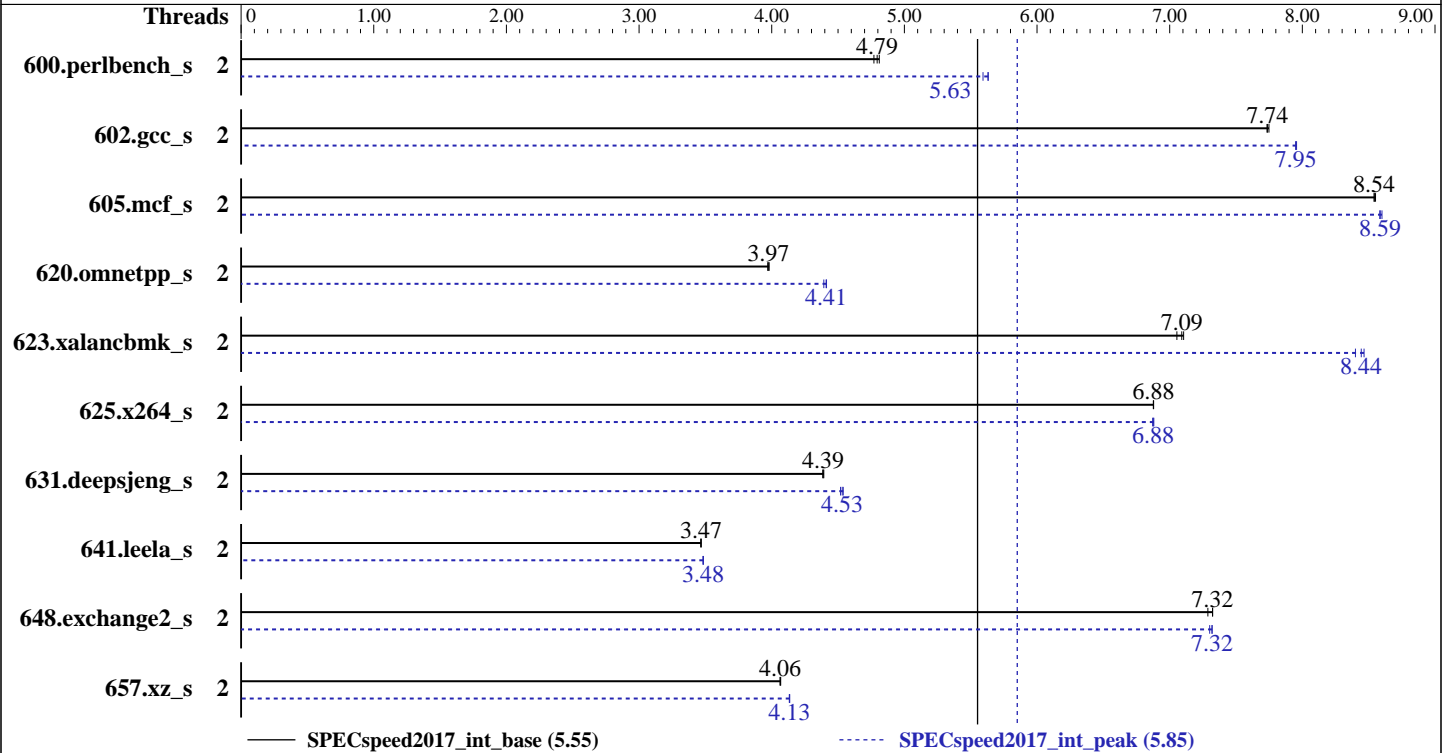
Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Oct-2018



## Hardware

CPU Name: Intel Celeron G4900  
 Max MHz.: 3100  
 Nominal: 3100  
 Enabled: 2 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 256 KB I+D on chip per core  
 L3: 2 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

## Software

OS: SUSE Linux Enterprise Server 12 SP3  
 Kernel 4.4.126-94.22-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 1.0.1 released Oct-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library V5.0.1;



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2019  
Hardware Availability: Dec-2018  
Software Availability: Oct-2018

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	2	372	4.77	<b><u>370</u></b>	<b><u>4.79</u></b>	369	4.81	2	<b><u>315</u></b>	<b><u>5.63</u></b>	317	5.59	315	5.63
602.gcc_s	2	515	7.73	<b><u>515</u></b>	<b><u>7.74</u></b>	514	7.75	2	501	7.95	500	7.96	<b><u>501</u></b>	<b><u>7.95</u></b>
605.mcf_s	2	<b><u>553</u></b>	<b><u>8.54</u></b>	553	8.54	552	8.55	2	550	8.58	549	8.60	<b><u>550</u></b>	<b><u>8.59</u></b>
620.omnetpp_s	2	411	3.97	<b><u>410</u></b>	<b><u>3.97</u></b>	410	3.98	2	<b><u>370</u></b>	<b><u>4.41</u></b>	371	4.39	370	4.41
623.xalancbmk_s	2	199	7.11	<b><u>200</u></b>	<b><u>7.09</u></b>	201	7.05	2	167	8.47	<b><u>168</u></b>	<b><u>8.44</u></b>	169	8.40
625.x264_s	2	256	6.88	<b><u>256</u></b>	<b><u>6.88</u></b>	257	6.88	2	256	6.88	<b><u>257</u></b>	<b><u>6.88</u></b>	257	6.87
631.deepsjeng_s	2	327	4.38	<b><u>327</u></b>	<b><u>4.39</u></b>	326	4.39	2	<b><u>316</u></b>	<b><u>4.53</u></b>	317	4.52	316	4.54
641.leela_s	2	492	3.47	<b><u>492</u></b>	<b><u>3.47</u></b>	492	3.47	2	490	3.48	<b><u>490</u></b>	<b><u>3.48</u></b>	490	3.48
648.exchange2_s	2	403	7.29	401	7.32	<b><u>401</u></b>	<b><u>7.32</u></b>	2	<b><u>402</u></b>	<b><u>7.32</u></b>	403	7.30	402	7.32
657.xz_s	2	1521	4.06	<b><u>1521</u></b>	<b><u>4.06</u></b>	1520	4.07	2	1495	4.14	<b><u>1496</u></b>	<b><u>4.13</u></b>	1496	4.13

SPECspeed2017\_int\_base = 5.55

SPECspeed2017\_int\_peak = 5.85

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets;

jemalloc: sources available via jemalloc.net



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2019

Hardware Availability: Dec-2018

Software Availability: Oct-2018

## Platform Notes

BIOS settings:

```

Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-gw0u Wed Mar 27 05:49:41 2019

```

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Celeron(R) G4900 CPU @ 3.10GHz
 1 "physical id"s (chips)
 2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 2
  siblings  : 2
  physical 0: cores 0 1

```

From lscpu:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                 2
On-line CPU(s) list:   0,1
Thread(s) per core:    1
Core(s) per socket:    2
Socket(s):              1
NUMA node(s):          1
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  158
Model name:             Intel(R) Celeron(R) G4900 CPU @ 3.10GHz
Stepping:               11
CPU MHz:                3100.000
CPU max MHz:           3100.0000
CPU min MHz:           800.0000

```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Oct-2018

## Platform Notes (Continued)

```

BogoMIPS:          6191.99
Virtualization:    VT-x
L1d cache:         32K
L1i cache:         32K
L2 cache:          256K
L3 cache:          2048K
NUMA node0 CPU(s): 0,1
Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg cx16
xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave rdrand
lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm hwp hwp_act_window
hwp_epp intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust smep erms invpcid mpx rdseed smap
clflushopt xsaveopt xsavec xgetbv1

```

```

/proc/cpuinfo cache data
cache size : 2048 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1
node 0 size: 64278 MB
node 0 free: 62812 MB
node distances:
node    0
0:     10

```

```

From /proc/meminfo
MemTotal:      65821136 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"

```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2019

Hardware Availability: Dec-2018

Software Availability: Oct-2018

## Platform Notes (Continued)

```

VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

```

```

uname -a:
Linux linux-gw0u 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

```

run-level 3 Mar 27 05:49 last=5

```

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 301G 17G 285G 6% /

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.1 10/19/2018

Memory:

```

3x 00AD00000A02 HMA82GU7CJR8N-VK 16 GB 2 rank 2666, configured at 2400
1x 00AD00000A06 HMA82GU7CJR8N-VK 16 GB 2 rank 2666, configured at 2400

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base,
    peak) 657.xz_s(base)
=====
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
=====
CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
=====

```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Oct-2018

## Compiler Version Notes (Continued)

```
-----
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CXXC 620.omnetpp_s(base) 623.xalanbmk_s(base) 631.deepsjeng_s(base)
      641.leela_s(base)
-----
```

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CXXC 620.omnetpp_s(peak) 623.xalanbmk_s(peak) 631.deepsjeng_s(peak)
      641.leela_s(peak)
-----
```

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
FC 648.exchange2_s(base, peak)
-----
```

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

602.gcc\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Oct-2018

## Base Portability Flags (Continued)

```
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

## Peak Compiler Invocation

C benchmarks:

```
icc
```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2019

Hardware Availability: Dec-2018

Software Availability: Oct-2018

## Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xSSE4.2 -qopt-prefetch -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -w1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xSSE4.2 -qopt-prefetch -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -w1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

(Continued on next page)





# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

CPU2017 License: 55

Test Date: Mar-2019

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Oct-2018

## Peak Optimization Flags (Continued)

625.x264\_s: -Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div  
-qopt-prefetch -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC\_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz\_s: Same as 602.gcc\_s

C++ benchmarks:

620.omnetpp\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xSSE4.2 -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -DSPEC\_SUPPRESS\_OPENMP -qopenmp  
-DSPEC\_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk\_s: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xSSE4.2 -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -DSPEC\_SUPPRESS\_OPENMP -qopenmp  
-DSPEC\_OPENMP -L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng\_s: Same as 620.omnetpp\_s

641.leela\_s: Same as 620.omnetpp\_s

Fortran benchmarks:

-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc

## Peak Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks (except as noted below):

-m64

623.xalancbmk\_s: -m32

Fortran benchmarks:

-m64



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed2017\_int\_base = 5.55

PowerEdge R240 (Intel Celeron G4900)

SPECspeed2017\_int\_peak = 5.85

**CPU2017 License:** 55

**Test Date:** Mar-2019

**Test Sponsor:** Dell Inc.

**Hardware Availability:** Dec-2018

**Tested by:** Dell Inc.

**Software Availability:** Oct-2018

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revE.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.5 on 2019-03-27 06:49:41-0400.

Report generated on 2019-04-16 17:16:00 by CPU2017 PDF formatter v6067.

Originally published on 2019-04-16.