



SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017_fp_base = 54.5

PowerEdge R6415 (AMD EPYC 7261, 2.50GHz)

SPECrate2017_fp_peak = 55.3

CPU2017 License: 55

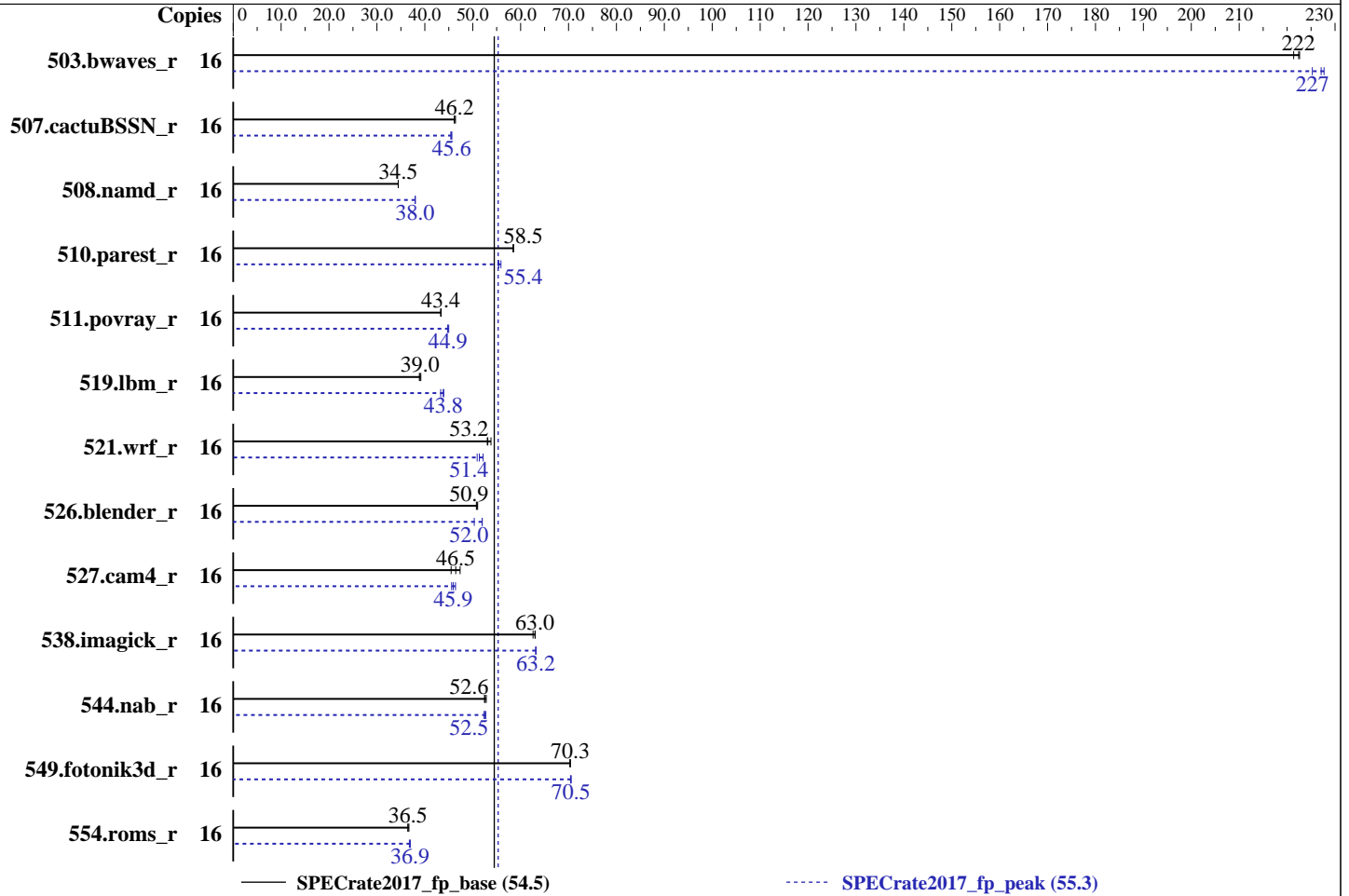
Test Date: Nov-2018

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Jul-2018



Hardware

CPU Name: AMD EPYC 7261
 Max MHz.: 2900
 Nominal: 2500
 Enabled: 8 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 64 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 64 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (8 x 64 GB 4DRx4 PC4-2667V-L)
 Storage: 1 x 480GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3
 kernel 4.4.126-94.22-default
 Compiler: C/C++: Version 1.0.0 of AOCC
 Fortran: Version 4.8.2 of GCC
 Parallel: No
 Firmware: Version 1.6.7 released Oct-2018
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator library V4.5.0



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 16 | 725 | 221 | <u>721</u> | <u>222</u> | 721 | 223 | 16 | 712 | 225 | 705 | 228 | <u>706</u> | <u>227</u> |
| 507.cactuBSSN_r | 16 | <u>439</u> | <u>46.2</u> | 439 | 46.2 | 437 | 46.4 | 16 | 444 | 45.6 | 446 | 45.4 | <u>444</u> | <u>45.6</u> |
| 508.namd_r | 16 | <u>441</u> | <u>34.5</u> | 441 | 34.5 | 441 | 34.4 | 16 | 400 | 38.0 | 400 | 38.0 | <u>400</u> | <u>38.0</u> |
| 510.parest_r | 16 | <u>716</u> | <u>58.5</u> | 716 | 58.5 | 716 | 58.5 | 16 | <u>755</u> | <u>55.4</u> | 749 | 55.9 | 756 | 55.4 |
| 511.povray_r | 16 | 861 | 43.4 | <u>862</u> | <u>43.4</u> | 863 | 43.3 | 16 | 830 | 45.0 | 835 | 44.7 | <u>833</u> | <u>44.9</u> |
| 519.lbm_r | 16 | 434 | 38.9 | <u>433</u> | <u>39.0</u> | 431 | 39.2 | 16 | 389 | 43.3 | <u>385</u> | <u>43.8</u> | 384 | 43.9 |
| 521.wrf_r | 16 | <u>674</u> | <u>53.2</u> | 666 | 53.8 | 676 | 53.0 | 16 | <u>698</u> | <u>51.4</u> | 704 | 50.9 | 687 | 52.2 |
| 526.blender_r | 16 | 480 | 50.8 | <u>479</u> | <u>50.9</u> | 478 | 51.0 | 16 | 484 | 50.3 | <u>469</u> | <u>52.0</u> | 469 | 52.0 |
| 527.cam4_r | 16 | <u>602</u> | <u>46.5</u> | 591 | 47.4 | 615 | 45.5 | 16 | 614 | 45.6 | <u>609</u> | <u>45.9</u> | 603 | 46.4 |
| 538.imagick_r | 16 | 631 | 63.0 | <u>632</u> | <u>63.0</u> | 636 | 62.6 | 16 | <u>630</u> | <u>63.2</u> | 630 | 63.2 | 630 | 63.2 |
| 544.nab_r | 16 | 510 | 52.8 | <u>512</u> | <u>52.6</u> | 513 | 52.5 | 16 | 511 | 52.7 | <u>513</u> | <u>52.5</u> | 514 | 52.4 |
| 549.fotonik3d_r | 16 | 886 | 70.4 | 887 | 70.3 | <u>886</u> | <u>70.3</u> | 16 | 884 | 70.5 | 886 | 70.4 | <u>884</u> | <u>70.5</u> |
| 554.roms_r | 16 | <u>697</u> | <u>36.5</u> | 697 | 36.5 | 693 | 36.7 | 16 | <u>689</u> | <u>36.9</u> | 691 | 36.8 | 688 | 37.0 |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

The AOCC Gold Linker plugin was installed and used for the link stage.

The AOCC Fortran Plugin version 1.0 was used to leverage AOCC optimizers with gfortran. It is available here: <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

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Operating System Notes (Continued)

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages were enabled for this run (OS default)

General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017-1.0.5/amd1704-rate-libs-revD/64;/home/cpu2017-1.0.5/amd1704-rate-libs-revD/32:"
MALLOC_CONF = "lg_chunk:28"

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using RHEL 7.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.5
in RHEL v7.2 under default conditions.
jemalloc: sources available from jemalloc.net or
<https://github.com/jemalloc/jemalloc/releases>
jemalloc uses environment variable MALLOC_CONF
with values narenas and lg_chunk:
narenas: sets the maximum number of arenas to use
for automatic multiplexing
of threads and arenas.
lg_chunk: set the virtual memory chunk size (log
base 2). For example,
lg_chunk:21 sets the default chunk size to 2^21 =
2MiB.

Platform Notes

BIOS settings:
Memory Interleaving set to Channel Interleaving
Virtualization Technology disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance

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Platform Notes (Continued)

Turbo Boost enabled
 C States set to Autonomous
 Memory Patrol Scrub disabled
 Memory Refresh Rate set to 1x
 PCI ASPM L1 Link Power Management disabled
 Determinism Slider set to Power Determinism
 Sysinfo program /home/cpu2017-1.0.5/bin/sysinfo
 Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
 running on linux-j270 Fri Nov 16 00:07:17 2018

SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
 model name : AMD EPYC 7261 8-Core Processor
 1 "physical id"s (chips)
 16 "processors"
 cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 8
 siblings : 16
 physical 0: cores 0 8 12 16 20 24 28

From lscpu:
 Architecture: x86_64
 CPU op-mode(s): 32-bit, 64-bit
 Byte Order: Little Endian
 CPU(s): 16
 On-line CPU(s) list: 0-15
 Thread(s) per core: 2
 Core(s) per socket: 8
 Socket(s): 1
 NUMA node(s): 4
 Vendor ID: AuthenticAMD
 CPU family: 23
 Model: 1
 Model name: AMD EPYC 7261 8-Core Processor
 Stepping: 2
 CPU MHz: 2495.291
 BogoMIPS: 4990.58
 Virtualization: AMD-V
 L1d cache: 32K
 L1i cache: 64K
 L2 cache: 512K
 L3 cache: 8192K
 NUMA node0 CPU(s): 0,4,8,12

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Platform Notes (Continued)

NUMA node1 CPU(s): 1,5,9,13
NUMA node2 CPU(s): 2,6,10,14
NUMA node3 CPU(s): 3,7,11,15

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc extd_apicid amd_dcm aperfmperf eagerfpu pni pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx arat cpb hw_pstate retpoline retpoline_amd npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold vmmcall avic fsgsbase bmi1 avx2 smep bmi2 rdseed adx smap clflushopt sha_ni xsaveopt xsavec xgetbv1 clzero irperf ibpb overflow_recov succor smca

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12
node 0 size: 128621 MB
node 0 free: 128461 MB
node 1 cpus: 1 5 9 13
node 1 size: 129018 MB
node 1 free: 128885 MB
node 2 cpus: 2 6 10 14
node 2 size: 129018 MB
node 2 free: 128866 MB
node 3 cpus: 3 7 11 15
node 3 size: 129017 MB
node 3 free: 128870 MB
node distances:
node 0 1 2 3
0: 10 16 16 16
1: 16 10 16 16
2: 16 16 10 16
3: 16 16 16 10
```

```
From /proc/meminfo
MemTotal: 528052476 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3
```

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Platform Notes (Continued)

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 3

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP3"

VERSION_ID="12.3"

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:

Linux linux-j270 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)

x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected

CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline + IBPB

run-level 3 Nov 15 15:10

SPEC is set to: /home/cpu2017-1.0.5

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda4 | xfs | 405G | 16G | 389G | 4% | /home |

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.6.7 10/29/2018

Memory:

8x 80CE863280CE M386A8K40BM2-CTD 64 GB 4 rank 2666

8x Not Specified Not Specified

(End of data from sysinfo program)



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Compiler Version Notes

=====
CC 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
CXXC 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
CC 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
FC 507.cactuBSSN_r(base, peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
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Compiler Version Notes (Continued)

InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

GNU Fortran (GCC) 4.8.2

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=====
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

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CC 521.wrf_r(base, peak) 527.cam4_r(base, peak)

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AOCC.LLVM.4.0.0.B35.2017_04_26)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

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Base Compiler Invocation (Continued)

Fortran benchmarks:

clang gfortran

Benchmarks using both Fortran and C:

clang gfortran

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang gfortran

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

507.cactuBSSN_r: -DSPEC_LP64

508.namd_r: -DSPEC_LP64

510.parest_r: -DSPEC_LP64

511.povray_r: -DSPEC_LP64

519.lbm_r: -DSPEC_LP64

521.wrf_r: -DSPEC_CASE_FLAG -fconvert=big-endian -DSPEC_LP64

526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64

527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-flto -Wl,-plugin-opt=-merge-constant

-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp

-O3 -ffast-math -march=znver1 -fstruct-layout=2

-mllvm -unroll-threshold=100 -fremap-arrays -mno-avx2

-mllvm -inline-threshold=1000 -mllvm -disable-vect-cmp -z muldefs

-ljemalloc

C++ benchmarks:

-flto -Wl,-plugin-opt=-merge-constant

-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp

-O3 -march=znver1 -mllvm -unroll-threshold=100 -finline-aggressive

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-fremap-arrays -mllvm -inline-threshold=1000 -mllvm -disable-vect-cmp  
-z muldefs -ljemalloc
```

Fortran benchmarks:

```
-flto -Wl,-plugin-opt=-merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp  
-O3 -mavx -madox -funroll-loops -ffast-math -z muldefs  
-fplugin=dragonegg.so -fplugin-arg-dragonegg-llvm-option=-merge-constant  
-fplugin-arg-dragonegg-llvm-option=-disable-vect-cmp -ljemalloc  
-lgfortran -lamdlibm
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-plugin-opt=-merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp  
-O3 -ffast-math -march=znver1 -fstruct-layout=2  
-mllvm -unroll-threshold=100 -fremap-arrays -mno-avx2  
-mllvm -inline-threshold=1000 -mllvm -disable-vect-cmp -mavx -madox  
-funroll-loops -z muldefs -fplugin=dragonegg.so  
-fplugin-arg-dragonegg-llvm-option=-merge-constant  
-fplugin-arg-dragonegg-llvm-option=-disable-vect-cmp -ljemalloc  
-lgfortran -lamdlibm
```

Benchmarks using both C and C++:

```
-flto -Wl,-plugin-opt=-merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp  
-O3 -ffast-math -march=znver1 -fstruct-layout=2  
-mllvm -unroll-threshold=100 -fremap-arrays -mno-avx2  
-mllvm -inline-threshold=1000 -mllvm -disable-vect-cmp  
-finline-aggressive -z muldefs -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-flto -Wl,-plugin-opt=-merge-constant  
-Wl,-plugin-opt=-lsr-in-nested-loop -Wl,-plugin-opt=-disable-vect-cmp  
-O3 -ffast-math -march=znver1 -fstruct-layout=2  
-mllvm -unroll-threshold=100 -fremap-arrays -mno-avx2  
-mllvm -inline-threshold=1000 -mllvm -disable-vect-cmp  
-finline-aggressive -mavx -madox -funroll-loops -z muldefs  
-fplugin=dragonegg.so -fplugin-arg-dragonegg-llvm-option=-merge-constant  
-fplugin-arg-dragonegg-llvm-option=-disable-vect-cmp -ljemalloc
```



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Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

clang gfortran

Benchmarks using both Fortran and C:

clang gfortran

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang gfortran

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively -mno-avx2
-mllvm -unroll-threshold=100 -fremap-arrays
-mllvm -inline-threshold=1000 -ljemalloc

C++ benchmarks:

-flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-finline-aggressive -mllvm -unroll-threshold=100 -fremap-arrays
-mllvm -inline-threshold=1000 -ljemalloc

Fortran benchmarks:

-flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -O3 -mavx2 -madvx -funroll-loops
-ffast-math -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-merge-constant

(Continued on next page)



SPEC CPU2017 Floating Point Rate Result

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Dell Inc.

SPECrate2017_fp_base = 54.5

PowerEdge R6415 (AMD EPYC 7261, 2.50GHz)

SPECrate2017_fp_peak = 55.3

CPU2017 License: 55

Test Date: Nov-2018

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2018

Tested by: Dell Inc.

Software Availability: Jul-2018

Peak Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -ljemalloc
-lgfortran -lamdlibm
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -O3 -mavx -ffast-math
-funroll-loops -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-merge-constant
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000
-ljemalloc -lgfortran -lamdlibm
```

```
527.cam4_r: -flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively
-mno-avx2 -mllvm -unroll-threshold=100 -fremap-arrays
-mllvm -inline-threshold=1000 -O3 -mavx2 -madox
-funroll-loops -ffast-math -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-merge-constant
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000
-ljemalloc -lgfortran -lamdlibm
```

Benchmarks using both C and C++:

```
-flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively -mno-avx2
-mllvm -unroll-threshold=100 -fremap-arrays
-mllvm -inline-threshold=1000 -finline-aggressive -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-flto -Wl,-plugin-opt=-merge-constant
-Wl,-plugin-opt=-lsr-in-nested-loop -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively -mno-avx2
-mllvm -unroll-threshold=100 -fremap-arrays
-mllvm -inline-threshold=1000 -finline-aggressive -O3 -mavx2 -madox
-funroll-loops -ffast-math -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-merge-constant
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aoccl100-flags-revC-I.2018-11-13.html>

<http://www.spec.org/cpu2017/flags/gcc.2018-02-16.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.2018-11-13.xml>

<http://www.spec.org/cpu2017/flags/gcc.2018-02-16.xml>

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