



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

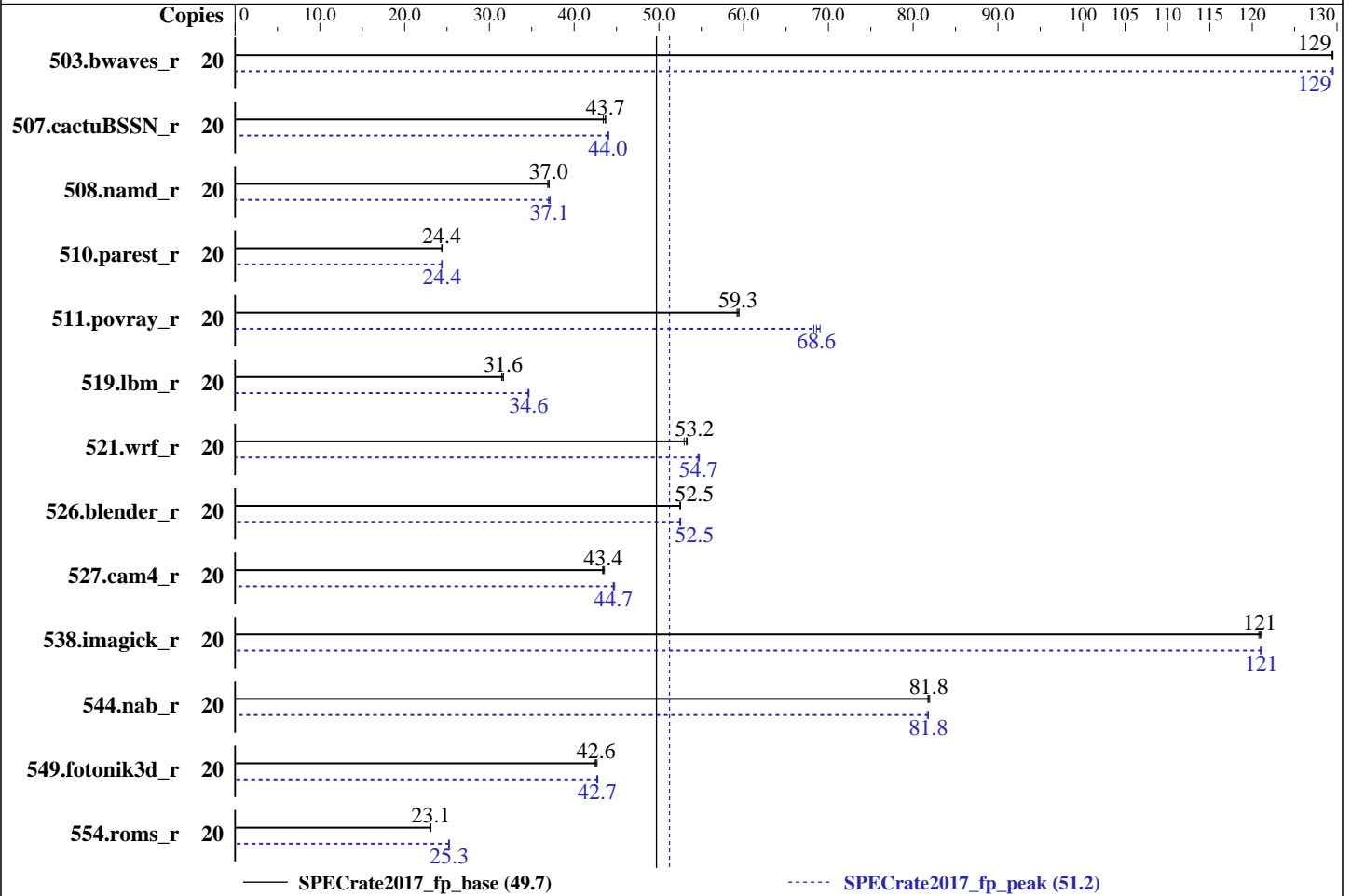
SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Oct-2018  
Hardware Availability: Jul-2017  
Software Availability: Mar-2018



### Hardware

CPU Name: Intel Xeon Silver 4114  
Max MHz.: 3000  
Nominal: 2200  
Enabled: 10 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 13.75 MB I+D on chip per chip  
Other: None  
Memory: 192 GB (6 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
Storage: 1 x 200 GB SATA III SSD  
Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86\_64)  
Kernel 4.4.114-94.11-default  
Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;  
Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux  
Parallel: No  
Firmware: Supermicro BIOS version 2.1 released Jun-2018  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Oct-2018  
Hardware Availability: Jul-2017  
Software Availability: Mar-2018

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	20	1550	129	<b>1549</b>	<b>129</b>	1548	130	20	1548	130	<b>1549</b>	<b>129</b>	1549	129
507.cactuBSSN_r	20	<b>580</b>	<b>43.7</b>	583	43.4	579	43.7	20	<b>575</b>	<b>44.0</b>	576	44.0	574	44.1
508.namd_r	20	<b>513</b>	<b>37.0</b>	515	36.9	513	37.0	20	514	37.0	511	37.1	<b>512</b>	<b>37.1</b>
510.parest_r	20	2142	24.4	2146	24.4	<b>2145</b>	<b>24.4</b>	20	2142	24.4	<b>2144</b>	<b>24.4</b>	2148	24.4
511.povray_r	20	785	59.5	789	59.2	<b>787</b>	<b>59.3</b>	20	677	69.0	<b>681</b>	<b>68.6</b>	684	68.3
519.lbm_r	20	670	31.5	<b>666</b>	<b>31.6</b>	666	31.6	20	610	34.6	608	34.7	<b>609</b>	<b>34.6</b>
521.wrf_r	20	<b>841</b>	<b>53.2</b>	840	53.3	846	53.0	20	<b>819</b>	<b>54.7</b>	820	54.6	818	54.8
526.blender_r	20	580	52.6	581	52.5	<b>580</b>	<b>52.5</b>	20	<b>580</b>	<b>52.5</b>	579	52.6	581	52.5
527.cam4_r	20	807	43.4	<b>805</b>	<b>43.4</b>	803	43.6	20	784	44.6	<b>783</b>	<b>44.7</b>	781	44.8
538.imagick_r	20	412	121	411	121	<b>411</b>	<b>121</b>	20	411	121	411	121	<b>411</b>	<b>121</b>
544.nab_r	20	412	81.7	<b>411</b>	<b>81.8</b>	411	81.9	20	412	81.7	411	81.8	<b>412</b>	<b>81.8</b>
549.fotonik3d_r	20	<b>1830</b>	<b>42.6</b>	1825	42.7	1835	42.5	20	<b>1823</b>	<b>42.7</b>	1827	42.7	1821	42.8
554.roms_r	20	1375	23.1	1377	23.1	<b>1376</b>	<b>23.1</b>	20	1261	25.2	<b>1259</b>	<b>25.3</b>	1258	25.3

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS Settings:

LLC prefetch = Enable  
Power Technology = Custom  
Power Performance Tuning = BIOS Controls EPB  
ENERGY\_PERF\_BIAS\_CFG mode = Maximum Performance  
Hardware P-state = Out of Band Mode  
XPT Prefetch = Enable  
Stale AtoS = Enable  
LLC dead line alloc = Disable  
Patrol Scrub = Disable  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-cyyj Tue Oct 9 02:55:44 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
 1 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 20
On-line CPU(s) list: 0-19
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

```

Model: 85
Model name: Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2200.000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-19
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

```

```

/proc/cpuinfo cache data
cache size : 14080 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
node 0 size: 192078 MB
node 0 free: 177522 MB
node distances:
node 0
0: 10

```

```

From /proc/meminfo
MemTotal: 196688068 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

```
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux linux-cyyj 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown):      Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB
```

```
run-level 3 Oct 8 15:33
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   145G   46G   99G   32% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 2.1 06/14/2018
Memory:
  2x NO DIMM NO DIMM
  6x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----
=====
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Compiler Version Notes (Continued)

CC 519.lbm\_r(peak)

icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CXXC 508.namd\_r(base) 510.parest\_r(base, peak)

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CXXC 508.namd\_r(peak)

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 511.povray\_r(base) 526.blender\_r(base, peak)

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 511.povray\_r(peak)

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 507.cactuBSSN\_r(base, peak)

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Compiler Version Notes (Continued)

=====  
FC 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 554.roms\_r(peak)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 521.wrf\_r(peak) 527.cam4\_r(peak)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

### Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64  
507.cactuBSSN_r: -DSPEC_LP64  
508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG  
538.imagick_r: -DSPEC_LP64  
544.nab_r: -DSPEC_LP64  
549.fotonik3d_r: -DSPEC_LP64  
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

(Continued on next page)





# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Oct-2018  
Hardware Availability: Jul-2017  
Software Availability: Mar-2018

## Base Optimization Flags (Continued)

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

## Peak Optimization Flags (Continued)

519.lbm\_r (continued):

-qopt-mem-layout-trans=3

538.imagick\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=3

544.nab\_r: Same as 538.imagick\_r

C++ benchmarks:

508.namd\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3

-no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3

510.parest\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=3 -auto

-nostandard-realloc-lhs

549.fotonik3d\_r: Same as 503.bwaves\_r

554.roms\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3

-no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3

-no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

511.povray\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3

-no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3

526.blender\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5019P-MT (X11SPi-TF , Intel Xeon Silver 4114)

SPECrate2017\_fp\_base = 49.7

SPECrate2017\_fp\_peak = 51.2

**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Tested by:** Supermicro

**Test Date:** Oct-2018

**Hardware Availability:** Jul-2017

**Software Availability:** Mar-2018

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.5 on 2018-10-08 14:55:44-0400.

Report generated on 2018-10-31 19:10:35 by CPU2017 PDF formatter v6067.

Originally published on 2018-10-30.