



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 126

### Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175

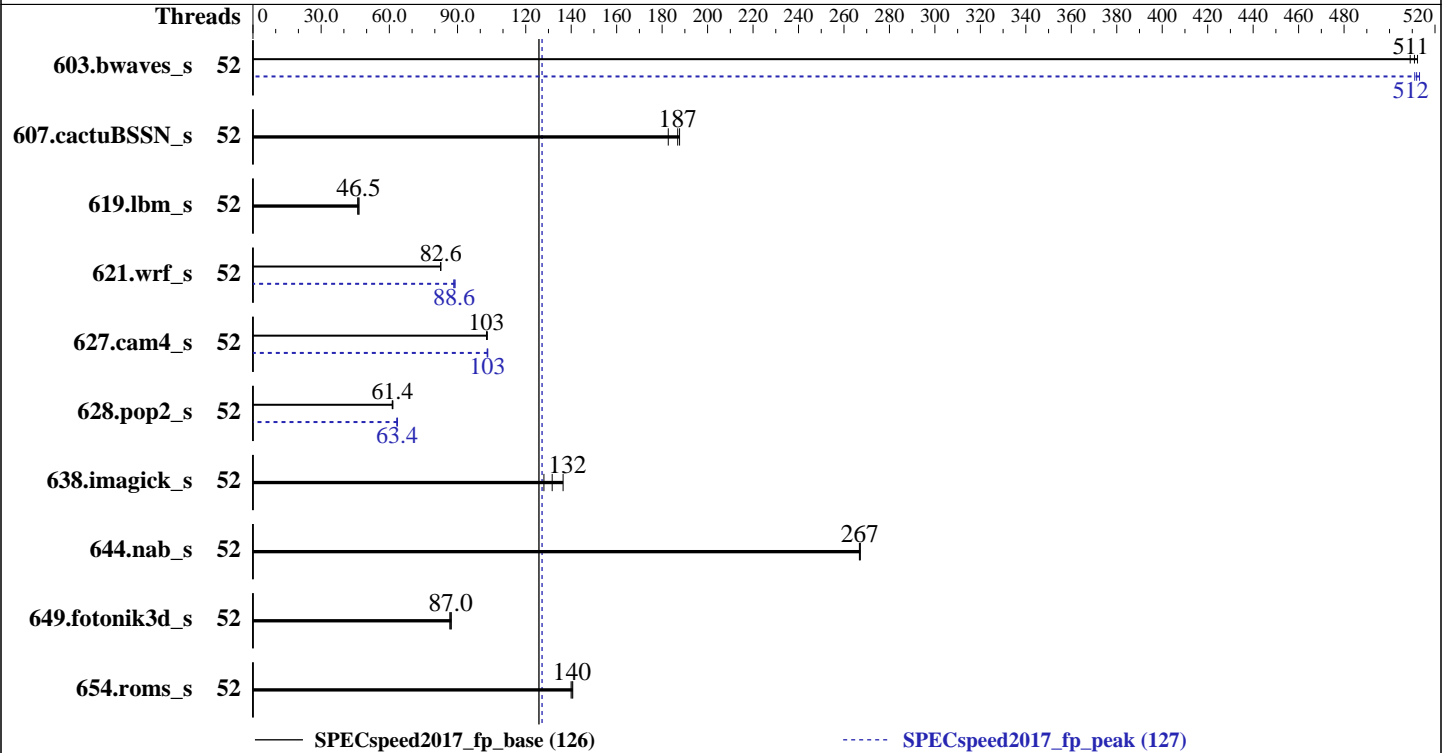
Test Sponsor: Huawei

Tested by: Huawei

Test Date: Sep-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018



### Hardware

CPU Name: Intel Xeon Platinum 8170  
 Max MHz.: 3700  
 Nominal: 2100  
 Enabled: 52 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 1200 GB SAS, 10000 RPM  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-693.11.6.el7.x86\_64  
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 0.80 Released Jun-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECSpeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECSpeed2017\_fp\_peak = 127

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: Sep-2018  
Hardware Availability: Jul-2017  
Software Availability: Mar-2018

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	52	115	512	116	509	<b>115</b>	<b>511</b>	52	115	511	<b>115</b>	<b>512</b>	115	513
607.cactuBSSN_s	52	91.2	183	88.8	188	<b>89.2</b>	<b>187</b>	52	91.2	183	88.8	188	<b>89.2</b>	<b>187</b>
619.lbm_s	52	114	46.0	<b>113</b>	<b>46.5</b>	112	46.6	52	114	46.0	<b>113</b>	<b>46.5</b>	112	46.6
621.wrf_s	52	<b>160</b>	<b>82.6</b>	160	82.6	160	82.5	52	149	88.9	150	88.3	<b>149</b>	<b>88.6</b>
627.cam4_s	52	<b>86.2</b>	<b>103</b>	86.0	103	86.2	103	52	<b>85.9</b>	<b>103</b>	86.0	103	85.8	103
628.pop2_s	52	194	61.3	<b>193</b>	<b>61.4</b>	193	61.6	52	<b>187</b>	<b>63.4</b>	188	63.3	187	63.6
638.imagick_s	52	113	128	<b>110</b>	<b>132</b>	106	136	52	113	128	<b>110</b>	<b>132</b>	106	136
644.nab_s	52	65.5	267	<b>65.4</b>	<b>267</b>	65.4	267	52	65.5	267	<b>65.4</b>	<b>267</b>	65.4	267
649.fotonik3d_s	52	104	87.3	<b>105</b>	<b>87.0</b>	105	86.6	52	104	87.3	<b>105</b>	<b>87.0</b>	105	86.6
654.roms_s	52	112	141	112	140	<b>112</b>	<b>140</b>	52	112	141	112	140	<b>112</b>	<b>140</b>

SPECSpeed2017\_fp\_base = **126**

SPECSpeed2017\_fp\_peak = **127**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64:/spec2017/je5.0.1-32:/spec2017/je5.0.1-64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Sep-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018

## Platform Notes

BIOS configuration:

Power Policy Set to Load Balance

Hyper-Threading Set to Disable

XPT Prefetch Set to Enabled

Sysinfo program /spec2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on localhost.localdomain Thu Sep 20 11:36:57 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz

2 "physical id"s (chips)

52 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 26

siblings : 26

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 52

On-line CPU(s) list: 0-51

Thread(s) per core: 1

Core(s) per socket: 26

Socket(s): 2

NUMA node(s): 2

Vendor ID: GenuineIntel

CPU family: 6

Model: 85

Model name: Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz

Stepping: 4

CPU MHz: 2101.000

BogoMIPS: 4205.23

Virtualization: VT-x

L1d cache: 32K

L1i cache: 32K

L2 cache: 1024K

L3 cache: 36608K

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Sep-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

NUMA node0 CPU(s): 0-25  
NUMA node1 CPU(s): 26-51

/proc/cpuinfo cache data  
cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
node 0 size: 194737 MB
node 0 free: 189361 MB
node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50
51
node 1 size: 196608 MB
node 1 free: 191415 MB
node distances:
node  0  1
  0:  10  21
  1:  21  10
```

From /proc/meminfo  
MemTotal: 394169164 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.3 (Maipo)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="7.3"  
PRETTY\_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"  
ANSI\_COLOR="0;31"  
CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.3:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.3:ga:server

uname -a:  
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86\_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Sep 20 06:39

SPEC is set to: /spec2017

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Sep-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	400G	8.3G	392G	3%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.80 06/27/2018

Memory:

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
-----

icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 619.lbm\_s(peak)  
-----

icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(base, peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base, peak)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: Sep-2018  
Hardware Availability: Jul-2017  
Software Availability: Mar-2018

## Compiler Version Notes (Continued)

=====  
FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak)  
=====

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)  
=====

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
CC 621.wrf\_s(peak) 628.pop2\_s(peak)  
=====

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Sep-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018

## Base Portability Flags (Continued)

```
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

Fortran benchmarks:

```
ifort -m64
```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Sep-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: basepeak = yes
```

```
638.imagick_s: basepeak = yes
```

```
644.nab_s: basepeak = yes
```

Fortran benchmarks:

```
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP  
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-qopenmp -nostandard-realloc-lhs
```

```
649.fotonik3d_s: basepeak = yes
```

```
654.roms_s: basepeak = yes
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs
```

```
627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs
```

(Continued on next page)





# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 126

Huawei CH225 V5 (Intel Xeon Platinum 8170)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Sep-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018

## Peak Optimization Flags (Continued)

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-09-20 11:36:56-0400.

Report generated on 2018-10-31 19:03:52 by CPU2017 PDF formatter v6067.

Originally published on 2018-10-16.