



SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR950
(2.50 GHz, Intel Xeon Platinum 8180)

SPECrate2017_fp_base = 777

SPECrate2017_fp_peak = Not Run

CPU2017 License: 9017

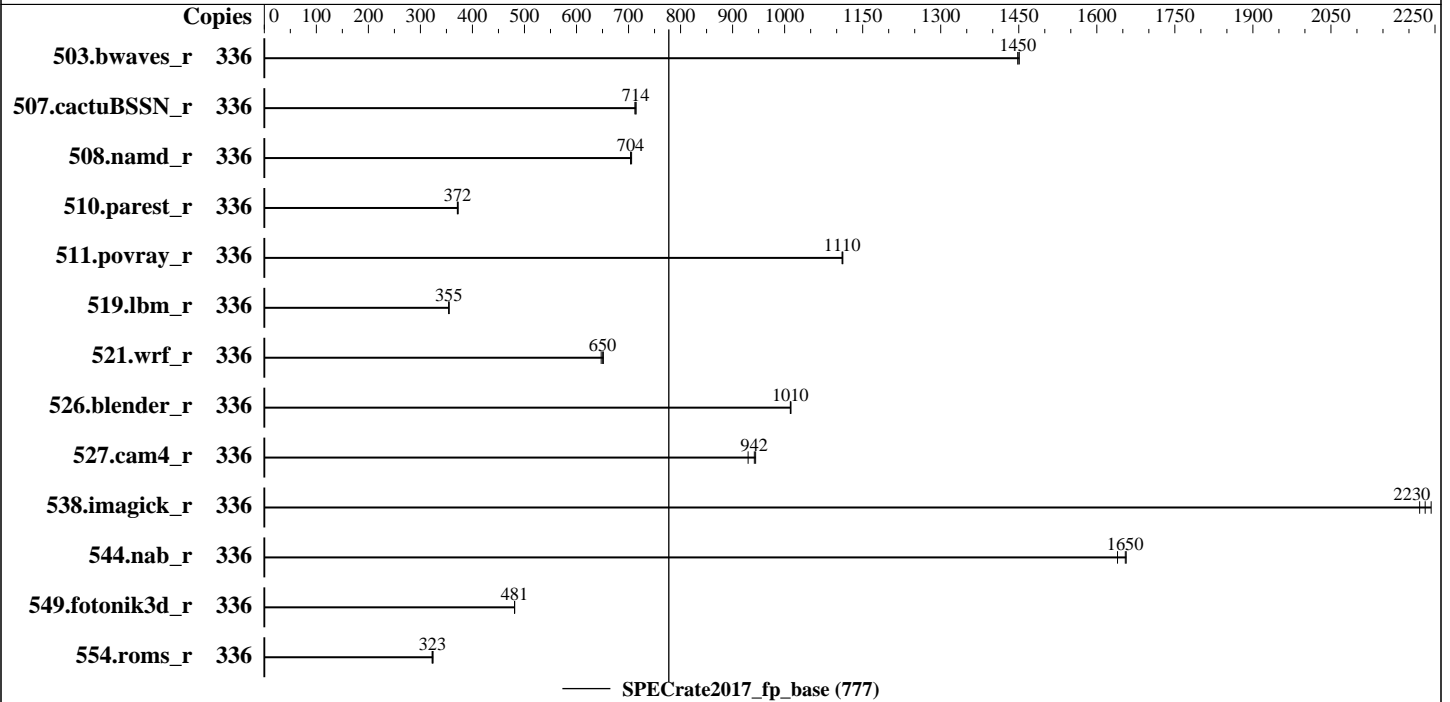
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2018

Hardware Availability: Sep-2017

Software Availability: May-2018



Hardware

CPU Name: Intel Xeon Platinum 8180
 Max MHz.: 3800
 Nominal: 2500
 Enabled: 168 cores, 6 chips, 2 threads/core
 Orderable: 2,3,4,6,8 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 38.5 MB I+D on chip per chip
 Other: None
 Memory: 1152 GB (72 x 16 GB 2Rx8 PC4-2666V-R)
 Storage: 1 x 800 GB SAS SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)
 Kernel 4.4.121-92.80-default
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.2.199 of Intel Fortran
 Compiler for Linux
 Parallel: No
 Firmware: Lenovo BIOS Version PSE113P 1.30 released Jun-2018
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	336	2326	1450	2322	1450	<u>2326</u>	<u>1450</u>							
507.cactuBSSN_r	336	597	712	<u>596</u>	<u>714</u>	595	714							
508.namd_r	336	<u>453</u>	<u>704</u>	452	706	453	704							
510.parest_r	336	2359	373	<u>2365</u>	<u>372</u>	2369	371							
511.povray_r	336	706	1110	<u>706</u>	<u>1110</u>	706	1110							
519.lbm_r	336	<u>998</u>	<u>355</u>	998	355	999	355							
521.wrf_r	336	<u>1157</u>	<u>650</u>	1155	652	1163	647							
526.blender_r	336	506	1010	<u>506</u>	<u>1010</u>	506	1010							
527.cam4_r	336	<u>624</u>	<u>942</u>	622	944	632	930							
538.imagick_r	336	373	2240	<u>375</u>	<u>2230</u>	376	2220							
544.nab_r	336	341	1660	345	1640	<u>342</u>	<u>1650</u>							
549.fotonik3d_r	336	<u>2722</u>	<u>481</u>	2723	481	2721	481							
554.roms_r	336	<u>1652</u>	<u>323</u>	1646	324	1656	322							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic18.0u2/lib/ia32:/home/cpu2017-1.0.5-ic18.0u2/lib/intel64"

LD_LIBRARY_PATH = "\$LD_LIBRARY_PATH:/home/cpu2017-1.0.5-ic18.0u2/je5.0.1-32:/home/cpu2017-1.0.5-ic18.0u2/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Max Performance
Choose Operating Mode set to Custom Mode
C-States set to Legacy
Execute Disable Bit set to Disable
Trusted Execution Technology set to Enable
Hardware Prefetcher set to Disable
SNC set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic18.0u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-s8se Fri Aug 3 16:27:38 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
6 "physical id"s (chips)
336 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 56
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 4: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 6: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

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Platform Notes (Continued)

From lscpu:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 336
On-line CPU(s) list:   0-335
Thread(s) per core:    2
Core(s) per socket:    28
Socket(s):              6
NUMA node(s):         12
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
Stepping:               4
CPU MHz:                2494.160
BogoMIPS:               4988.32
Virtualization:        VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               39424K
NUMA node0 CPU(s):     0-3,7-9,14-17,21-23,168-171,175-177,182-185,189-191
NUMA node1 CPU(s):     4-6,10-13,18-20,24-27,172-174,178-181,186-188,192-195
NUMA node2 CPU(s):     28-31,35-37,42-45,49-51,196-199,203-205,210-213,217-219
NUMA node3 CPU(s):     32-34,38-41,46-48,52-55,200-202,206-209,214-216,220-223
NUMA node4 CPU(s):     56-59,63-65,70-73,77-79,224-227,231-233,238-241,245-247
NUMA node5 CPU(s):     60-62,66-69,74-76,80-83,228-230,234-237,242-244,248-251
NUMA node6 CPU(s):     84-87,91-93,98-101,105-107,252-255,259-261,266-269,273-275
NUMA node7 CPU(s):     88-90,94-97,102-104,108-111,256-258,262-265,270-272,276-279
NUMA node8 CPU(s):     112-115,119-121,126-129,133-135,280-283,287-289,294-297,301-303
NUMA node9 CPU(s):     116-118,122-125,130-132,136-139,284-286,290-293,298-300,304-307
NUMA node10 CPU(s):    140-143,147-149,154-157,161-163,308-311,315-317,322-325,329-331
NUMA node11 CPU(s):    144-146,150-153,158-160,164-167,312-314,318-321,326-328,332-335
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl stibp ssbd retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm

```

(Continued on next page)



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Platform Notes (Continued)

```
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
```

```
/proc/cpuinfo cache data
cache size : 39424 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 12 nodes (0-11)
node 0 cpus: 0 1 2 3 7 8 9 14 15 16 17 21 22 23 168 169 170 171 175 176 177 182 183 184
185 189 190 191
node 0 size: 96212 MB
node 0 free: 95735 MB
node 1 cpus: 4 5 6 10 11 12 13 18 19 20 24 25 26 27 172 173 174 178 179 180 181 186 187
188 192 193 194 195
node 1 size: 96756 MB
node 1 free: 96285 MB
node 2 cpus: 28 29 30 31 35 36 37 42 43 44 45 49 50 51 196 197 198 199 203 204 205 210
211 212 213 217 218 219
node 2 size: 96756 MB
node 2 free: 96456 MB
node 3 cpus: 32 33 34 38 39 40 41 46 47 48 52 53 54 55 200 201 202 206 207 208 209 214
215 216 220 221 222 223
node 3 size: 96756 MB
node 3 free: 96455 MB
node 4 cpus: 56 57 58 59 63 64 65 70 71 72 73 77 78 79 224 225 226 227 231 232 233 238
239 240 241 245 246 247
node 4 size: 96756 MB
node 4 free: 96304 MB
node 5 cpus: 60 61 62 66 67 68 69 74 75 76 80 81 82 83 228 229 230 234 235 236 237 242
243 244 248 249 250 251
node 5 size: 96756 MB
node 5 free: 96465 MB
node 6 cpus: 84 85 86 87 91 92 93 98 99 100 101 105 106 107 252 253 254 255 259 260 261
266 267 268 269 273 274 275
node 6 size: 96756 MB
node 6 free: 96468 MB
node 7 cpus: 88 89 90 94 95 96 97 102 103 104 108 109 110 111 256 257 258 262 263 264
265 270 271 272 276 277 278 279
node 7 size: 96756 MB
node 7 free: 96466 MB
node 8 cpus: 112 113 114 115 119 120 121 126 127 128 129 133 134 135 280 281 282 283
287 288 289 294 295 296 297 301 302 303
node 8 size: 96756 MB
node 8 free: 96467 MB
node 9 cpus: 116 117 118 122 123 124 125 130 131 132 136 137 138 139 284 285 286 290
291 292 293 298 299 300 304 305 306 307
```

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Platform Notes (Continued)

```

node 9 size: 96756 MB
node 9 free: 96473 MB
node 10 cpus: 140 141 142 143 147 148 149 154 155 156 157 161 162 163 308 309 310 311
315 316 317 322 323 324 325 329 330 331
node 10 size: 96756 MB
node 10 free: 96405 MB
node 11 cpus: 144 145 146 150 151 152 153 158 159 160 164 165 166 167 312 313 314 318
319 320 321 326 327 328 332 333 334 335
node 11 size: 96752 MB
node 11 free: 96393 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11
 0:  10 11 21 21 31 31 21 21 21 21 20 20
 1:  11 10 21 21 31 31 21 21 21 21 20 20
 2:  21 21 10 11 21 21 31 31 31 31 20 20
 3:  21 21 11 10 21 21 31 31 31 31 20 20
 4:  31 31 21 21 10 11 21 21 31 31 20 20
 5:  31 31 21 21 11 10 21 21 31 31 20 20
 6:  21 21 31 31 21 21 10 11 31 31 20 20
 7:  21 21 31 31 21 21 11 10 31 31 20 20
 8:  21 21 31 31 31 31 31 31 10 11 20 20
 9:  21 21 31 31 31 31 31 31 11 10 20 20
10:  20 20 20 20 20 20 20 20 20 20 10 20
11:  20 20 20 20 20 20 20 20 20 20 20 10

```

From /proc/meminfo

```

MemTotal:      1188386584 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

uname -a:

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Platform Notes (Continued)

Linux linux-s8se 4.4.121-92.80-default #1 SMP Mon May 21 14:40:10 UTC 2018 (2afdd00)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Aug 3 11:32

SPEC is set to: /home/cpu2017-1.0.5-ic18.0u2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb3 xfs 743G 166G 578G 23% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE113P-1.30]- 06/14/2018
Memory:
24x NO DIMM NO DIMM
72x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CXXC 508.namd_r(base) 510.parest_r(base)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 511.povray_r(base) 526.blender_r(base)

icpc (ICC) 18.0.2 20180210

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Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 507.cactuBSSN_r(base)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CC 521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

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Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

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Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-H.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-H.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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