SPEC® CPU2017 Integer Rate Result

Supermicro

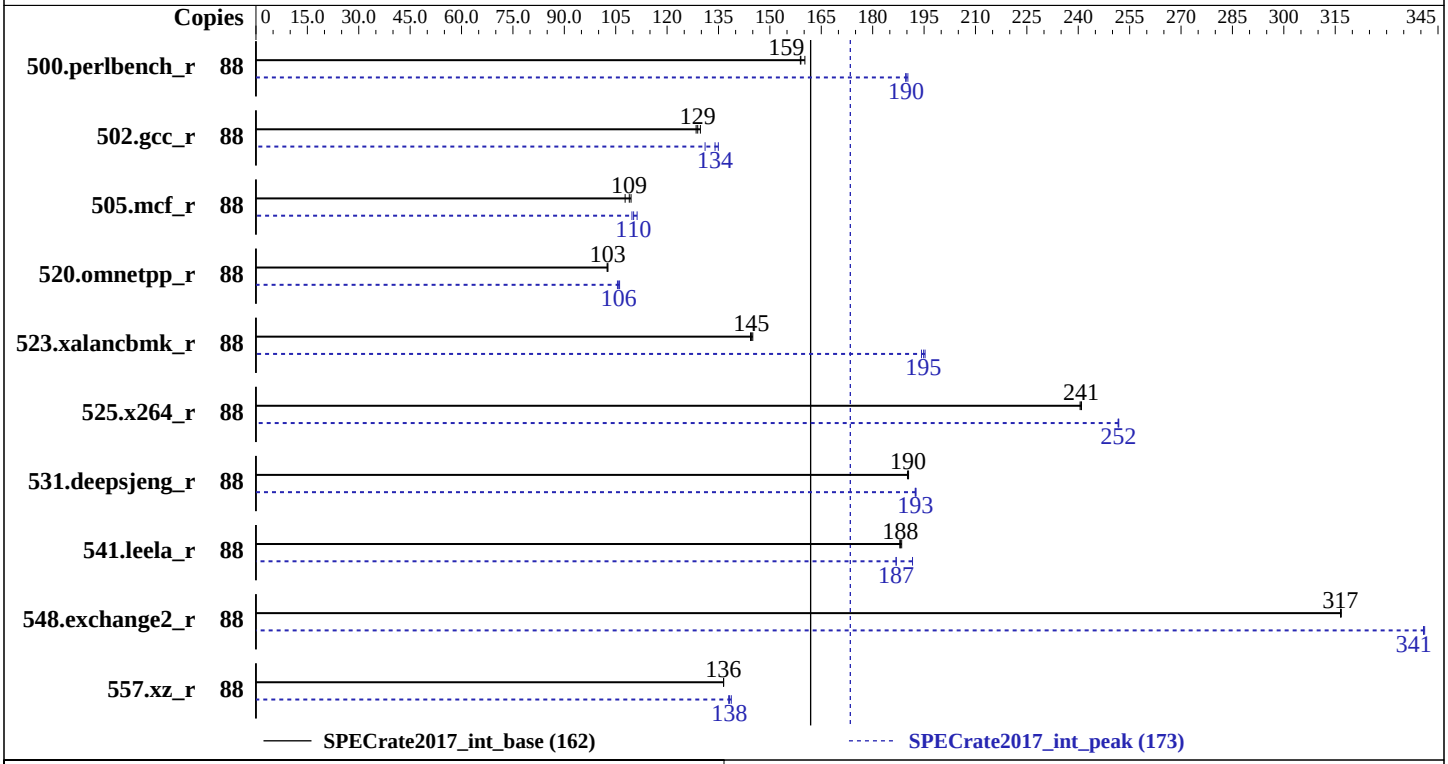
SuperServer 2028U-TR4T+ (X10DRU-i+, Intel Xeon E5-2699A v4)

SPECrate2017_int_base = 162

SPECrate2017_int_peak = 173

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Aug-2017
Hardware Availability: Oct-2016
Software Availability: May-2017



Hardware

CPU Name:	Intel Xeon E5-2699A v4
Max MHz.:	3600
Nominal:	2400
Enabled:	44 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	256 KB I+D on chip per core
L3:	55 MB I+D on chip per chip
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Storage:	600 GB, SATA2, SSD
Other:	None

Software

OS:	SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
Compiler:	C/C++: Version 17.0.4.196 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.4.196 of Intel Fortran Compiler for Linux
Parallel:	No
Firmware:	BIOS American Megatrends Inc. Version 2.0c released Apr-2017
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other:	Microquill SmartHeap V10.2

SPEC CPU2017 Integer Rate Result

Supermicro

SuperServer 2028U-TR4T+ (X10DRU-i+, Intel Xeon E5-2699A v4)

SPECrate2017_int_base = 162

SPECrate2017_int_peak = 173

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Aug-2017
Hardware Availability: Oct-2016
Software Availability: May-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	88	882	159	**881**	**159**	874	160	88	**738**	**190**	739	190	736	190
502.gcc_r	88	**966**	**129**	961	130	970	128	88	923	135	**930**	**134**	951	131
505.mcf_r	88	1320	108	1299	109	**1305**	**109**	88	**1290**	**110**	1296	110	1278	111
520.omnetpp_r	88	**1124**	**103**	1127	102	1124	103	88	1094	106	1088	106	**1091**	**106**
523.xalancbmk_r	88	644	144	641	145	**642**	**145**	88	476	195	478	194	**477**	**195**
525.x264_r	88	**640**	**241**	641	241	640	241	88	612	252	612	252	**612**	**252**
531.deepsjeng_r	88	529	190	**530**	**190**	530	190	88	**524**	**193**	524	193	524	192
541.leela_r	88	773	188	775	188	**775**	**188**	88	780	187	**780**	**187**	760	192
548.exchange2_r	88	**728**	**317**	728	317	728	317	88	**677**	**341**	677	341	676	341
557.xz_r	88	**696**	**136**	696	136	696	136	88	**688**	**138**	685	139	689	138

SPECrate2017_int_base = 162

SPECrate2017_int_peak = 173

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
```

Platform Notes

```
BIOS Settings:
QPI Configuration:
 COD Enable: enabled
 Early Snoop: disabled
Memory Configuration:
 Patrol Scrub: disabled

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on 196-204 Tue Aug 22 04:21:00 2017

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
       2  "physical id"s (chips)
       88 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 22
       siblings  : 44
       physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
       physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

 From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                88
      On-line CPU(s) list:   0-87
      Thread(s) per core:    2
      Core(s) per socket:    22
      Socket(s):             2
      NUMA node(s):          4
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 79
      Model name:            Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
      Stepping:              1
      CPU MHz:               3000.325
      CPU max MHz:           3600.0000
      CPU min MHz:           1200.0000
      BogoMIPS:              4799.98
      Virtualization:        VT-x
      L1d cache:             32K
```

(Continued on next page)

Platform Notes (Continued)

```
      L1i cache:             32K
      L2 cache:              256K
      L3 cache:              28160K
      NUMA node0 CPU(s):     0-10,44-54
      NUMA node1 CPU(s):     11-21,55-65
      NUMA node2 CPU(s):     22-32,66-76
      NUMA node3 CPU(s):     33-43,77-87
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf
      eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
      xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
      avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
      tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
      erms invpcid rtm cqm rdseed adx smap xsaveopt cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 28160 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 44 45 46 47 48 49 50 51 52 53 54
   node 0 size: 128828 MB
   node 0 free: 128442 MB
   node 1 cpus: 11 12 13 14 15 16 17 18 19 20 21 55 56 57 58 59 60 61 62 63 64 65
   node 1 size: 129020 MB
   node 1 free: 128688 MB
   node 2 cpus: 22 23 24 25 26 27 28 29 30 31 32 66 67 68 69 70 71 72 73 74 75 76
   node 2 size: 129020 MB
   node 2 free: 128662 MB
   node 3 cpus: 33 34 35 36 37 38 39 40 41 42 43 77 78 79 80 81 82 83 84 85 86 87
   node 3 size: 129019 MB
   node 3 free: 128669 MB
   node distances:
   node   0   1   2   3
     0:  10  11  21  21
     1:  11  10  21  21
     2:  21  21  10  11
     3:  21  21  11  10

 From /proc/meminfo
    MemTotal:       528270604 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 /usr/bin/lsb_release -d
```

(Continued on next page)

Platform Notes (Continued)

```
    SUSE Linux Enterprise Server 12 SP2

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 2
       # This file is deprecated and will be removed in a future service pack or release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP2"
       VERSION_ID="12.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp2"

 uname -a:
    Linux 196-204 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64
    x86_64 x86_64 GNU/Linux

 run-level 3 Aug 22 04:20

 SPEC is set to: /home/cpu2017
    Filesystem                              Type  Size  Used Avail Use% Mounted on
    /dev/mapper/isw_cefabaa_OS_Disk-part8 xfs   489G   91G  398G  19% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS American Megatrends Inc. 2.0c 04/21/2017
   Memory:
    8x NO DIMM NO DIMM
    16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 17.0.4 20170411
```


(Continued on next page)

Compiler Version Notes (Continued)

```
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
     557.xz_r(peak)
------------------------------------------------------------------------------
icc (ICC) 17.0.4 20170411
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 17.0.4 20170411
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
     541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 17.0.4 20170411
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 17.0.4 20170411
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC   548.exchange2_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 17.0.4 20170411
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

(Continued on next page)

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -L/sh10.2 -lsmartheap64

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks (except as noted below):
icpc -m64

(Continued on next page)

Peak Compiler Invocation (Continued)

523.xalancbmk_r: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -qopt-mem-layout-trans=3
-fno-strict-overflow

502.gcc_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -qopt-mem-layout-trans=3

505.mcf_r: Same as 502.gcc_r

525.x264_r: Same as 502.gcc_r

557.xz_r: Same as 502.gcc_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -L/sh10.2 -lsmartheap64

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -L/sh10.2 -lsmartheap

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

(Continued on next page)

Peak Optimization Flags (Continued)

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -qopt-mem-layout-trans=3
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-revI.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-revI.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.1 on 2017-08-22 07:20:59-0400.
Report generated on 2018-10-31 12:42:06 by CPU2017 PDF formatter v6067.
Originally published on 2017-09-26.