



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL560 Gen10  
(2.50 GHz, Intel Xeon Platinum 8180M)

**SPECint®2006 = Not Run**

**SPECint\_base2006 = 80.5**

**CPU2006 license:** 3

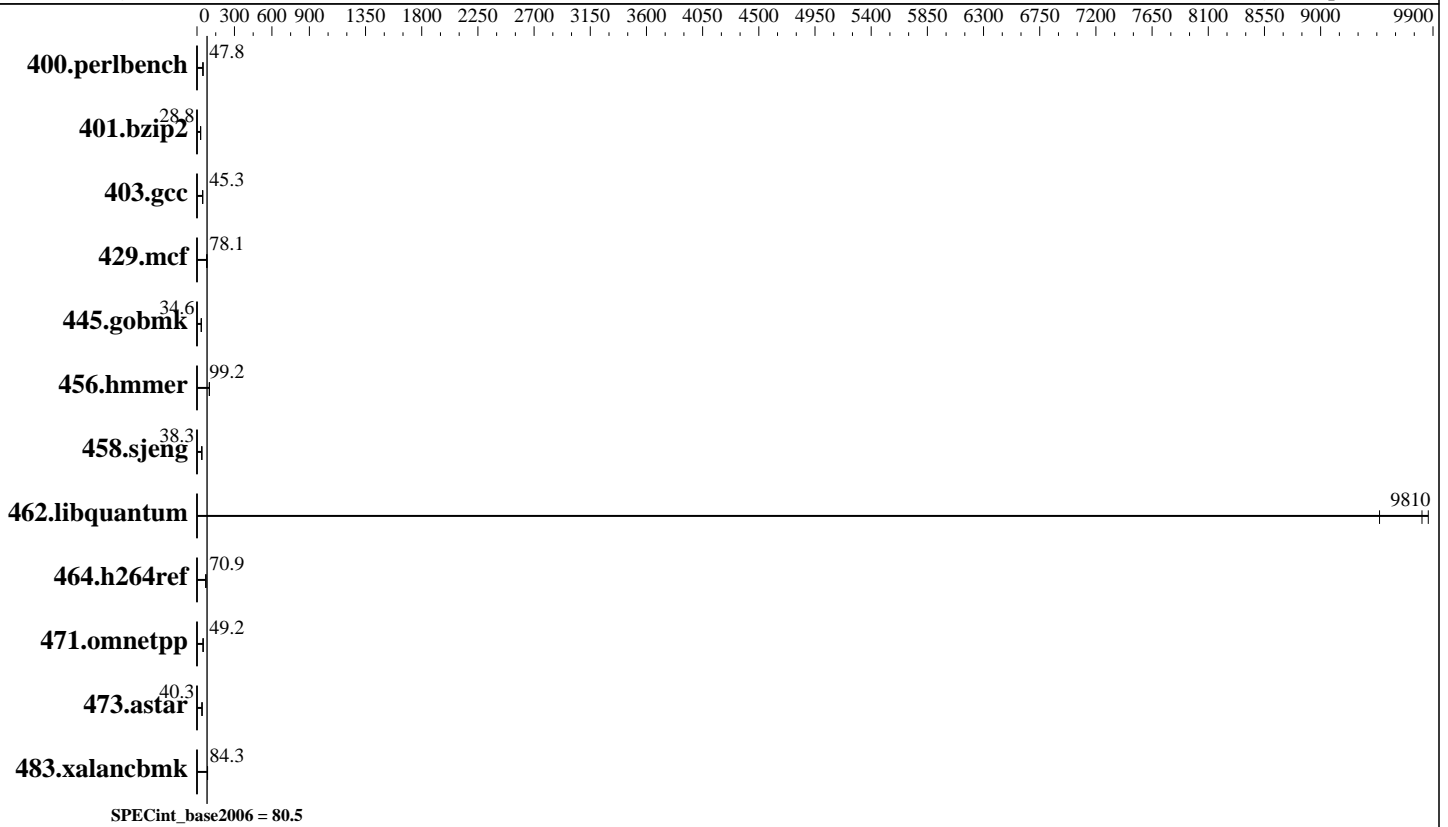
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Nov-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



### Hardware

**CPU Name:** Intel Xeon Platinum 8180M  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.80 GHz  
**CPU MHz:** 2500  
**FPU:** Integrated  
**CPU(s) enabled:** 112 cores, 4 chips, 28 cores/chip  
**CPU(s) orderable:** 1, 2, 4 chip(s)  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** 38.5 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
**Disk Subsystem:** 1 x 480 GB SATA SSD, RAID 0  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Kernel 3.10.0-514.el7.x86\_64  
**Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** Not Applicable  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	205	47.7	<b><u>205</u></b>	<b><u>47.8</u></b>	205	47.8						
401.bzip2	335	28.8	334	28.9	<b><u>335</u></b>	<b><u>28.8</u></b>						
403.gcc	<b><u>178</u></b>	<b><u>45.3</u></b>	178	45.3	178	45.3						
429.mcf	114	80.2	118	77.6	<b><u>117</u></b>	<b><u>78.1</u></b>						
445.gobmk	304	34.5	304	34.6	<b><u>304</u></b>	<b><u>34.6</u></b>						
456.hmmr	93.8	99.4	94.1	99.1	<b><u>94.1</u></b>	<b><u>99.2</u></b>						
458.sjeng	316	38.3	316	38.3	<b><u>316</u></b>	<b><u>38.3</u></b>						
462.libquantum	2.10	9860	2.19	9470	<b><u>2.11</u></b>	<b><u>9810</u></b>						
464.h264ref	312	70.9	312	71.0	<b><u>312</u></b>	<b><u>70.9</u></b>						
471.omnetpp	127	49.2	127	49.1	<b><u>127</u></b>	<b><u>49.2</u></b>						
473.astar	174	40.4	174	40.2	<b><u>174</u></b>	<b><u>40.3</u></b>						
483.xalancbmk	<b><u>81.8</u></b>	<b><u>84.3</u></b>	81.9	84.2	81.7	84.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
IRQ balance service was stop using "service irqbalance stop"  
Tuned-adm profile was set to Throughput-Performance

## Platform Notes

### BIOS Configuration:

Intel Hyperthreading set to Disabled  
Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Stale A to S set to Enabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Peak Frequency Compute  
Energy/Performance Bias set to Maximum Performance  
Workload Profile set to Custom  
NUMA Group Size Optimization set to Flat  
Sysinfo program /cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on DL560-Gen10 Mon Nov 13 17:11:09 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8180M CPU @ 2.50GHz

4 "physical id"s (chips)

112 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 28

siblings : 28

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24  
25 26 27 28 29 30

cache size : 39424 KB

From /proc/meminfo

MemTotal: 792066452 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.3 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.3"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.3:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.3:ga:server

uname -a:

Linux DL560-Gen10 3.10.0-514.el7.x86\_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016

x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Nov 13 15:52

SPEC is set to: /cpu2006

Filesystem Type Size Used Avail Use% Mounted on  
/dev/sdb1 xfs 447G 39G 409G 9% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

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## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/cpu2006/lib/ia32:/cpu2006/lib/intel64:/cpu2006/sh10.2"

OMP\_NUM\_THREADS = "112"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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Standard Performance Evaluation Corporation

[info@spec.org](mailto:info@spec.org)

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