



SPEC® CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.20 GHz, Intel Xeon Gold 6146)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

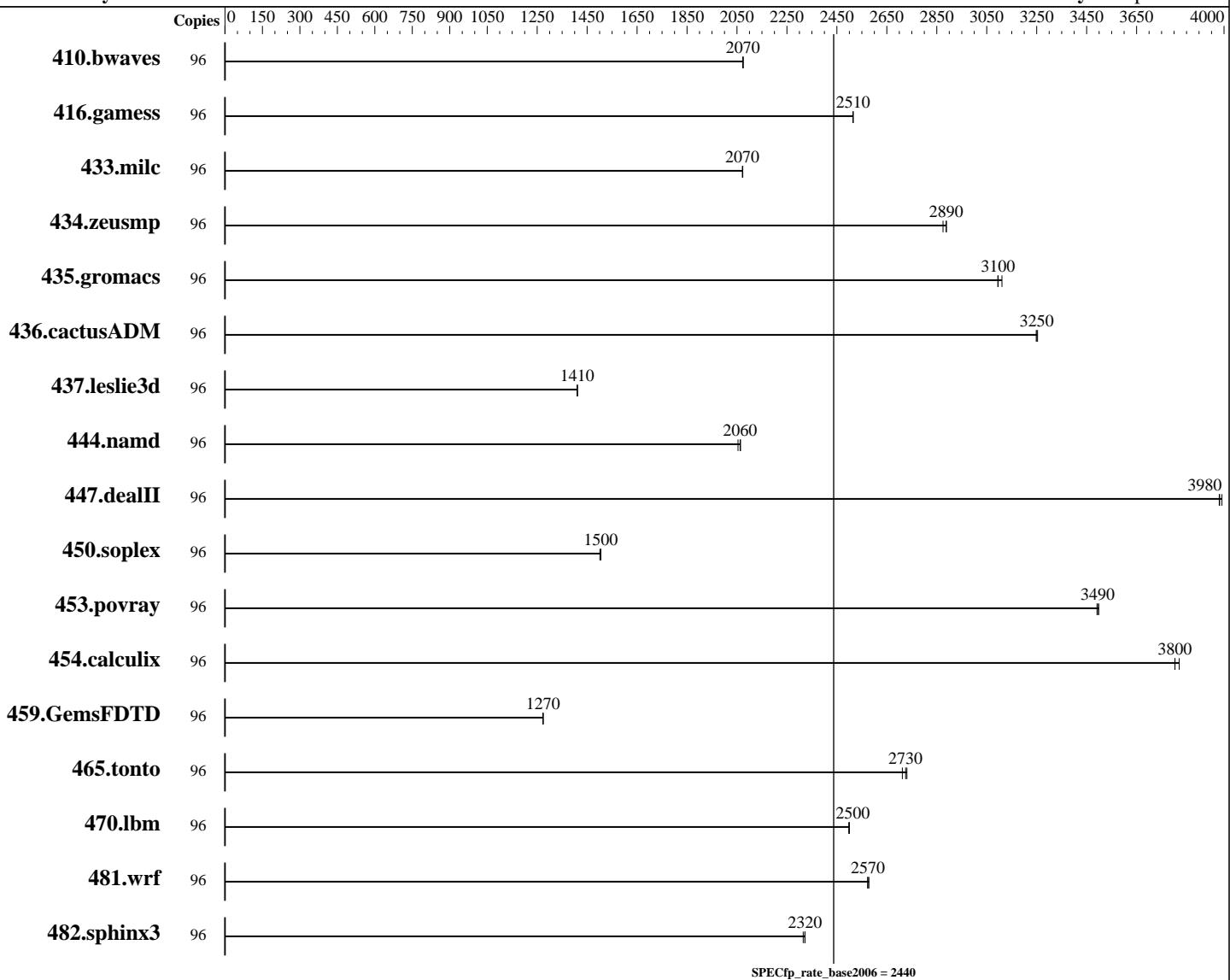
Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6146
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2, 4 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Compiler: Kernel 4.4.21-69-default
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL560 Gen10
(3.20 GHz, Intel Xeon Gold 6146)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 2 x 480 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	629	2070	629	2070	629	2070							
416.gamess	96	747	2520	747	2510	747	2510							
433.milc	96	425	2070	425	2070	425	2070							
434.zeusmp	96	302	2890	303	2890	304	2870							
435.gromacs	96	222	3090	221	3100	220	3110							
436.cactusADM	96	353	3250	353	3250	353	3250							
437.leslie3d	96	640	1410	640	1410	640	1410							
444.namd	96	373	2060	375	2050	373	2060							
447.dealII	96	276	3980	275	3990	276	3980							
450.soplex	96	533	1500	532	1510	532	1500							
453.povray	96	146	3500	146	3490	146	3490							
454.calculix	96	207	3820	208	3800	208	3800							
459.GemsFDTD	96	800	1270	799	1270	800	1270							
465.tonto	96	347	2730	346	2730	348	2710							
470.lbm	96	528	2500	528	2500	528	2500							
481.wrf	96	417	2570	416	2580	417	2570							
482.sphinx3	96	808	2320	806	2320	806	2320							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

irqbalance disabled with "service irqbalance stop"

tuned profile set with "tuned-adm profile throughput-performance"

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.20 GHz, Intel Xeon Gold 6146)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel numa_balancing"

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Stale A to S set to Enabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-e6az Mon Dec 4 20:18:46 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
4 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 9 10 16 18 19 25 26
physical 1: cores 0 1 2 3 4 8 9 11 17 18 19 20
physical 2: cores 0 1 2 3 8 9 10 11 18 19 24 27
physical 3: cores 0 1 2 3 4 8 9 11 17 18 19 20
cache size : 25344 KB

From /proc/meminfo
MemTotal: 792278428 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
This file is deprecated and will be removed in a future service pack or release.

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.20 GHz, Intel Xeon Gold 6146)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Platform Notes (Continued)

```
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-e6az 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 4 16:03

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda4        xfs   852G  126G  726G  15% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017
Memory:
 48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.20 GHz, Intel Xeon Gold 6146)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(3.20 GHz, Intel Xeon Gold 6146)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 2440

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.

For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jan 16 12:09:28 2018 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 14 January 2018.