



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

SPECfp<sup>®</sup>2006 = 152

SPECfp\_base2006 = 146

CPU2006 license: 9017

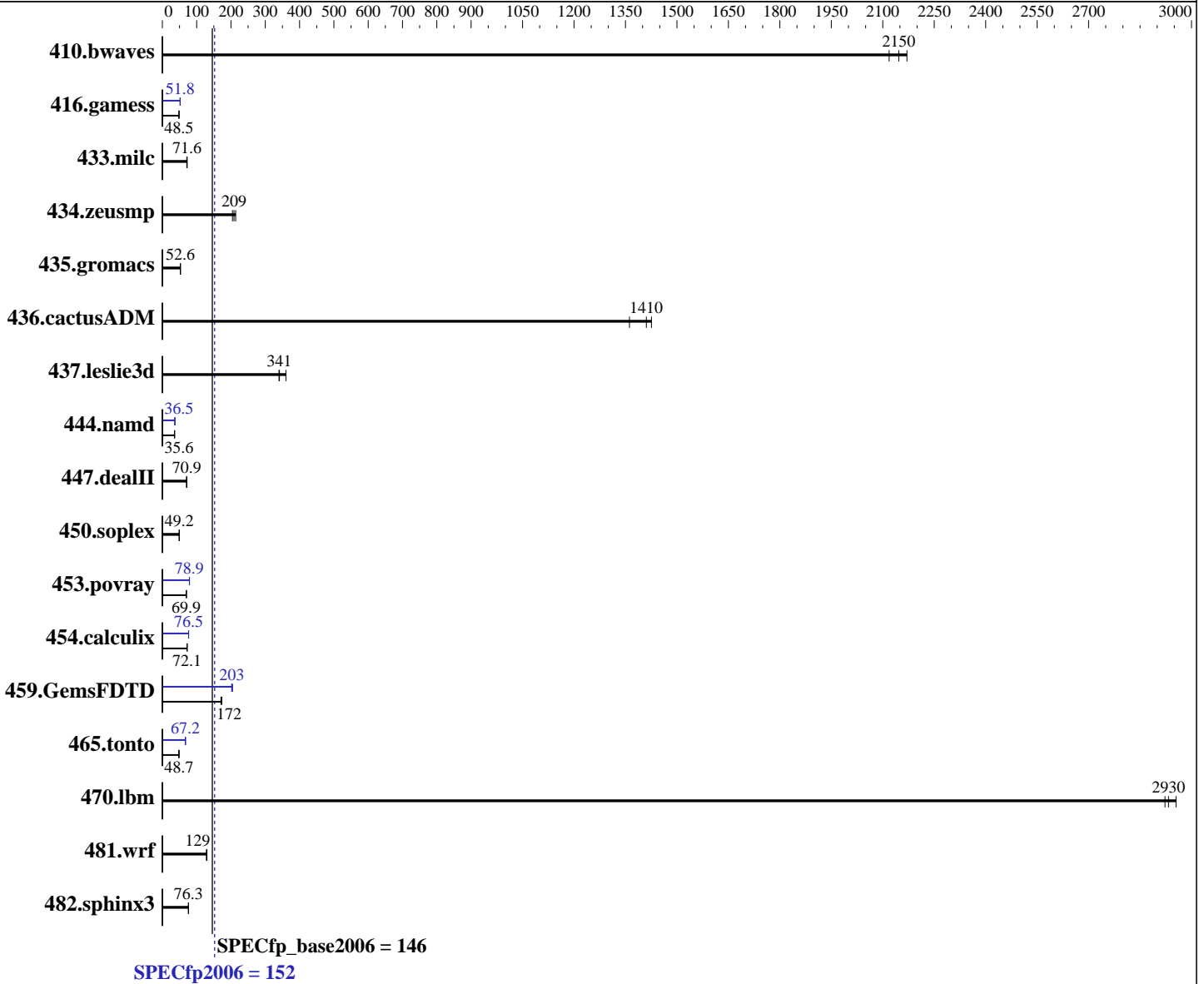
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Jul-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon Gold 6142  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECfp2006 = **152**

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

SPECfp\_base2006 = **146**

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

L3 Cache: 22 MB I+D on chip per chip  
Other Cache: None  
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 960 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	6.41	2120	6.26	2170	<b><u>6.33</u></b>	<b><u>2150</u></b>	6.41	2120	6.26	2170	<b><u>6.33</u></b>	<b><u>2150</u></b>
416.gamess	<b><u>404</u></b>	<b><u>48.5</u></b>	403	48.5	404	48.5	<b><u>378</u></b>	<b><u>51.8</u></b>	377	51.9	378	51.8
433.milc	127	72.4	<b><u>128</u></b>	<b><u>71.6</u></b>	129	71.2	127	72.4	<b><u>128</u></b>	<b><u>71.6</u></b>	129	71.2
434.zeusmp	<b><u>43.6</u></b>	<b><u>209</u></b>	44.5	204	42.6	214	<b><u>43.6</u></b>	<b><u>209</u></b>	44.5	204	42.6	214
435.gromacs	135	52.7	<b><u>136</u></b>	<b><u>52.6</u></b>	136	52.4	135	52.7	<b><u>136</u></b>	<b><u>52.6</u></b>	136	52.4
436.cactusADM	8.78	1360	<b><u>8.47</u></b>	<b><u>1410</u></b>	8.38	1430	8.78	1360	<b><u>8.47</u></b>	<b><u>1410</u></b>	8.38	1430
437.leslie3d	27.7	340	26.1	360	<b><u>27.5</u></b>	<b><u>341</u></b>	27.7	340	26.1	360	<b><u>27.5</u></b>	<b><u>341</u></b>
444.namd	225	35.6	<b><u>225</u></b>	<b><u>35.6</u></b>	225	35.6	<b><u>220</u></b>	<b><u>36.5</u></b>	220	36.5	220	36.5
447.dealII	162	70.8	161	71.0	<b><u>161</u></b>	<b><u>70.9</u></b>	162	70.8	161	71.0	<b><u>161</u></b>	<b><u>70.9</u></b>
450.soplex	168	49.7	<b><u>170</u></b>	<b><u>49.2</u></b>	171	48.9	168	49.7	<b><u>170</u></b>	<b><u>49.2</u></b>	171	48.9
453.povray	75.9	70.1	<b><u>76.1</u></b>	<b><u>69.9</u></b>	76.2	69.8	<b><u>67.5</u></b>	<b><u>78.9</u></b>	67.1	79.3	67.6	78.7
454.calculix	114	72.3	<b><u>114</u></b>	<b><u>72.1</u></b>	115	72.0	<b><u>108</u></b>	<b><u>76.5</u></b>	108	76.7	108	76.4
459.GemsFDTD	62.1	171	61.2	173	<b><u>61.6</u></b>	<b><u>172</u></b>	51.8	205	<b><u>52.3</u></b>	<b><u>203</u></b>	52.5	202
465.tonto	202	48.7	<b><u>202</u></b>	<b><u>48.7</u></b>	207	47.6	146	67.2	<b><u>146</u></b>	<b><u>67.2</u></b>	146	67.4
470.lbm	4.65	2950	4.70	2920	<b><u>4.68</u></b>	<b><u>2930</u></b>	4.65	2950	4.70	2920	<b><u>4.68</u></b>	<b><u>2930</u></b>
481.wrf	<b><u>86.6</u></b>	<b><u>129</u></b>	85.9	130	86.8	129	<b><u>86.6</u></b>	<b><u>129</u></b>	85.9	130	86.8	129
482.sphinx3	257	75.8	<b><u>255</u></b>	<b><u>76.3</u></b>	255	76.4	257	75.8	<b><u>255</u></b>	<b><u>76.3</u></b>	255	76.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
Hyper-Threading set to Disable  
Per Core P-state set to Disable  
DCA set to Disable  
Patrol Scrub set to Disable  
LLC dead line alloc set to Disable  
Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on SN850-01 Mon Jul 31 11:06:32 2017

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

**SPECfp2006 = 152**

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

**SPECfp\_base2006 = 146**

**CPU2006 license:** 9017

**Test date:** Jul-2017

**Test sponsor:** Lenovo Global Technology

**Hardware Availability:** Aug-2017

**Tested by:** Lenovo Global Technology

**Software Availability:** Nov-2016

### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
 4 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 16
  siblings     : 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size     : 22528 KB

```

From /proc/meminfo

```

MemTotal:      1584967192 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

uname -a:

```

Linux SN850-01 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 31 11:05

SPEC is set to: /home/cpu2006-1.2-ic17.0

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   836G  292G  545G  35% /home

```

Additional information from dmidecode:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 152

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

SPECfp\_base2006 = 146

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE109A-1.00]- 04/27/2017

Memory:

48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

OMP\_NUM\_THREADS = "64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 152

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

SPECfp\_base2006 = 146

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

## Base Portability Flags (Continued)

```

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

SPECfp2006 = 152

SPECfp\_base2006 = 146

CPU2006 license: 9017

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Jul-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SN850  
(2.60 GHz, Intel Xeon Gold 6142)

**SPECfp2006 = 152**

**SPECfp\_base2006 = 146**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test date:** Jul-2017

**Hardware Availability:** Aug-2017

**Software Availability:** Nov-2016

## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Oct 4 12:40:09 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 October 2017.