



SPEC® CFP2006 Result

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Lenovo Global Technology

ThinkSystem SN850
(2.00 GHz, Intel Xeon Platinum 8164)

SPECfp®2006 = 147

SPECfp_base2006 = 139

CPU2006 license: 9017

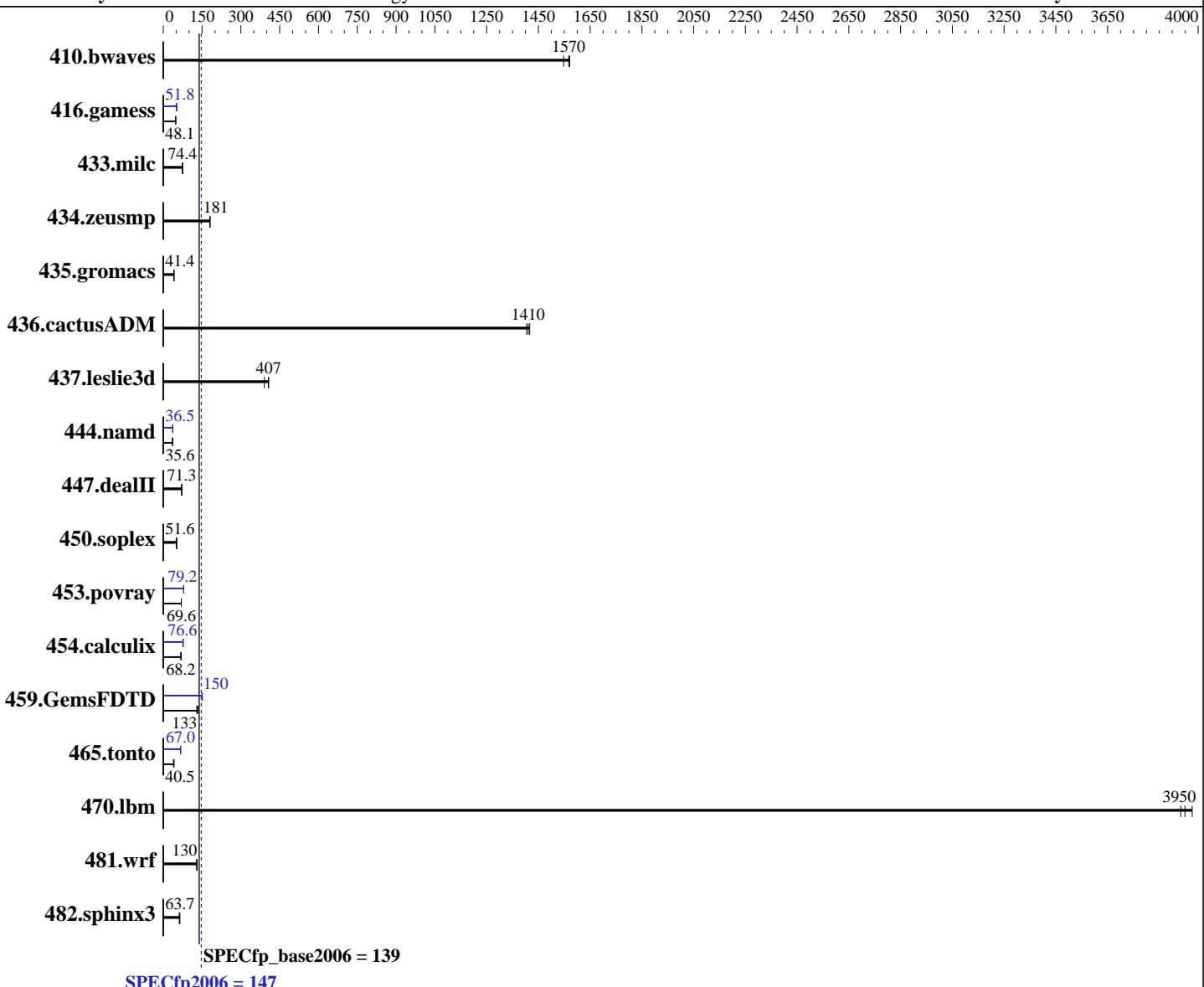
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Aug-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



Hardware

CPU Name: Intel Xeon Platinum 8164
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 104 cores, 4 chips, 26 cores/chip
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64)
Compiler: Kernel 4.4.21-69-default
C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;
Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache: 35.75 MB I+D on chip per chip
Other Cache: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	8.78	1550	8.67	1570	8.65	1570	8.78	1550	8.67	1570	8.65	1570
416.gamess	408	48.0	407	48.1	405	48.3	378	51.8	378	51.8	378	51.8
433.milc	124	73.8	123	74.4	122	75.0	124	73.8	123	74.4	122	75.0
434.zeusmp	50.3	181	50.3	181	50.6	180	50.3	181	50.3	181	50.6	180
435.gromacs	172	41.5	173	41.4	174	41.0	172	41.5	173	41.4	174	41.0
436.cactusADM	8.51	1400	8.44	1420	8.47	1410	8.51	1400	8.44	1420	8.47	1410
437.leslie3d	23.1	407	24.0	391	23.1	407	23.1	407	24.0	391	23.1	407
444.namd	225	35.6	225	35.6	225	35.6	220	36.5	220	36.5	220	36.5
447.dealII	160	71.5	160	71.3	161	71.2	160	71.5	160	71.3	161	71.2
450.soplex	162	51.6	162	51.6	162	51.6	162	51.6	162	51.6	162	51.6
453.povray	76.5	69.6	76.4	69.6	76.3	69.8	67.2	79.2	67.2	79.2	67.8	78.5
454.calculix	121	68.4	121	68.2	121	68.2	108	76.6	107	76.9	108	76.6
459.GemsFDTD	79.7	133	79.7	133	82.0	129	70.7	150	70.6	150	70.2	151
465.tonto	249	39.5	235	41.9	243	40.5	147	67.0	147	67.0	147	67.0
470.lbm	3.49	3930	3.48	3950	3.45	3980	3.49	3930	3.48	3950	3.45	3980
481.wrf	86.7	129	86.0	130	85.3	131	86.7	129	86.0	130	85.3	131
482.sphinx3	305	64.0	306	63.7	315	61.8	305	64.0	306	63.7	315	61.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

Hyper-Threading set to Disable

Per Core P-state set to Disable

DCA set to Disable

Patrol Scrub set to Disable

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on SN850-01 Wed Aug 2 13:58:38 2017

Continued on next page



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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
        4 "physical id"s (chips)
        104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 26
    siblings : 26
    physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
    26 27 28 29
    physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
    26 27 28 29
    physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
    26 27 28 29
    physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
    26 27 28 29
cache size : 36608 KB
```

```
From /proc/meminfo
MemTotal:      1584966120 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux SN850-01 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 2 13:57
```

Continued on next page



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Platform Notes (Continued)

```
SPEC is set to: /home/cpu2006-1.2-ic17.0
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda4        xfs   836G  292G  545G  35%  /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Lenovo -[IVE109A-1.00]- 04/27/2017
Memory:
 48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"
OMP_NUM_THREADS = "104"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

```
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Continued on next page



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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.xml>

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