



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECfp<sup>®</sup>2006 = **109**

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

SPECfp\_base2006 = **103**

CPU2006 license: 9017

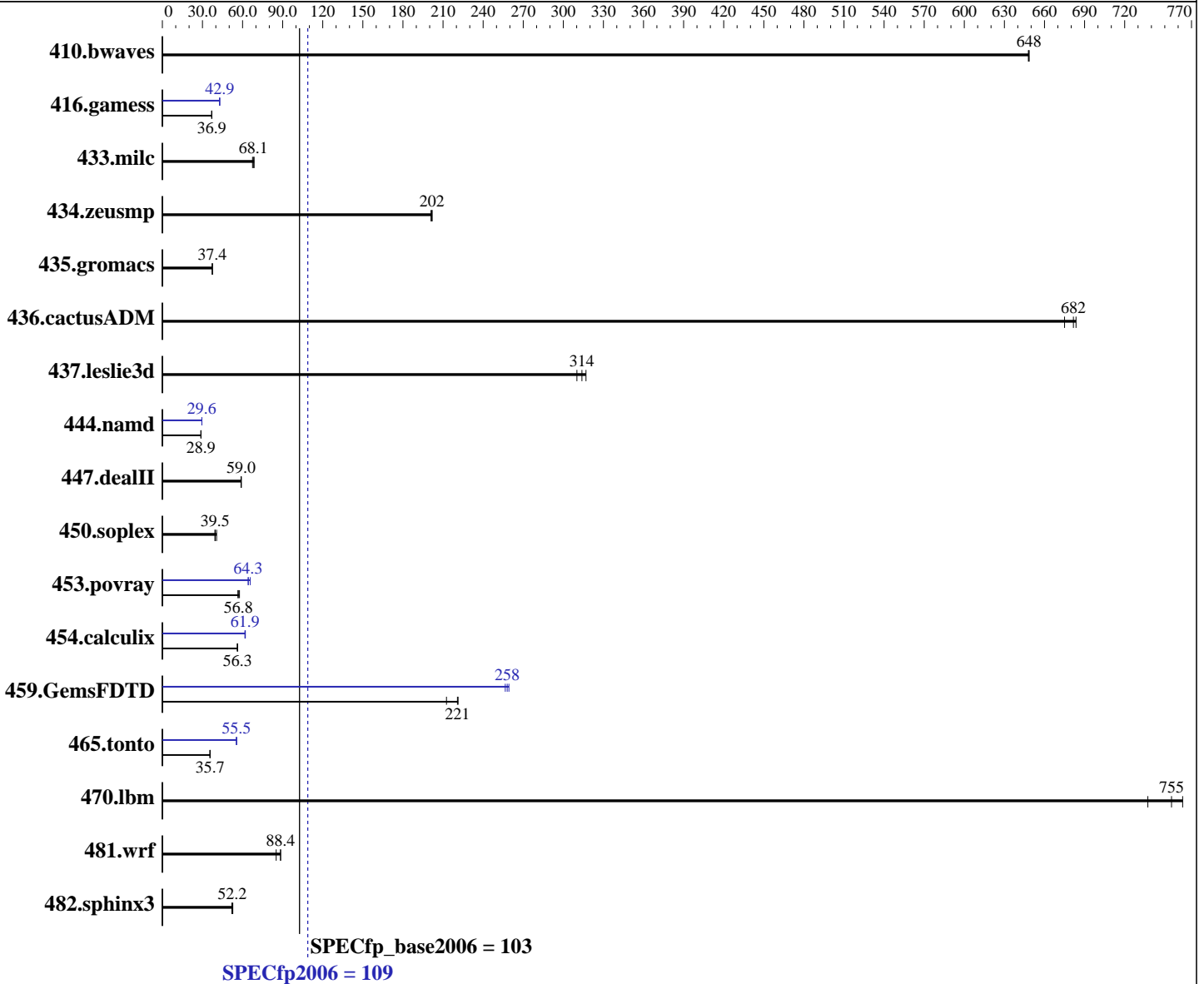
Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon Silver 4108  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECfp2006 = **109**

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

SPECfp\_base2006 = **103**

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

L3 Cache: 11 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	21.0	648	21.0	648	<b>21.0</b>	<b>648</b>	21.0	648	21.0	648	<b>21.0</b>	<b>648</b>
416.gamess	529	37.0	<b>530</b>	<b>36.9</b>	530	36.9	<b>456</b>	<b>42.9</b>	456	42.9	457	42.8
433.milc	134	68.7	136	67.5	<b>135</b>	<b>68.1</b>	134	68.7	136	67.5	<b>135</b>	<b>68.1</b>
434.zeusmp	<b>45.1</b>	<b>202</b>	45.1	202	45.3	201	<b>45.1</b>	<b>202</b>	45.1	202	45.3	201
435.gromacs	<b>191</b>	<b>37.4</b>	191	37.4	191	37.4	<b>191</b>	<b>37.4</b>	191	37.4	191	37.4
436.cactusADM	17.7	675	<b>17.5</b>	<b>682</b>	17.5	684	17.7	675	<b>17.5</b>	<b>682</b>	17.5	684
437.leslie3d	29.7	317	30.3	310	<b>29.9</b>	<b>314</b>	29.7	317	30.3	310	<b>29.9</b>	<b>314</b>
444.namd	278	28.9	278	28.9	<b>278</b>	<b>28.9</b>	271	29.6	271	29.6	<b>271</b>	<b>29.6</b>
447.dealII	194	59.1	<b>194</b>	<b>59.0</b>	194	58.8	194	59.1	<b>194</b>	<b>59.0</b>	194	58.8
450.soplex	<b>211</b>	<b>39.5</b>	206	40.6	212	39.4	<b>211</b>	<b>39.5</b>	206	40.6	212	39.4
453.povray	94.1	56.5	92.2	57.7	<b>93.6</b>	<b>56.8</b>	<b>82.8</b>	<b>64.3</b>	82.8	64.3	80.9	65.8
454.calculix	<b>147</b>	<b>56.3</b>	146	56.4	147	56.0	<b>133</b>	<b>61.9</b>	133	62.1	133	61.8
459.GemsFDTD	48.0	221	<b>48.1</b>	<b>221</b>	49.9	213	40.9	259	<b>41.1</b>	<b>258</b>	41.4	257
465.tonto	<b>276</b>	<b>35.7</b>	276	35.7	276	35.7	<b>177</b>	<b>55.5</b>	178	55.4	177	55.5
470.lbm	<b>18.2</b>	<b>755</b>	18.6	737	18.0	763	<b>18.2</b>	<b>755</b>	18.6	737	18.0	763
481.wrf	126	88.5	131	85.2	<b>126</b>	<b>88.4</b>	126	88.5	131	85.2	<b>126</b>	<b>88.4</b>
482.sphinx3	<b>373</b>	<b>52.2</b>	370	52.7	375	52.0	<b>373</b>	<b>52.2</b>	370	52.7	375	52.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Operating Mode set to Maximum Performance  
Hyper-Threading set to Disabled  
Uncore Frequency Scaling set to Disable  
LLC dead line alloc set to Disable  
Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-g50d Sun Aug 13 18:23:20 2017

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

**SPECfp2006 = 109**

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

**SPECfp\_base2006 = 103**

**CPU2006 license:** 9017

**Test date:** Aug-2017

**Test sponsor:** Lenovo Global Technology

**Hardware Availability:** Aug-2017

**Tested by:** Lenovo Global Technology

**Software Availability:** Nov-2016

### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 8
siblings       : 8
physical 0:    cores 0 1 2 3 4 5 6 7
physical 1:    cores 0 1 2 3 4 5 6 7
cache size     : 11264 KB

```

```

From /proc/meminfo
MemTotal:      395883100 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-g50d 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 13 18:21

```

SPEC is set to: /home/cpu2006-1.2-ic17.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       btrfs 744G 106G 637G  15% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 109

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

SPECfp\_base2006 = 103

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE105Z-1.00]- 04/27/2017

Memory:

12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 109

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

SPECfp\_base2006 = 103

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

## Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

```

## Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 109

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

SPECfp\_base2006 = 103

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR550  
(1.80 GHz, Intel Xeon Silver 4108)

**SPECfp2006 = 109**

**SPECfp\_base2006 = 103**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test date:** Aug-2017

**Hardware Availability:** Aug-2017

**Software Availability:** Nov-2016

## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Sep 20 11:05:34 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 19 September 2017.