



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 156

Huawei CH121 V5 (Intel Xeon Gold 6136)

SPECfp_base2006 = 150

CPU2006 license: 3175

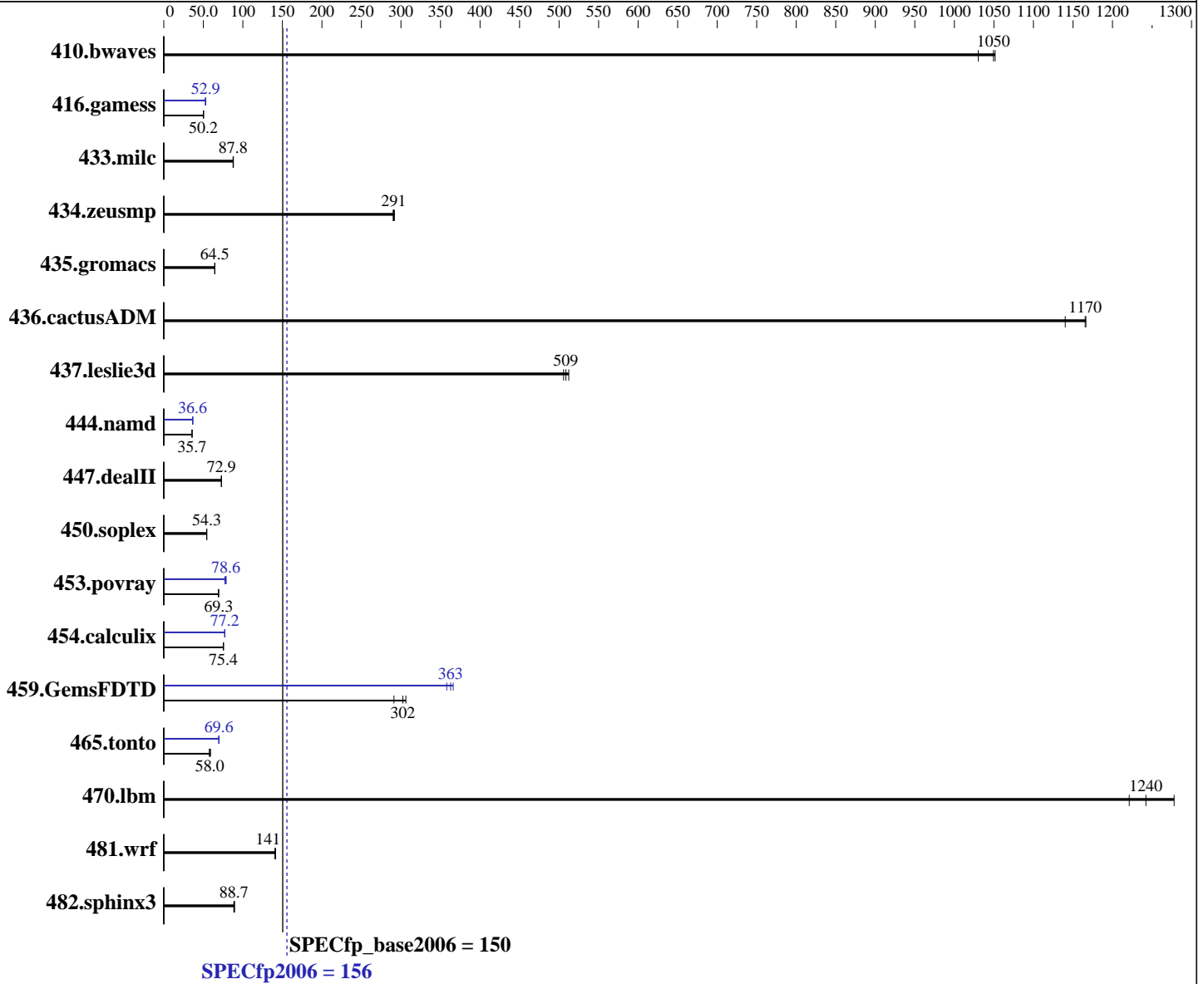
Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



Hardware

CPU Name: Intel Xeon Gold 6136
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 3.10.0-514.el7.x86_64
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs

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L3 Cache: 24.75 MB I+D on chip per chip
 Other Cache: None
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>12.9</u>	<u>1050</u>	13.2	1030	12.9	1050	<u>12.9</u>	<u>1050</u>	13.2	1030	12.9	1050
416.gamess	<u>390</u>	<u>50.2</u>	390	50.2	390	50.2	370	52.9	<u>370</u>	<u>52.9</u>	372	52.6
433.milc	<u>105</u>	<u>87.8</u>	105	87.6	104	88.1	<u>105</u>	<u>87.8</u>	105	87.6	104	88.1
434.zeusmp	31.4	290	31.2	292	<u>31.3</u>	<u>291</u>	31.4	290	31.2	292	<u>31.3</u>	<u>291</u>
435.gromacs	111	64.6	111	64.5	<u>111</u>	<u>64.5</u>	111	64.6	111	64.5	<u>111</u>	<u>64.5</u>
436.cactusADM	10.2	1170	<u>10.3</u>	<u>1170</u>	10.5	1140	10.2	1170	<u>10.3</u>	<u>1170</u>	10.5	1140
437.leslie3d	<u>18.5</u>	<u>509</u>	18.6	506	18.4	512	<u>18.5</u>	<u>509</u>	18.6	506	18.4	512
444.namd	<u>224</u>	<u>35.7</u>	224	35.7	224	35.8	219	36.6	219	36.6	<u>219</u>	<u>36.6</u>
447.dealII	157	73.0	<u>157</u>	<u>72.9</u>	157	72.8	157	73.0	<u>157</u>	<u>72.9</u>	157	72.8
450.soplex	<u>154</u>	<u>54.3</u>	153	54.4	154	54.2	<u>154</u>	<u>54.3</u>	153	54.4	154	54.2
453.povray	76.9	69.2	<u>76.8</u>	<u>69.3</u>	76.8	69.3	67.6	78.7	<u>67.7</u>	<u>78.6</u>	68.8	77.3
454.calculix	109	75.5	<u>109</u>	<u>75.4</u>	109	75.4	107	77.0	<u>107</u>	<u>77.2</u>	107	77.2
459.GemsFDTD	36.4	291	34.6	306	<u>35.1</u>	<u>302</u>	<u>29.2</u>	<u>363</u>	29.6	358	29.0	366
465.tonto	166	59.1	<u>170</u>	<u>58.0</u>	171	57.6	<u>141</u>	<u>69.6</u>	141	69.6	141	69.9
470.lbm	10.8	1280	11.3	1220	<u>11.1</u>	<u>1240</u>	10.8	1280	11.3	1220	<u>11.1</u>	<u>1240</u>
481.wrf	<u>79.3</u>	<u>141</u>	79.0	141	79.4	141	<u>79.3</u>	<u>141</u>	79.0	141	79.4	141
482.sphinx3	<u>220</u>	<u>88.7</u>	220	88.7	218	89.6	<u>220</u>	<u>88.7</u>	220	88.7	218	89.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Hyper-Threading to Disable
 Sysinfo program /spec17/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on localhost.localdomain Wed Aug 3 06:29:16 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
 2 "physical id"s (chips)
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 12
  physical 0: cores 0 1 2 3 4 8 9 11 17 18 19 20
  physical 1: cores 0 1 2 3 4 9 10 16 18 19 25 26
cache size : 25344 KB

```

```

From /proc/meminfo
MemTotal:      791256284 kB
HugePages_Total:      0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 2 10:15

```

SPEC is set to: /spec17
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   898G  15G  884G   2% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.13 04/11/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz
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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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For other inquiries, please contact webmaster@spec.org.

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