



# SPEC® CFP2006 Result

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## Lenovo Global Technology

ThinkSystem SR550  
(2.00 GHz, Intel Xeon Gold 6138)

SPECfp®2006 = 145

SPECfp\_base2006 = 137

CPU2006 license: 9017

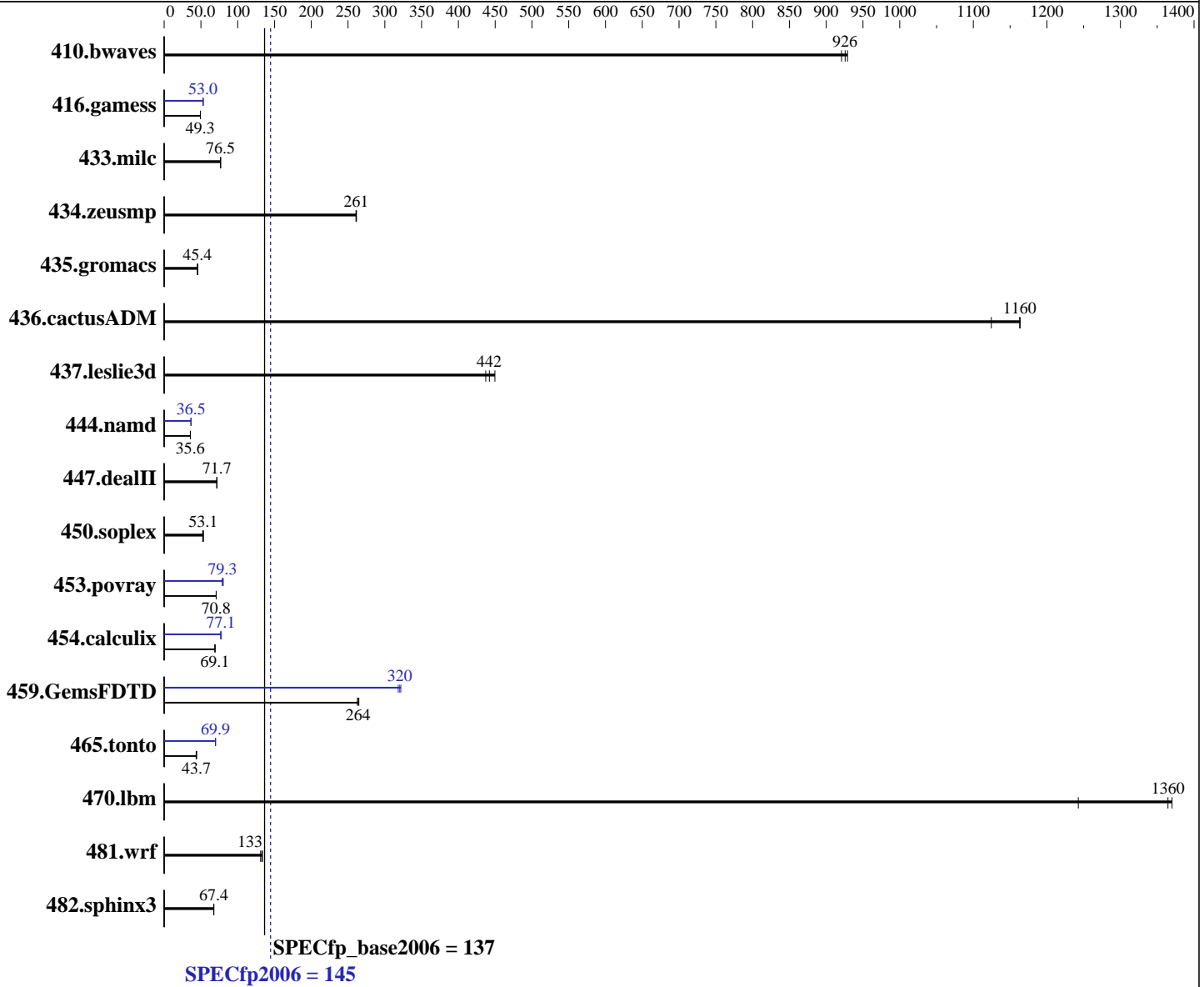
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Jun-2017

Hardware Availability: Aug-2017

Software Availability: Nov-2016



**Hardware**

CPU Name: Intel Xeon Gold 6138  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

*Continued on next page*

**Software**

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 27.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	14.8	921	14.6	929	<b>14.7</b>	<b>926</b>	14.8	921	14.6	929	<b>14.7</b>	<b>926</b>
416.gamess	397	49.3	<b>397</b>	<b>49.3</b>	398	49.2	369	53.0	369	53.0	<b>369</b>	<b>53.0</b>
433.milc	<b>120</b>	<b>76.5</b>	119	77.0	120	76.5	<b>120</b>	<b>76.5</b>	119	77.0	120	76.5
434.zeusmp	34.8	261	34.9	261	<b>34.8</b>	<b>261</b>	34.8	261	34.9	261	<b>34.8</b>	<b>261</b>
435.gromacs	157	45.3	<b>157</b>	<b>45.4</b>	157	45.6	157	45.3	<b>157</b>	<b>45.4</b>	157	45.6
436.cactusADM	10.3	1160	10.6	1120	<b>10.3</b>	<b>1160</b>	10.3	1160	10.6	1120	<b>10.3</b>	<b>1160</b>
437.leslie3d	20.9	450	21.5	437	<b>21.3</b>	<b>442</b>	20.9	450	21.5	437	<b>21.3</b>	<b>442</b>
444.namd	<b>225</b>	<b>35.6</b>	225	35.7	225	35.6	<b>220</b>	<b>36.5</b>	220	36.5	220	36.5
447.dealII	160	71.6	159	71.8	<b>160</b>	<b>71.7</b>	160	71.6	159	71.8	<b>160</b>	<b>71.7</b>
450.soplex	156	53.3	159	52.4	<b>157</b>	<b>53.1</b>	156	53.3	159	52.4	<b>157</b>	<b>53.1</b>
453.povray	<b>75.2</b>	<b>70.8</b>	75.2	70.7	75.1	70.8	<b>67.1</b>	<b>79.3</b>	66.2	80.4	67.4	78.9
454.calculix	<b>119</b>	<b>69.1</b>	119	69.2	120	69.0	<b>107</b>	<b>77.1</b>	107	77.4	107	76.8
459.GemsFDTD	40.4	263	<b>40.2</b>	<b>264</b>	40.1	265	33.3	318	33.0	322	<b>33.1</b>	<b>320</b>
465.tonto	226	43.5	<b>225</b>	<b>43.7</b>	223	44.2	141	69.7	141	69.9	<b>141</b>	<b>69.9</b>
470.lbm	<b>10.1</b>	<b>1360</b>	11.1	1240	10.0	1370	<b>10.1</b>	<b>1360</b>	11.1	1240	10.0	1370
481.wrf	83.6	134	<b>84.1</b>	<b>133</b>	85.2	131	83.6	134	<b>84.1</b>	<b>133</b>	85.2	131
482.sphinx3	289	67.4	<b>289</b>	<b>67.4</b>	288	67.7	289	67.4	<b>289</b>	<b>67.4</b>	288	67.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Operating Mode set to Maximum Performance  
Hyper-Threading set to Disabled  
Uncore Frequency Scaling set to disable  
LLC dead line alloc set to Disable  
Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-yyst Sun Jun 25 16:15:42 2017

This section contains SUT (System Under Test) info as seen by  
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### Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6138 CPU @ 2.00GHz
  2 "physical id"s (chips)
  40 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 20
    siblings  : 20
    physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
    physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  cache size : 28160 KB

```

```

From /proc/meminfo
MemTotal:      395881536 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-yyst 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 25 08:12

```

SPEC is set to: /home/cpu2006-1.2-ic17.0
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2       btrfs    744G  202G  541G   28% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE105Z-1.00]- 04/27/2017

Memory:

12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled by default.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

## Peak Compiler Invocation

C benchmarks:  
icc -m64  
C++ benchmarks:  
icpc -m64  
Fortran benchmarks:  
ifort -m64  
Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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