



# SPEC® CFP2006 Result

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## Cisco Systems

Cisco UCS C240 M4 (Intel Xeon E5-2699A v4, 2.40 GHz)

SPECfp®2006 = 127

SPECfp\_base2006 = 121

CPU2006 license: 9019

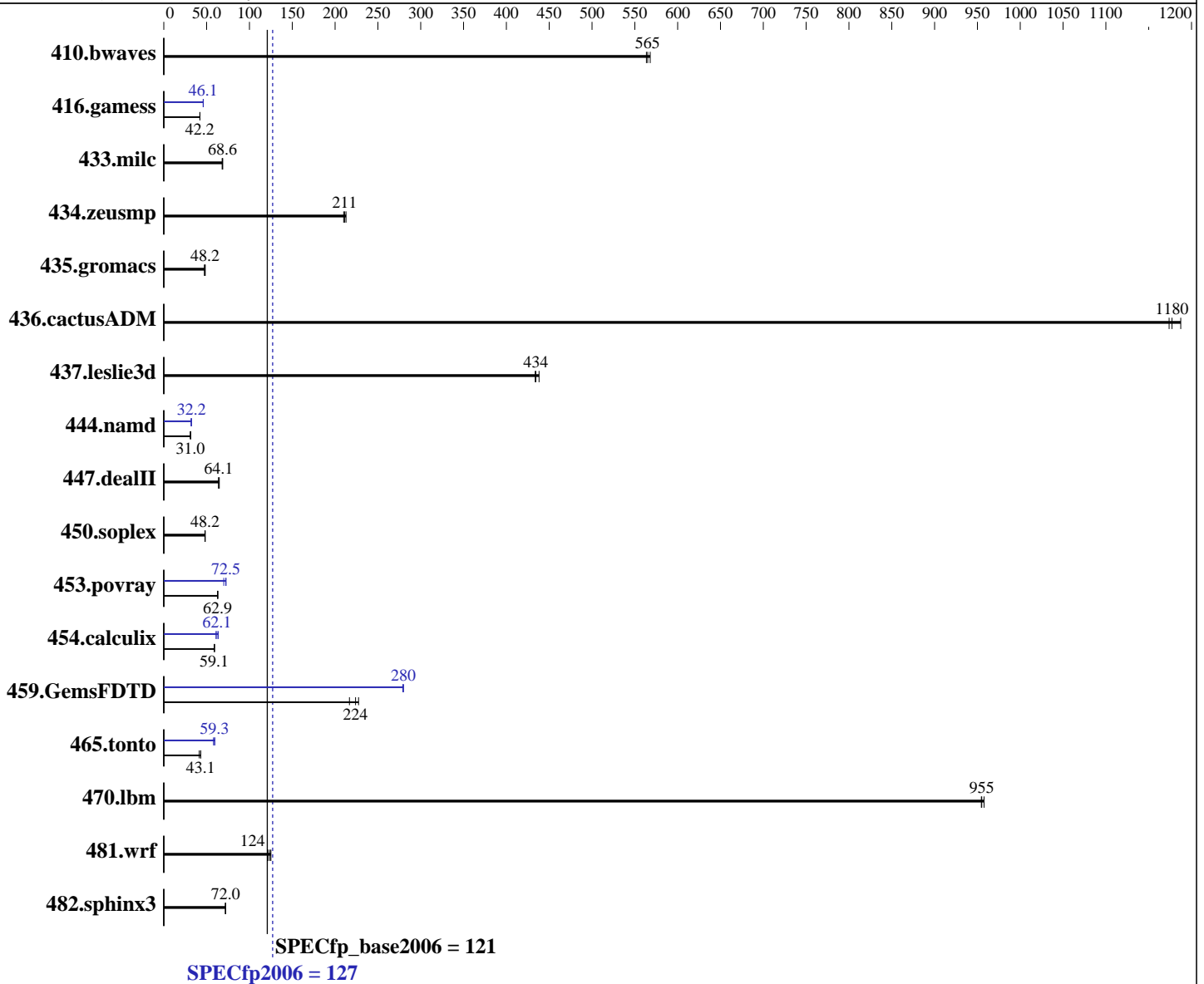
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Feb-2017

Hardware Availability: Apr-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2699A v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 3.12.49-11-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran  
 Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 5 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 1.2 TB SAS HDD 10K RPM  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24.1	563	<u>24.1</u>	<u>565</u>	23.9	568	24.1	563	<u>24.1</u>	<u>565</u>	23.9	568
416.gamess	<u>464</u>	<u>42.2</u>	465	42.1	463	42.2	426	45.9	<u>425</u>	<u>46.1</u>	424	46.1
433.milc	<u>134</u>	<u>68.6</u>	134	68.8	135	68.1	<u>134</u>	<u>68.6</u>	134	68.8	135	68.1
434.zeusmp	43.3	210	42.8	213	<u>43.1</u>	<u>211</u>	43.3	210	42.8	213	<u>43.1</u>	<u>211</u>
435.gromacs	<u>148</u>	<u>48.2</u>	151	47.3	148	48.3	<u>148</u>	<u>48.2</u>	151	47.3	148	48.3
436.cactusADM	10.1	1190	<u>10.2</u>	<u>1180</u>	10.2	1170	10.1	1190	<u>10.2</u>	<u>1180</u>	10.2	1170
437.leslie3d	<u>21.6</u>	<u>434</u>	21.7	434	21.5	438	<u>21.6</u>	<u>434</u>	21.7	434	21.5	438
444.namd	254	31.5	259	31.0	<u>258</u>	<u>31.0</u>	<u>249</u>	<u>32.2</u>	251	31.9	249	32.2
447.dealII	<u>179</u>	<u>64.1</u>	179	64.0	177	64.8	<u>179</u>	<u>64.1</u>	179	64.0	177	64.8
450.soplex	173	48.3	174	48.0	<u>173</u>	<u>48.2</u>	173	48.3	174	48.0	<u>173</u>	<u>48.2</u>
453.povray	<u>84.5</u>	<u>62.9</u>	84.3	63.1	84.7	62.8	76.0	70.0	73.3	72.6	<u>73.4</u>	<u>72.5</u>
454.calculix	139	59.2	<u>140</u>	<u>59.1</u>	140	59.0	<u>133</u>	<u>62.1</u>	130	63.7	135	60.9
459.GemsFDTD	48.9	217	46.7	227	<u>47.4</u>	<u>224</u>	37.9	280	38.0	279	<u>37.9</u>	<u>280</u>
465.tonto	238	41.4	<u>228</u>	<u>43.1</u>	228	43.2	165	59.5	170	58.0	<u>166</u>	<u>59.3</u>
470.lbm	14.3	958	<u>14.4</u>	<u>955</u>	14.4	955	14.3	958	<u>14.4</u>	<u>955</u>	14.4	955
481.wrf	<u>90.2</u>	<u>124</u>	89.3	125	91.7	122	<u>90.2</u>	<u>124</u>	89.3	125	91.7	122
482.sphinx3	270	72.1	271	71.9	<u>271</u>	<u>72.0</u>	270	72.1	271	71.9	<u>271</u>	<u>72.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### BIOS Settings:

Intel Hyper-Threading Technology option set to Disabled

CPU performance set to Enterprise

Power Technology set to Energy Efficient

Energy Performance BIAS setting set to Balanced Performance

Memory RAS configuration set to Maximum Performance

Memory Power Saving Mode set to Disabled

QPI Snoop Mode set to Home Directory Snoop with OSB

Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

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Hardware Availability: Apr-2016

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### Platform Notes (Continued)

running on linux-cd5x Sat Feb 4 05:48:01 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
 2 "physical id"s (chips)
 44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 22
  siblings  : 22
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
 28
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
 28
cache size : 56320 KB

```

From /proc/meminfo

```

MemTotal:      264565692 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12 SP1

From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP1"
  VERSION_ID="12.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

Linux linux-cd5x 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 5 Feb 3 07:16

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## Platform Notes (Continued)

SPEC is set to: /opt/cpu2006-1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	xfs	1.1T	26G	1.1T	3%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C240M4.2.0.13d.0.0812161132 08/12/2016

Memory:

16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh10.2"

OMP\_NUM\_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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