SPEC® CFP2006 Result

M Computers s.r.o.

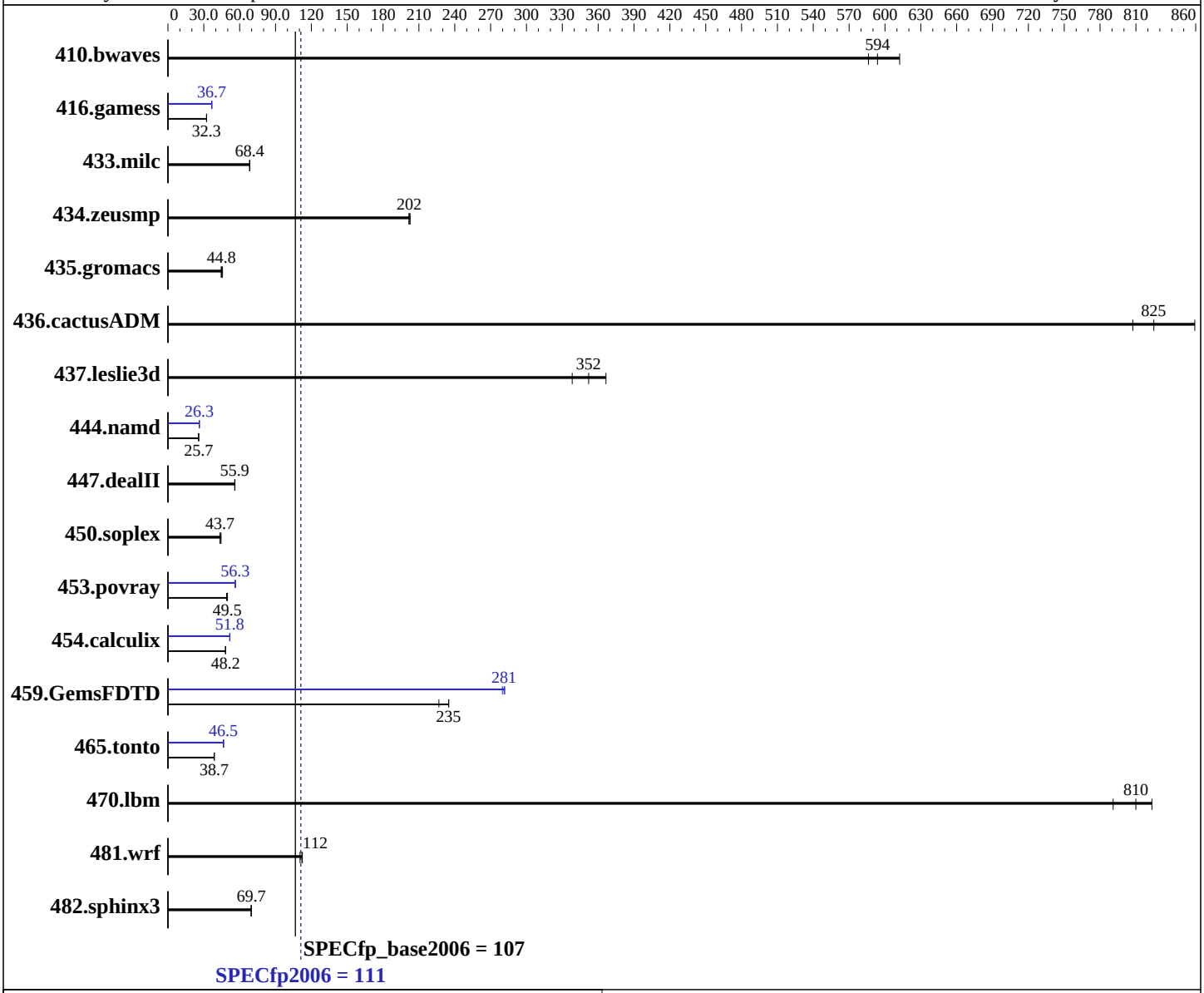
HPC S2600WT2R (Intel Xeon E5-2650 v4, 2.2 GHz)

SPECfp®2006 = 111

SPECfp_base2006 = 107

CPU2006 license: 4204
Test sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test date: Jan-2017
Hardware Availability: Mar-2016
Software Availability: Feb-2016



Hardware

CPU Name:	Intel Xeon E5-2650 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	CentOS 7.2 3.10.0-327.18.2.el7.x86_64
Compiler:	C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

Continued on next page

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2400 MHz)
Disk Subsystem: 1 x 300 GB SAS 15k
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.2	612	23.2	586	**22.9**	**594**	22.2	612	23.2	586	**22.9**	**594**
416.gamess	**606**	**32.3**	607	32.2	605	32.4	**534**	**36.7**	535	36.6	533	36.8
433.milc	**134**	**68.4**	134	68.3	134	68.6	**134**	**68.4**	134	68.3	134	68.6
434.zeusmp	44.9	203	45.1	202	**45.1**	**202**	44.9	203	45.1	202	**45.1**	**202**
435.gromacs	**159**	**44.8**	156	45.7	160	44.5	**159**	**44.8**	156	45.7	160	44.5
436.cactusADM	**14.5**	**825**	14.8	807	13.9	859	**14.5**	**825**	14.8	807	13.9	859
437.leslie3d	27.8	338	25.6	367	**26.7**	**352**	27.8	338	25.6	367	**26.7**	**352**
444.namd	312	25.7	**313**	**25.7**	313	25.7	306	26.2	305	26.3	**305**	**26.3**
447.dealII	205	55.9	**205**	**55.9**	204	56.1	205	55.9	**205**	**55.9**	204	56.1
450.soplex	191	43.6	**191**	**43.7**	188	44.4	191	43.6	**191**	**43.7**	188	44.4
453.povray	**107**	**49.5**	107	49.8	108	49.1	94.5	56.3	**94.4**	**56.3**	94.3	56.4
454.calculix	171	48.2	**171**	**48.2**	171	48.1	159	51.8	**159**	**51.8**	160	51.7
459.GemsFDTD	45.1	235	**45.2**	**235**	46.8	227	**37.7**	**281**	37.9	280	37.6	282
465.tonto	253	38.8	**254**	**38.7**	254	38.7	212	46.5	211	46.6	**212**	**46.5**
470.lbm	17.4	791	**17.0**	**810**	16.7	823	17.4	791	**17.0**	**810**	16.7	823
481.wrf	101	111	**99.6**	**112**	99.3	112	101	111	**99.6**	**112**	99.3	112
482.sphinx3	280	69.6	**280**	**69.7**	279	69.7	280	69.6	**280**	**69.7**	279	69.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Configuration:
CPU and Power Performance Policy = Performance
Set Fan Profile = Performance
Fan PWM Offset = 100
Intel(R) Hyper-Threading Tech = Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh:$/opt/intel/compilers_and_libraries_2016.2.181/linux/compiler/lib/intel64_lin"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 2x Intel Xeon E5-2650 v4 CPU + 256GB
 memory using CentOS 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/MComputers-Platform-Settings-V1.2-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/MComputers-Platform-Settings-V1.2-revB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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