



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = **110**

Huawei XH321 V3 (Intel Xeon E5-2637 v4)

SPECfp_base2006 = **107**

CPU2006 license: 3175

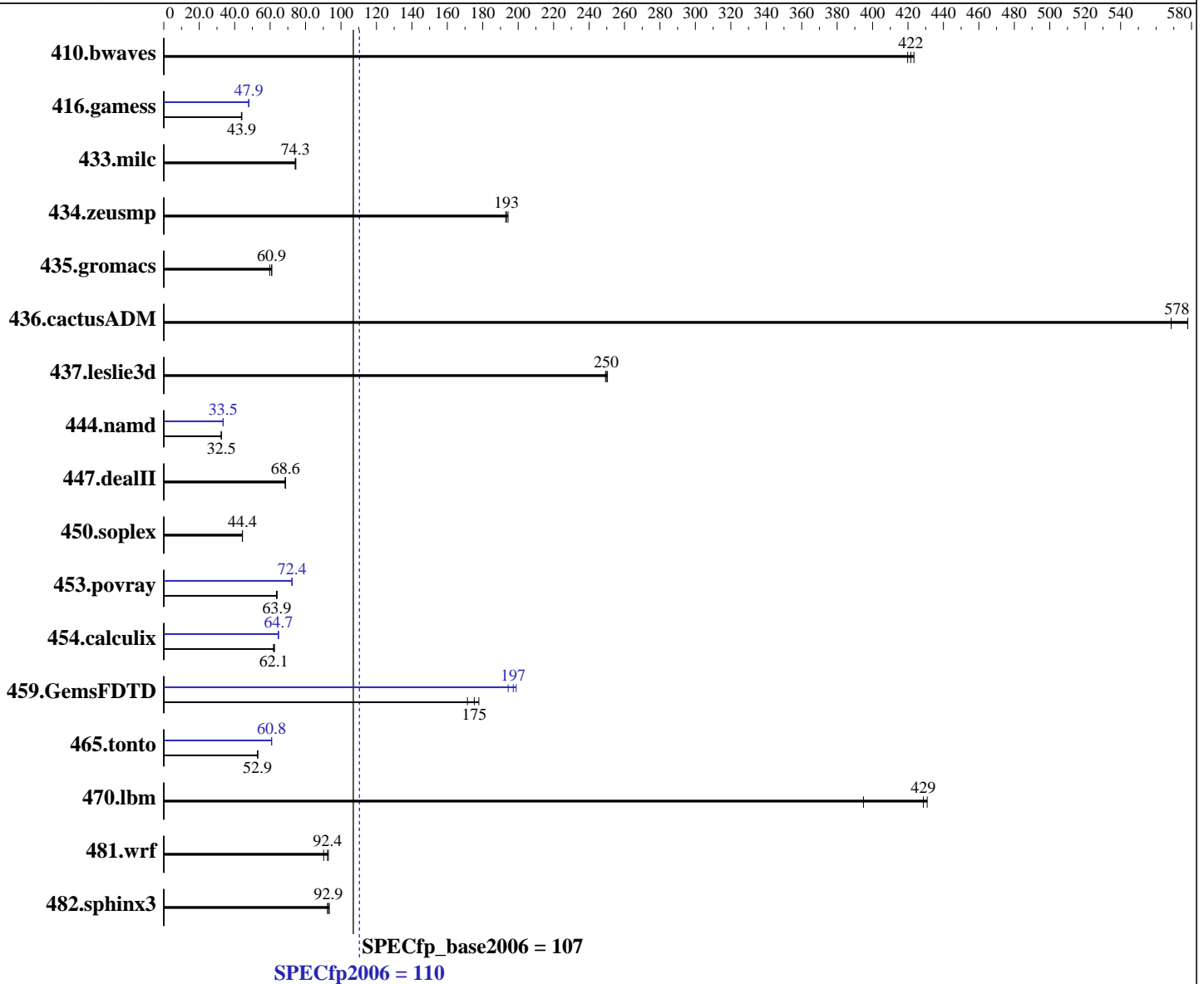
Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2637 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx8 PC4-2400T-R)
 Disk Subsystem: 1 x 200 GB SATA SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>32.2</u>	<u>422</u>	32.4	420	32.1	423	<u>32.2</u>	<u>422</u>	32.4	420	32.1	423
416.gamess	<u>446</u>	<u>43.9</u>	445	44.0	447	43.8	408	47.9	408	48.0	<u>408</u>	<u>47.9</u>
433.milc	124	74.2	<u>124</u>	<u>74.3</u>	123	74.5	124	74.2	<u>124</u>	<u>74.3</u>	123	74.5
434.zeusmp	46.8	194	<u>47.1</u>	<u>193</u>	47.2	193	46.8	194	<u>47.1</u>	<u>193</u>	47.2	193
435.gromacs	117	60.9	<u>117</u>	<u>60.9</u>	119	59.8	117	60.9	<u>117</u>	<u>60.9</u>	119	59.8
436.cactusADM	<u>20.7</u>	<u>578</u>	20.7	578	21.0	568	<u>20.7</u>	<u>578</u>	20.7	578	21.0	568
437.leslie3d	37.7	249	<u>37.6</u>	<u>250</u>	37.5	250	37.7	249	<u>37.6</u>	<u>250</u>	37.5	250
444.namd	247	32.4	<u>247</u>	<u>32.5</u>	247	32.5	239	33.5	239	33.5	<u>239</u>	<u>33.5</u>
447.dealII	<u>167</u>	<u>68.6</u>	167	68.5	167	68.7	<u>167</u>	<u>68.6</u>	167	68.5	167	68.7
450.soplex	188	44.4	188	44.3	<u>188</u>	<u>44.4</u>	188	44.4	188	44.3	<u>188</u>	<u>44.4</u>
453.povray	<u>83.3</u>	<u>63.9</u>	83.7	63.6	83.2	63.9	73.5	72.4	73.6	72.3	<u>73.5</u>	<u>72.4</u>
454.calculix	<u>133</u>	<u>62.1</u>	132	62.5	133	62.0	127	64.7	128	64.6	<u>128</u>	<u>64.7</u>
459.GemsFDTD	<u>60.6</u>	<u>175</u>	59.7	178	61.9	171	53.4	199	<u>53.8</u>	<u>197</u>	54.6	194
465.tonto	<u>186</u>	<u>52.9</u>	185	53.0	186	52.9	162	60.8	<u>162</u>	<u>60.8</u>	162	60.9
470.lbm	31.9	431	34.8	395	<u>32.1</u>	<u>429</u>	31.9	431	34.8	395	<u>32.1</u>	<u>429</u>
481.wrf	124	90.2	<u>121</u>	<u>92.4</u>	120	92.9	124	90.2	<u>121</u>	<u>92.4</u>	120	92.9
482.sphinx3	<u>210</u>	<u>92.9</u>	211	92.4	209	93.4	<u>210</u>	<u>92.9</u>	211	92.4	209	93.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS mode
 Set Patrol Scrub to Disable
 Set Hyper-Threading to Disable
 Sysinfo program /spec16/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Thu Dec 29 05:52:22 2016

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2637 v4 @ 3.50GHz
    2 "physical id"s (chips)
    8 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 4
    siblings  : 4
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
  cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      65570444 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 29 05:49

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  99G   5.5G  88G   6% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

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Platform Notes (Continued)

Memory:

8x Hynix HMA41GR7AFR8N-UH 8 GB 2 rank 2400 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

Peak Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
 icpc -m64

Fortran benchmarks:
 ifort -m64

Benchmarks using both Fortran and C:
 icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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For other inquiries, please contact webmaster@spec.org.

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