



# SPEC® CFP2006 Result

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## Cisco Systems

Cisco UCS C240 M4 (Intel Xeon E5-2680 v4 2.40 GHz)

**SPECfp®2006 = 120**

**SPECfp\_base2006 = 114**

CPU2006 license: 9019

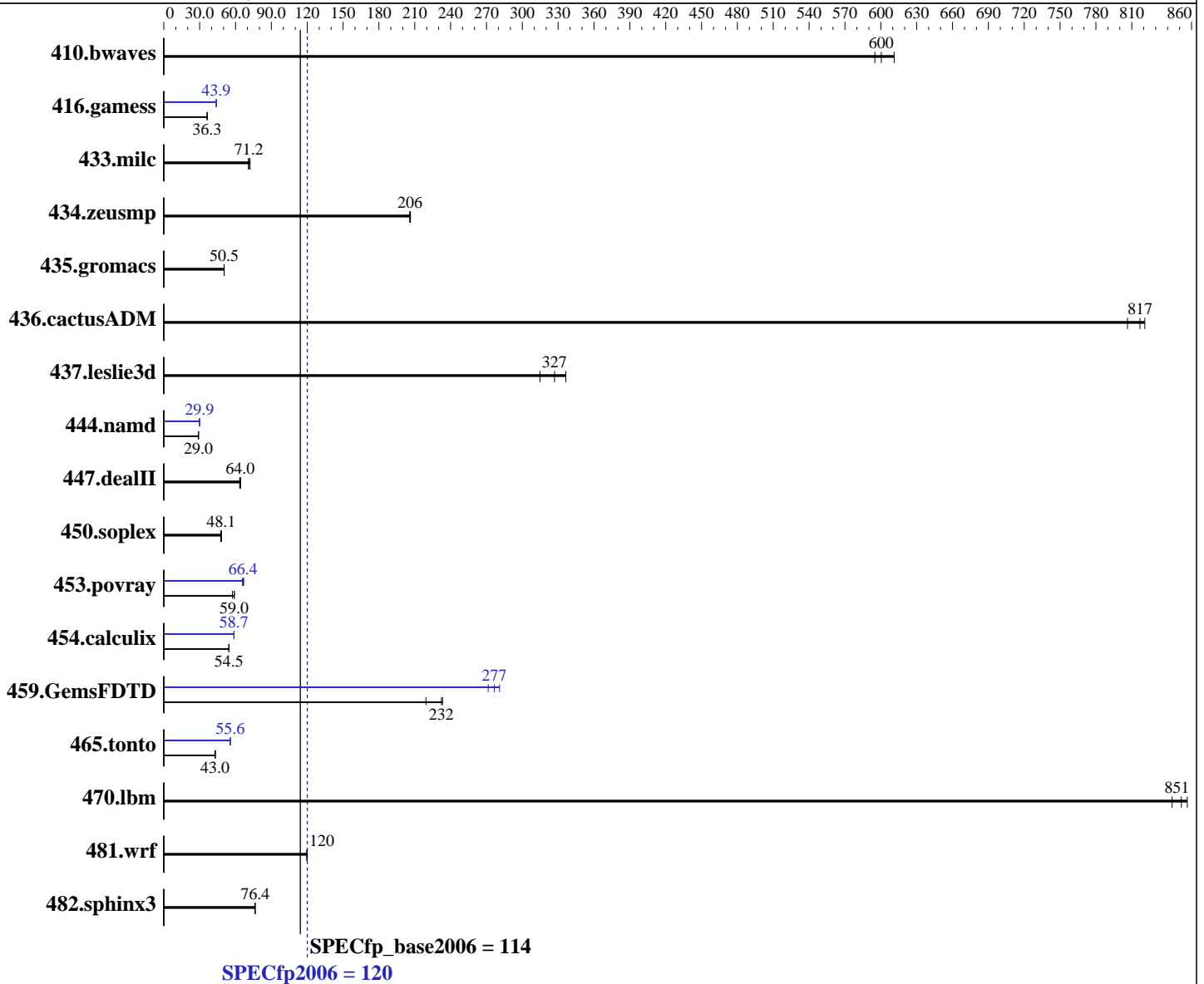
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Nov-2016

Hardware Availability: Apr-2016

Software Availability: Dec-2015



**Hardware**

CPU Name: Intel Xeon E5-2680 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64) 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 400 GB SAS SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>22.6</u>	<u>600</u>	22.2	611	22.8	595	<u>22.6</u>	<u>600</u>	22.2	611	22.8	595
416.gamess	538	36.4	540	36.2	<u>540</u>	<u>36.3</u>	445	44.0	<u>446</u>	<u>43.9</u>	446	43.9
433.milc	130	70.8	<u>129</u>	<u>71.2</u>	127	72.2	130	70.8	<u>129</u>	<u>71.2</u>	127	72.2
434.zeusmp	44.2	206	44.1	206	<u>44.2</u>	<u>206</u>	44.2	206	44.1	206	<u>44.2</u>	<u>206</u>
435.gromacs	141	50.5	141	50.5	<u>141</u>	<u>50.5</u>	141	50.5	141	50.5	<u>141</u>	<u>50.5</u>
436.cactusADM	14.6	821	<u>14.6</u>	<u>817</u>	14.8	806	14.6	821	<u>14.6</u>	<u>817</u>	14.8	806
437.leslie3d	<u>28.8</u>	<u>327</u>	29.9	315	27.9	336	<u>28.8</u>	<u>327</u>	29.9	315	27.9	336
444.namd	277	29.0	<u>277</u>	<u>29.0</u>	276	29.0	268	29.9	268	30.0	<u>268</u>	<u>29.9</u>
447.dealII	180	63.5	<u>179</u>	<u>64.0</u>	178	64.3	180	63.5	<u>179</u>	<u>64.0</u>	178	64.3
450.soplex	<u>173</u>	<u>48.1</u>	175	47.6	173	48.3	<u>173</u>	<u>48.1</u>	175	47.6	173	48.3
453.povray	90.2	59.0	92.6	57.4	<u>90.2</u>	<u>59.0</u>	<u>80.1</u>	<u>66.4</u>	80.9	65.8	79.5	66.9
454.calculix	152	54.4	<u>151</u>	<u>54.5</u>	151	54.6	140	58.8	<u>140</u>	<u>58.7</u>	141	58.7
459.GemsFDTD	48.4	219	45.5	233	<u>45.7</u>	<u>232</u>	<u>38.4</u>	<u>277</u>	39.1	272	37.8	281
465.tonto	<u>229</u>	<u>43.0</u>	229	42.9	228	43.2	177	55.7	<u>177</u>	<u>55.6</u>	177	55.6
470.lbm	16.3	844	<u>16.1</u>	<u>851</u>	16.0	856	16.3	844	<u>16.1</u>	<u>851</u>	16.0	856
481.wrf	<u>93.2</u>	<u>120</u>	93.3	120	93.1	120	<u>93.2</u>	<u>120</u>	93.3	120	93.1	120
482.sphinx3	256	76.2	<u>255</u>	<u>76.4</u>	255	76.5	<u>256</u>	<u>76.2</u>	<u>255</u>	<u>76.4</u>	255	76.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Intel Hyper-Threading Technology option set to Disabled  
CPU performance set to Enterprise  
Power Technology set to Energy Efficient  
Energy Performance BIAS setting set to Balanced Performance  
Memory RAS configuration set to Maximum Performance  
Memory Power Saving Mode set to Disabled  
QPI Snoop Mode set to Home Directory Snoop with OSB  
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

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Test sponsor: Cisco Systems

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Test date: Nov-2016

Hardware Availability: Apr-2016

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### Platform Notes (Continued)

running on linux-5aj7 Mon Nov 28 13:07:55 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E5-2680 v4@ 2.40GHz
 2 "physical id"s (chips)
 28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 14
  siblings     : 14
 physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
 physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
 cache size    : 35840 KB

```

From /proc/meminfo

```

MemTotal:      264568492 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

```

Linux linux-5aj7 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 28 12:52

SPEC is set to: /opt/cpu2006-1.2

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       xfs   372G  15G  358G  4% /

```

Additional information from dmidecode:

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C240M4.2.0.13d.0.0812161132 08/12/2016

Memory:

16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

OMP\_NUM\_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

```

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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**Hardware Availability:** Apr-2016

**Software Availability:** Dec-2015

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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## Peak Optimization Flags (Continued)

465.tonto (continued):

`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml>

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