



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **111**

Huawei RH1288 V3(Intel Xeon E5-2640 v4)

SPECfp\_base2006 = **105**

CPU2006 license: 3175

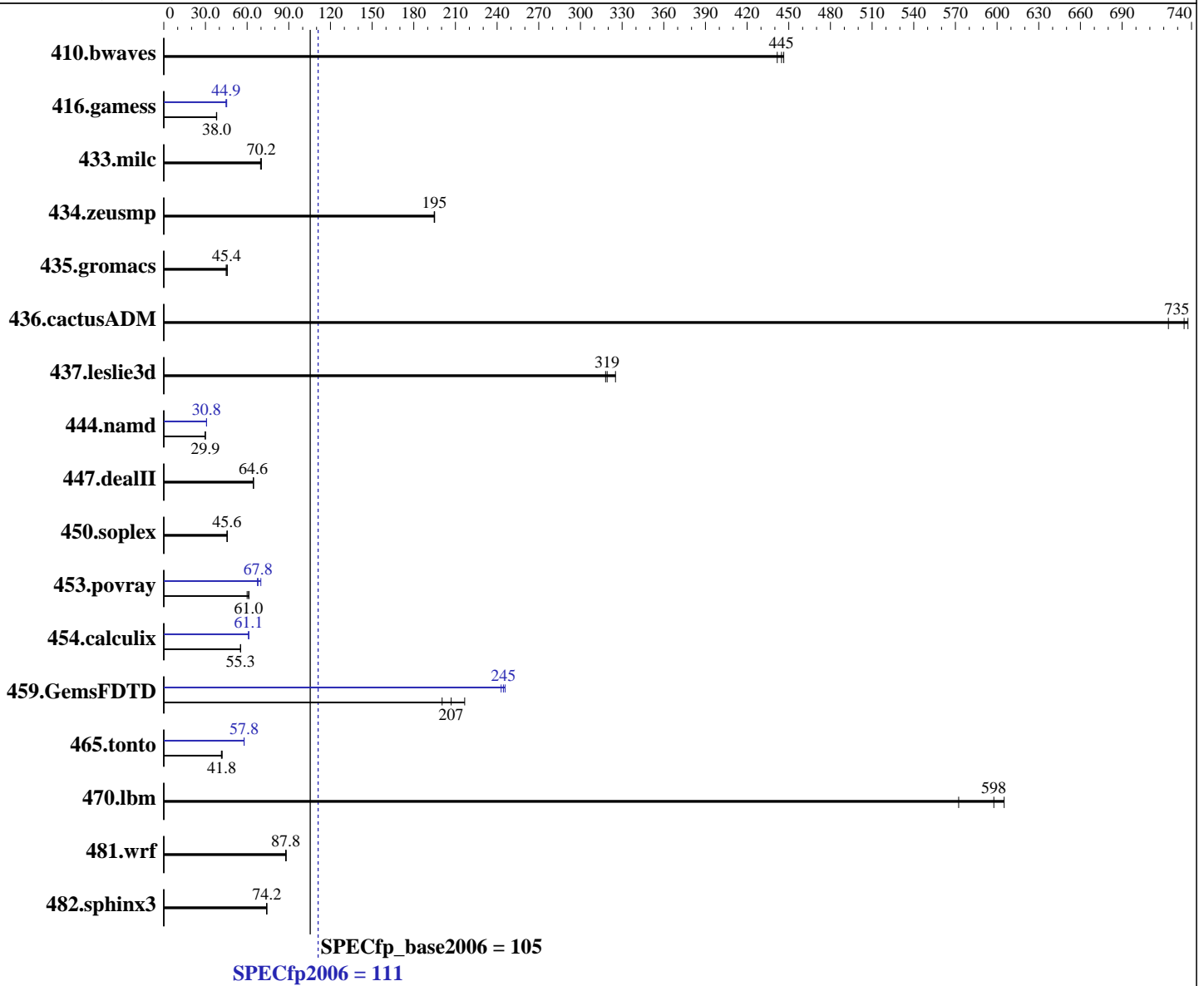
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



**Hardware**

CPU Name: Intel Xeon E5-2640 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	30.8	442	30.4	446	<b>30.6</b>	<b>445</b>	30.8	442	30.4	446	<b>30.6</b>	<b>445</b>
416.gamess	<b>515</b>	<b>38.0</b>	517	37.9	514	38.1	432	45.3	436	44.9	<b>436</b>	<b>44.9</b>
433.milc	<b>131</b>	<b>70.2</b>	131	70.2	132	69.7	<b>131</b>	<b>70.2</b>	131	70.2	132	69.7
434.zeusmp	46.7	195	<b>46.7</b>	<b>195</b>	46.7	195	46.7	195	<b>46.7</b>	<b>195</b>	46.7	195
435.gromacs	159	44.9	156	45.8	<b>157</b>	<b>45.4</b>	159	44.9	156	45.8	<b>157</b>	<b>45.4</b>
436.cactusADM	<b>16.3</b>	<b>735</b>	16.5	723	16.2	737	<b>16.3</b>	<b>735</b>	16.5	723	16.2	737
437.leslie3d	29.5	318	28.9	325	<b>29.4</b>	<b>319</b>	29.5	318	28.9	325	<b>29.4</b>	<b>319</b>
444.namd	<b>268</b>	<b>29.9</b>	268	29.9	269	29.9	260	30.8	<b>260</b>	<b>30.8</b>	261	30.8
447.dealII	178	64.4	177	64.6	<b>177</b>	<b>64.6</b>	178	64.4	177	64.6	<b>177</b>	<b>64.6</b>
450.soplex	183	45.5	<b>183</b>	<b>45.6</b>	182	45.7	183	45.5	<b>183</b>	<b>45.6</b>	182	45.7
453.povray	86.7	61.3	<b>87.2</b>	<b>61.0</b>	88.7	60.0	78.9	67.4	76.1	69.9	<b>78.4</b>	<b>67.8</b>
454.calculix	149	55.3	<b>149</b>	<b>55.3</b>	149	55.2	135	61.2	<b>135</b>	<b>61.1</b>	135	61.0
459.GemsFDTD	<b>51.3</b>	<b>207</b>	52.9	200	49.0	217	43.7	243	<b>43.4</b>	<b>245</b>	43.2	246
465.tonto	237	41.5	234	42.0	<b>236</b>	<b>41.8</b>	170	57.8	<b>170</b>	<b>57.8</b>	170	57.9
470.lbm	<b>23.0</b>	<b>598</b>	22.7	605	24.0	572	<b>23.0</b>	<b>598</b>	22.7	605	24.0	572
481.wrf	127	87.8	127	88.2	<b>127</b>	<b>87.8</b>	127	87.8	127	88.2	<b>127</b>	<b>87.8</b>
482.sphinx3	263	74.2	<b>263</b>	<b>74.2</b>	264	74.0	263	74.2	<b>263</b>	<b>74.2</b>	264	74.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-j81m Sun Nov 13 09:25:38 2016

This section contains SUT (System Under Test) info as seen by  
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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
    2 "physical id"s (chips)
    20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 10
    siblings  : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264077184 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-j81m 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 13 09:06

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  884G  15G  869G   2% /
Additional information from dmidecode:

```

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

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Tested by: Huawei

Software Availability: Dec-2015

## Base Portability Flags (Continued)

```

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Software Availability: Dec-2015

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Tested by: Huawei

Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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