



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **99.5**

Huawei RH2288 V3(Intel Xeon E5-2630L v4)

SPECfp\_base2006 = **93.7**

CPU2006 license: 3175

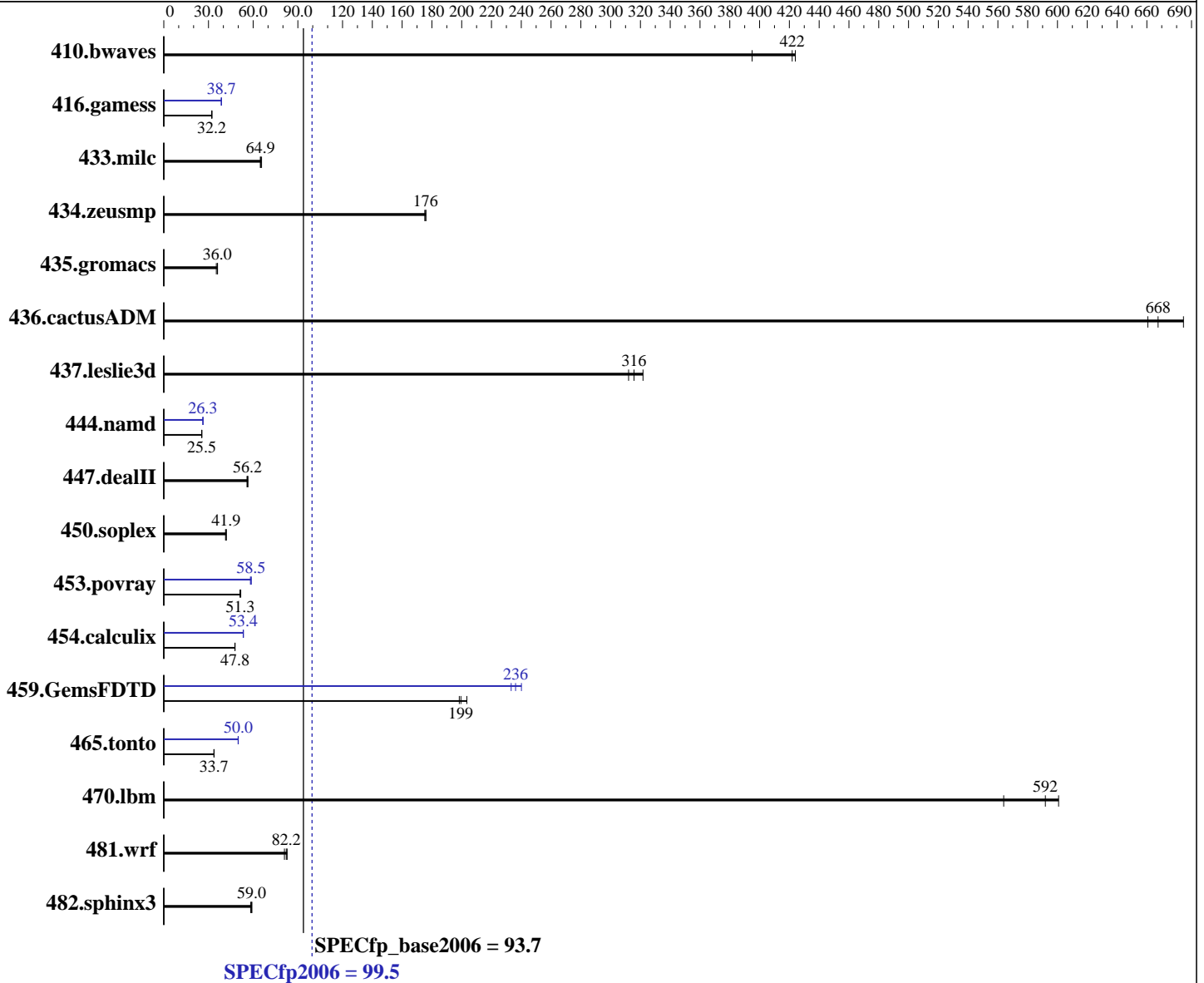
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2630L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>32.2</u>	<u>422</u>	32.1	424	34.4	395	<u>32.2</u>	<u>422</u>	32.1	424	34.4	395
416.gamess	607	32.3	<u>608</u>	<u>32.2</u>	608	32.2	<u>506</u>	<u>38.7</u>	507	38.6	506	38.7
433.milc	140	65.6	<u>141</u>	<u>64.9</u>	142	64.9	140	65.6	<u>141</u>	<u>64.9</u>	142	64.9
434.zeusmp	51.9	175	51.7	176	<u>51.8</u>	<u>176</u>	51.9	175	51.7	176	<u>51.8</u>	<u>176</u>
435.gromacs	203	35.3	<u>199</u>	<u>36.0</u>	198	36.1	203	35.3	<u>199</u>	<u>36.0</u>	198	36.1
436.cactusADM	<u>17.9</u>	<u>668</u>	18.1	661	17.5	685	<u>17.9</u>	<u>668</u>	18.1	661	17.5	685
437.leslie3d	<u>29.8</u>	<u>316</u>	30.1	312	29.2	322	<u>29.8</u>	<u>316</u>	30.1	312	29.2	322
444.namd	315	25.5	315	25.5	<u>315</u>	<u>25.5</u>	305	26.3	305	26.3	<u>305</u>	<u>26.3</u>
447.dealII	202	56.5	<u>204</u>	<u>56.2</u>	205	55.8	202	56.5	<u>204</u>	<u>56.2</u>	205	55.8
450.soplex	201	41.4	<u>199</u>	<u>41.9</u>	199	42.0	201	41.4	<u>199</u>	<u>41.9</u>	199	42.0
453.povray	104	51.2	103	51.7	<u>104</u>	<u>51.3</u>	<u>90.9</u>	<u>58.5</u>	91.5	58.1	90.6	58.7
454.calculix	173	47.8	<u>173</u>	<u>47.8</u>	173	47.7	<u>155</u>	<u>53.4</u>	155	53.3	154	53.5
459.GemsFDTD	<u>53.2</u>	<u>199</u>	53.5	198	52.1	203	44.2	240	<u>44.9</u>	<u>236</u>	45.5	233
465.tonto	292	33.6	291	33.8	<u>292</u>	<u>33.7</u>	197	49.9	197	50.0	<u>197</u>	<u>50.0</u>
470.lbm	22.9	601	24.4	564	<u>23.2</u>	<u>592</u>	22.9	601	24.4	564	<u>23.2</u>	<u>592</u>
481.wrf	135	82.8	<u>136</u>	<u>82.2</u>	138	81.0	135	82.8	<u>136</u>	<u>82.2</u>	138	81.0
482.sphinx3	<u>331</u>	<u>59.0</u>	334	58.3	330	59.1	<u>331</u>	<u>59.0</u>	334	58.3	330	59.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-jorn Sat Nov 12 15:15:19 2016

This section contains SUT (System Under Test) info as seen by  
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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
    2 "physical id"s (chips)
    20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 10
    siblings  : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264063872 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-jorn 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 12 15:10

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  884G  14G  870G   2% /
Additional information from dmidecode:

```

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

```

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64



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Tested by: Huawei

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Hardware Availability: Mar-2016

Software Availability: Dec-2015

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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