



# SPEC® CFP2006 Result

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## Huawei

SPECfp®2006 = 119

Huawei CH121 V3 (Intel Xeon E5-2699 v4)

SPECfp\_base2006 = 112

CPU2006 license: 3175

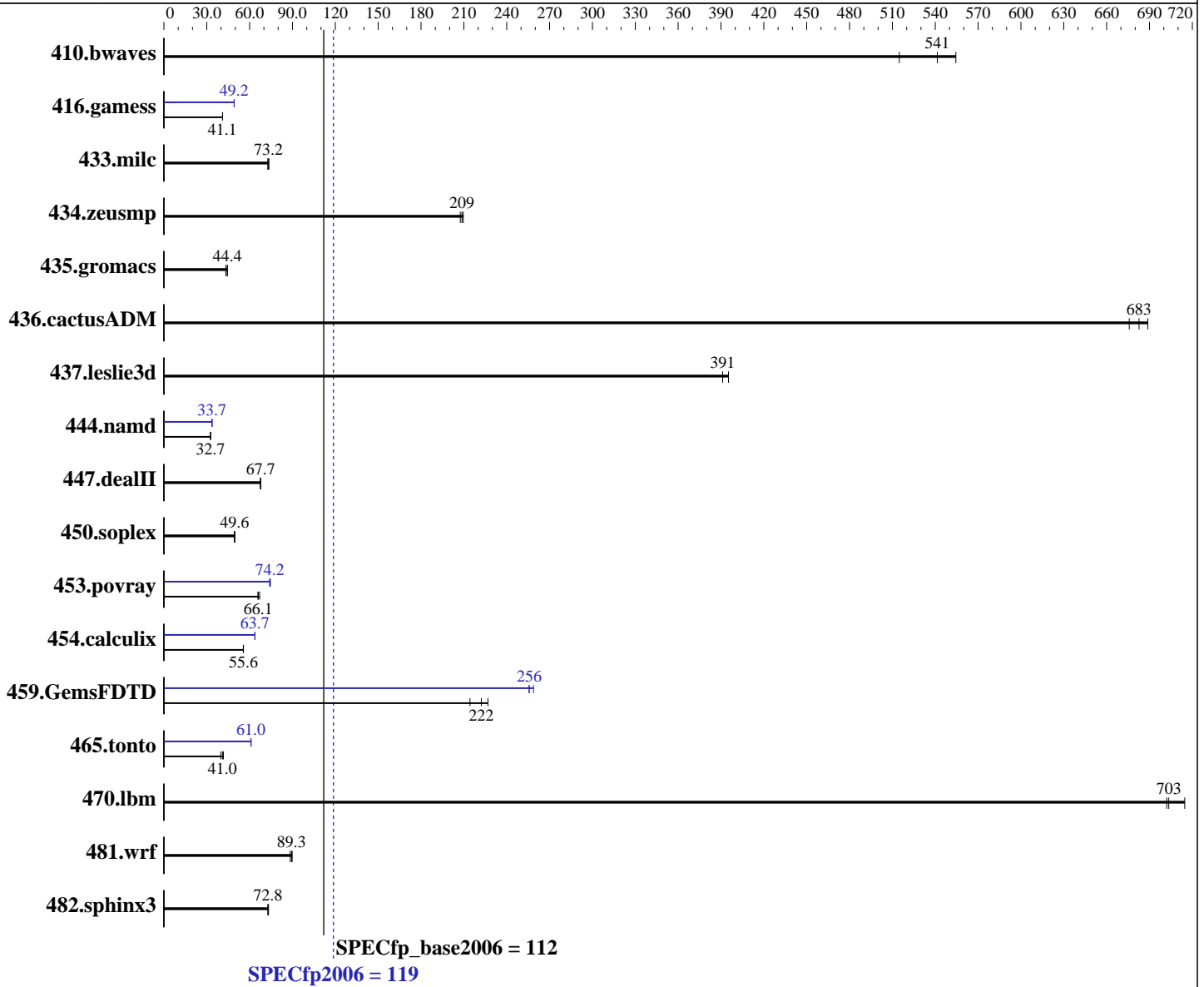
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016



**Hardware**

CPU Name: Intel Xeon E5-2699 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

**Software**

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs

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L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.4	515	24.5	554	<u>25.1</u>	<u>541</u>	26.4	515	24.5	554	<u>25.1</u>	<u>541</u>
416.gamess	477	41.0	476	41.1	<u>477</u>	<u>41.1</u>	<u>398</u>	<u>49.2</u>	398	49.2	398	49.2
433.milc	<u>125</u>	<u>73.2</u>	126	72.7	125	73.5	<u>125</u>	<u>73.2</u>	126	72.7	125	73.5
434.zeusmp	43.9	207	<u>43.6</u>	<u>209</u>	43.4	210	43.9	207	<u>43.6</u>	<u>209</u>	43.4	210
435.gromacs	<u>161</u>	<u>44.4</u>	161	44.4	165	43.3	<u>161</u>	<u>44.4</u>	161	44.4	165	43.3
436.cactusADM	<u>17.5</u>	<u>683</u>	17.3	689	17.7	676	<u>17.5</u>	<u>683</u>	17.3	689	17.7	676
437.leslie3d	24.0	391	<u>24.0</u>	<u>391</u>	23.8	395	24.0	391	<u>24.0</u>	<u>391</u>	23.8	395
444.namd	<u>245</u>	<u>32.7</u>	246	32.7	245	32.7	<u>238</u>	<u>33.7</u>	238	33.6	238	33.7
447.dealII	<u>169</u>	<u>67.7</u>	170	67.5	169	67.9	<u>169</u>	<u>67.7</u>	170	67.5	169	67.9
450.soplex	168	49.6	169	49.4	<u>168</u>	<u>49.6</u>	168	49.6	169	49.4	<u>168</u>	<u>49.6</u>
453.povray	79.4	67.0	<u>80.5</u>	<u>66.1</u>	80.9	65.8	<u>71.7</u>	<u>74.2</u>	71.9	74.0	71.3	74.6
454.calculix	<u>148</u>	<u>55.6</u>	148	55.7	148	55.6	130	63.7	129	63.8	<u>129</u>	<u>63.7</u>
459.GemsFDTD	46.8	227	49.5	214	<u>47.7</u>	<u>222</u>	41.0	259	<u>41.5</u>	<u>256</u>	41.5	256
465.tonto	247	39.8	<u>240</u>	<u>41.0</u>	236	41.7	162	60.9	161	61.0	<u>161</u>	<u>61.0</u>
470.lbm	<u>19.5</u>	<u>703</u>	19.6	702	19.2	715	<u>19.5</u>	<u>703</u>	19.6	702	19.2	715
481.wrf	126	88.5	124	89.8	<u>125</u>	<u>89.3</u>	126	88.5	124	89.8	<u>125</u>	<u>89.3</u>
482.sphinx3	268	72.8	<u>268</u>	<u>72.8</u>	266	73.2	268	72.8	<u>268</u>	<u>72.8</u>	266	73.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on localhost.localdomain Thu Oct 20 09:52:48 2016

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
 2 "physical id"s (chips)
 44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores      : 22
  siblings       : 22
  physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
  physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size      : 56320 KB

```

From /proc/meminfo

```

MemTotal:        263566508 kB
HugePages_Total: 0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

uname -a:

```

Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 20 09:41

SPEC is set to: /spec16

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2        xfs       254G  9.5G  245G   4% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

16x Micron 18ASF2G72PDZ-2G3B1 16 GB 2 rank 2400 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

Continued on next page

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Page 4



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Tested by: Huawei

Software Availability: Mar-2016

## Base Portability Flags (Continued)

```

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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