



SPEC[®] CFP2006 Result

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Cisco Systems

Cisco UCS C220 M4 (Intel Xeon E5-2680 v4, 2.40 GHz)

SPECfp[®]2006 = **121**

SPECfp_base2006 = **115**

CPU2006 license: 9019

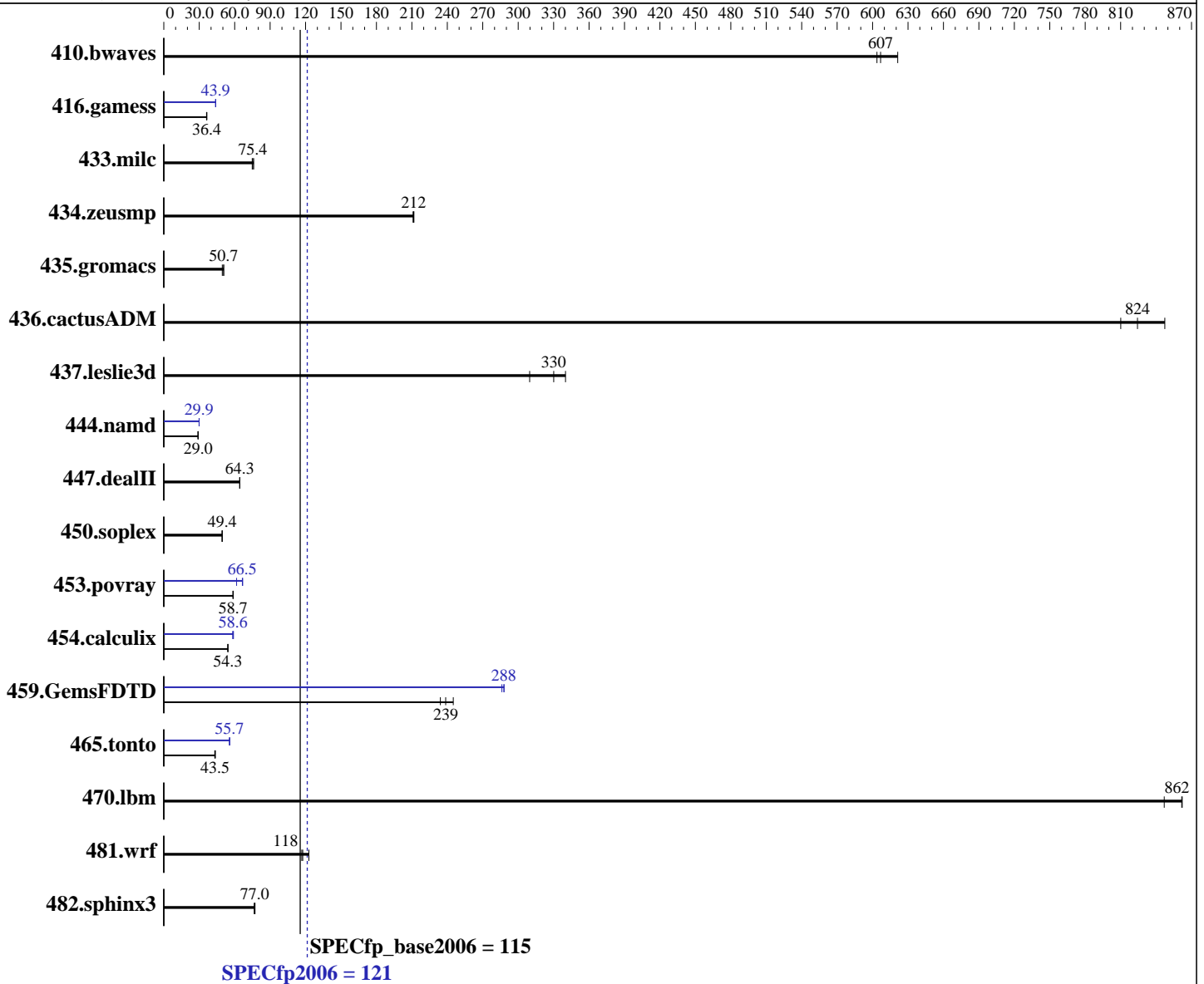
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: May-2016

Hardware Availability: Apr-2016

Software Availability: Dec-2015



Hardware	
CPU Name:	Intel Xeon E5-2680 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	28 cores, 2 chips, 14 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Software	
Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 300 GB SAS HDD, 15K RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>22.4</u>	<u>607</u>	21.9	621	22.5	604	<u>22.4</u>	<u>607</u>	21.9	621	22.5	604
416.gamess	<u>538</u>	<u>36.4</u>	538	36.4	541	36.2	<u>446</u>	<u>43.9</u>	447	43.8	446	43.9
433.milc	121	76.1	123	74.9	<u>122</u>	<u>75.4</u>	121	76.1	123	74.9	<u>122</u>	<u>75.4</u>
434.zeusmp	43.0	212	43.1	211	<u>43.0</u>	<u>212</u>	43.0	212	43.1	211	<u>43.0</u>	<u>212</u>
435.gromacs	141	50.7	<u>141</u>	<u>50.7</u>	144	49.5	141	50.7	<u>141</u>	<u>50.7</u>	144	49.5
436.cactusADM	14.1	847	<u>14.5</u>	<u>824</u>	14.8	810	14.1	847	<u>14.5</u>	<u>824</u>	14.8	810
437.leslie3d	27.6	340	30.4	310	<u>28.5</u>	<u>330</u>	27.6	340	30.4	310	<u>28.5</u>	<u>330</u>
444.namd	<u>276</u>	<u>29.0</u>	276	29.0	276	29.0	268	29.9	268	29.9	<u>268</u>	<u>29.9</u>
447.dealII	<u>178</u>	<u>64.3</u>	178	64.3	178	64.3	<u>178</u>	<u>64.3</u>	178	64.3	178	64.3
450.soplex	169	49.5	169	49.4	<u>169</u>	<u>49.4</u>	169	49.5	169	49.4	<u>169</u>	<u>49.4</u>
453.povray	<u>90.7</u>	<u>58.7</u>	90.4	58.9	91.0	58.5	86.3	61.6	79.6	66.8	<u>80.0</u>	<u>66.5</u>
454.calculix	152	54.2	152	54.3	<u>152</u>	<u>54.3</u>	<u>141</u>	<u>58.6</u>	140	59.0	141	58.4
459.GemsFDTD	43.3	245	<u>44.5</u>	<u>239</u>	45.3	234	<u>36.9</u>	<u>288</u>	36.8	288	37.1	286
465.tonto	226	43.6	<u>226</u>	<u>43.5</u>	228	43.1	176	55.8	<u>177</u>	<u>55.7</u>	177	55.6
470.lbm	16.2	847	<u>15.9</u>	<u>862</u>	15.9	862	16.2	847	<u>15.9</u>	<u>862</u>	15.9	862
481.wrf	<u>95.0</u>	<u>118</u>	95.6	117	90.9	123	<u>95.0</u>	<u>118</u>	95.6	117	90.9	123
482.sphinx3	255	76.5	<u>253</u>	<u>77.0</u>	252	77.2	255	76.5	<u>253</u>	<u>77.0</u>	252	77.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Intel Hyper-Threading Technology option set to Disabled

CPU performance set to Enterprise

Power Technology set to Energy Efficient

Energy Performance BIAS setting set to Balanced Performance

Memory RAS configuration set to Maximum Performance

Memory Power Saving Mode set to Disabled

QPI Snoop Mode set to Home Directory Snoop with OSB

Sysinfo program /home/CISCO_Benchmarks/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

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Test date: May-2016
Hardware Availability: Apr-2016
Software Availability: Dec-2015

Platform Notes (Continued)

running on linux-f3gd Tue May 31 22:52:51 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v4@ 2.40GHz
 2 "physical id"s (chips)
 28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      264368624 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-f3gd 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 31 03:21
```

```
SPEC is set to: /home/CISCO_Benchmarks/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
Continued on next page
```



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Platform Notes (Continued)

/dev/sdb1 xfs 238G 44G 194G 19% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C220M4.2.0.9.42.021920161702 02/19/2016

Memory:

16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/CISCO_Benchmarks/cpu2006/libs/32:/home/CISCO_Benchmarks/cpu2006/libs/64:/home/CISCO_Benchmarks/cpu2006/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Tue Sep 6 16:57:23 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 September 2016.