



# SPEC® CFP2006 Result

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## Lenovo Group Limited

SPECfp®2006 = **95.7**

Lenovo System x3650 X6  
(Intel Xeon E5-2650L v4, 1.70 GHz)

SPECfp\_base2006 = **90.1**

CPU2006 license: 9017

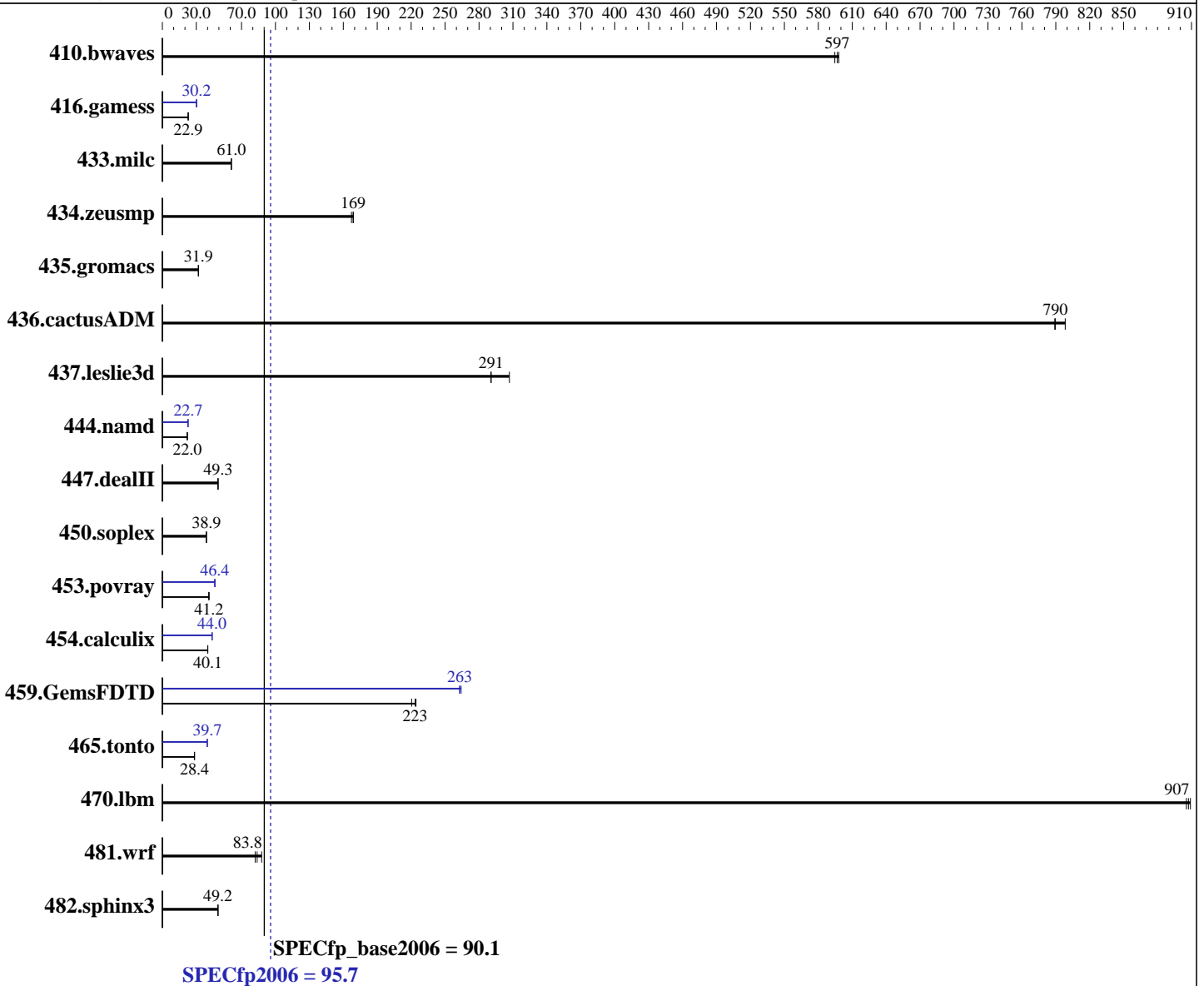
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2650L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.7	598	22.9	595	<u>22.8</u>	<u>597</u>	22.7	598	22.9	595	<u>22.8</u>	<u>597</u>
416.gamess	856	22.9	<u>857</u>	<u>22.9</u>	857	22.9	649	30.2	<u>648</u>	<u>30.2</u>	648	30.2
433.milc	151	61.0	<u>151</u>	<u>61.0</u>	151	60.9	151	61.0	<u>151</u>	<u>61.0</u>	151	60.9
434.zeusmp	54.4	167	<u>54.0</u>	<u>169</u>	53.9	169	54.4	167	<u>54.0</u>	<u>169</u>	53.9	169
435.gromacs	224	31.8	<u>224</u>	<u>31.9</u>	224	31.9	224	31.8	<u>224</u>	<u>31.9</u>	224	31.9
436.cactusADM	<u>15.1</u>	<u>790</u>	15.0	798	15.1	789	<u>15.1</u>	<u>790</u>	15.0	798	15.1	789
437.leslie3d	30.6	307	32.4	291	<u>32.3</u>	<u>291</u>	30.6	307	32.4	291	<u>32.3</u>	<u>291</u>
444.namd	364	22.0	364	22.0	<u>364</u>	<u>22.0</u>	353	22.7	353	22.7	<u>353</u>	<u>22.7</u>
447.dealII	233	49.0	232	49.4	<u>232</u>	<u>49.3</u>	233	49.0	232	49.4	<u>232</u>	<u>49.3</u>
450.soplex	215	38.8	214	39.0	<u>214</u>	<u>38.9</u>	215	38.8	214	39.0	<u>214</u>	<u>38.9</u>
453.povray	<u>129</u>	<u>41.2</u>	129	41.2	129	41.1	<u>115</u>	<u>46.4</u>	114	46.5	115	46.3
454.calculix	206	40.1	205	40.2	<u>206</u>	<u>40.1</u>	<u>187</u>	<u>44.0</u>	187	44.0	188	43.9
459.GemsFDTD	<u>47.5</u>	<u>223</u>	48.1	220	47.3	224	40.2	264	40.4	263	<u>40.3</u>	<u>263</u>
465.tonto	348	28.3	<u>346</u>	<u>28.4</u>	345	28.5	<u>248</u>	<u>39.7</u>	248	39.6	248	39.7
470.lbm	15.2	905	<u>15.1</u>	<u>907</u>	15.1	909	15.2	905	<u>15.1</u>	<u>907</u>	15.1	909
481.wrf	127	87.8	<u>133</u>	<u>83.8</u>	136	82.1	127	87.8	<u>133</u>	<u>83.8</u>	136	82.1
482.sphinx3	<u>396</u>	<u>49.2</u>	396	49.2	397	49.1	<u>396</u>	<u>49.2</u>	396	49.2	397	49.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:  
Operating Mode set to Maximum Performance  
Intel Hyperthreading set to Disabled  
COD Preference set to Disable  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-gp44 Thu May 12 04:33:27 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
 2 "physical id"s (chips)
 28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

```

From /proc/meminfo

```

MemTotal:      263960164 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

```

Linux linux-gp44 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 10 22:53

SPEC is set to: /home/cpu2006-1.2-ic16.0

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        xfs      688G      7.6G  681G   2% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS LENOVO -[TCE124I-2.10]- 04/27/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP\_NUM\_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -ansi-alias

C++ benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

Fortran benchmarks:  
 ifort -m64

Benchmarks using both Fortran and C:  
 icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

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