# SPEC® CFP2006 Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

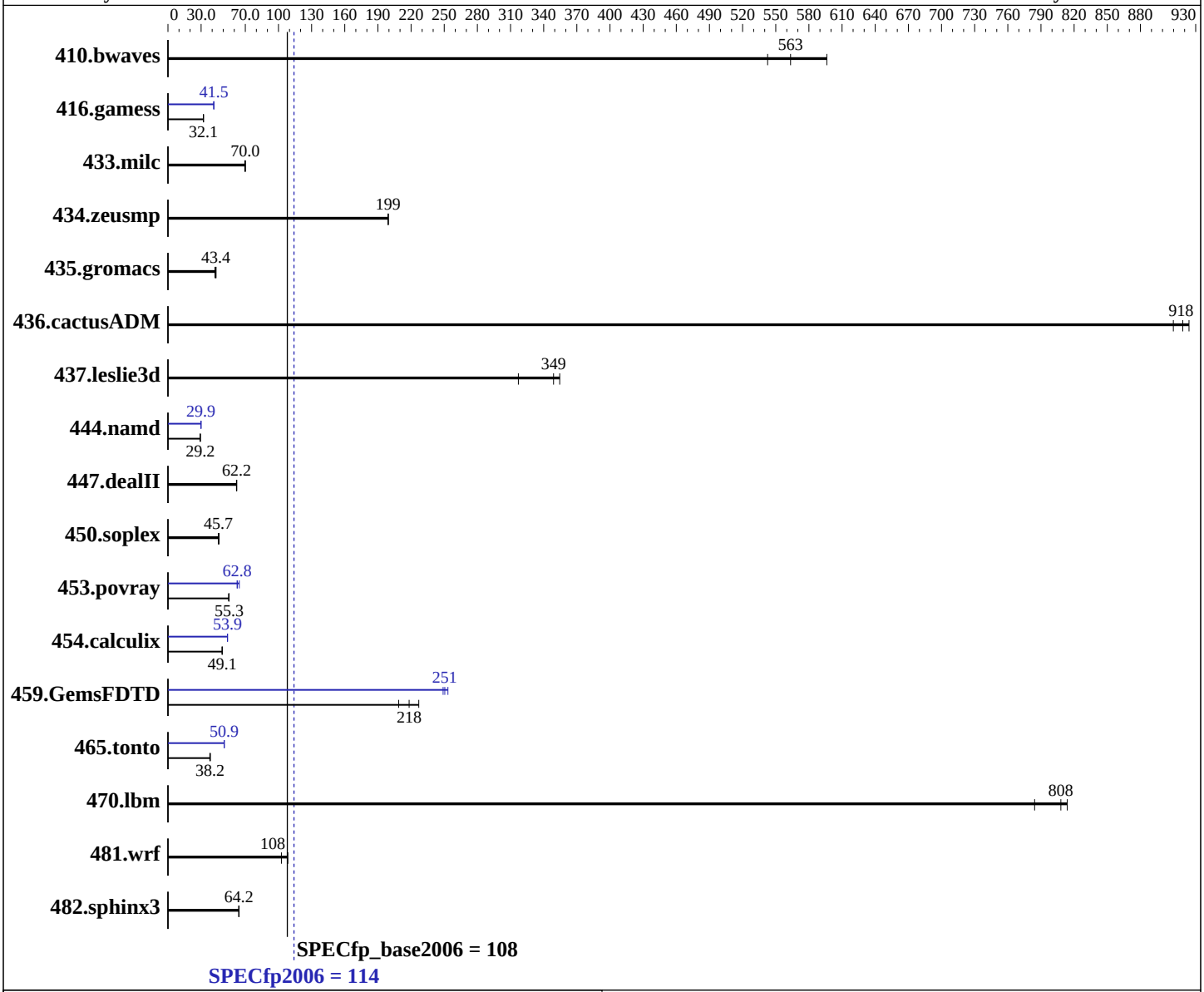
ProLiant DL160 Gen9
(2.10 GHz, Intel Xeon E5-2695 v4)

SPECfp®2006 = 114

SPECfp_base2006 = 108

**CPU2006 license:** 3
**Test sponsor:** HPE
**Tested by:** HPE

**Test date:** May-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Nov-2015




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2695 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.30 GHz |
| CPU MHz: | 2100 |
| FPU: | Integrated |
| CPU(s) enabled: | 36 cores, 2 chips, 18 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.2 (Maipo)<br>Kernel 3.10.0-327.el7.x86_64 |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;<br>Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | **24.1** | **563** | 22.8 | 596 | 25.0 | 543 | **24.1** | **563** | 22.8 | 596 | 25.0 | 543 |
| 416.gamess | **609** | **32.1** | 608 | 32.2 | 612 | 32.0 | 472 | 41.5 | **472** | **41.5** | 471 | 41.5 |
| 433.milc | 131 | 70.1 | **131** | **70.0** | 132 | 69.5 | 131 | 70.1 | **131** | **70.0** | 132 | 69.5 |
| 434.zeusmp | 45.7 | 199 | 45.6 | 199 | **45.7** | **199** | 45.7 | 199 | 45.6 | 199 | **45.7** | **199** |
| 435.gromacs | 164 | 43.4 | **165** | **43.4** | 168 | 42.6 | 164 | 43.4 | **165** | **43.4** | 168 | 42.6 |
| 436.cactusADM | 13.1 | 910 | **13.0** | **918** | 12.9 | 924 | 13.1 | 910 | **13.0** | **918** | 12.9 | 924 |
| 437.leslie3d | **26.9** | **349** | 26.5 | 354 | 29.6 | 317 | **26.9** | **349** | 26.5 | 354 | 29.6 | 317 |
| 444.namd | 274 | 29.3 | **274** | **29.2** | 274 | 29.2 | 268 | 29.9 | **268** | **29.9** | 269 | 29.9 |
| 447.dealII | 184 | 62.2 | **184** | **62.2** | 185 | 62.0 | 184 | 62.2 | **184** | **62.2** | 185 | 62.0 |
| 450.soplex | **182** | **45.7** | 180 | 46.2 | 183 | 45.7 | **182** | **45.7** | 180 | 46.2 | 183 | 45.7 |
| 453.povray | **96.2** | **55.3** | 96.2 | 55.3 | 97.1 | 54.8 | 85.0 | 62.6 | 82.4 | 64.6 | **84.8** | **62.8** |
| 454.calculix | 168 | 49.1 | 168 | 49.0 | **168** | **49.1** | 153 | 53.9 | **153** | **53.9** | 153 | 53.8 |
| 459.GemsFDTD | 46.7 | 227 | **48.6** | **218** | 50.8 | 209 | 41.9 | 253 | **42.4** | **251** | 42.6 | 249 |
| 465.tonto | **257** | **38.2** | 260 | 37.9 | 257 | 38.3 | 194 | 50.8 | 193 | 51.0 | **193** | **50.9** |
| 470.lbm | 17.5 | 784 | **17.0** | **808** | 16.9 | 814 | 17.5 | 784 | **17.0** | **808** | 16.9 | 814 |
| 481.wrf | 109 | 103 | 103 | 108 | **103** | **108** | 109 | 103 | 103 | 108 | **103** | **108** |
| 482.sphinx3 | 303 | 64.4 | **304** | **64.2** | 304 | 64.0 | 303 | 64.4 | **304** | **64.2** | 304 | 64.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

```
BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C1E State
  Minimum Processor Idle Power Package C-State set to No Package State
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
```

Continued on next page

## Platform Notes (Continued)

```
      Processor Power and Utilization Monitoring set to Disabled
      Memory Refresh Rate set to 1x Refresh
      Intel Hyperthreading set to Enabled
    Sysinfo program /cpu2006-HP/config/sysinfo.rev6914
    $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
    running on DL160Gen9 Wed May 18 10:58:18 2016

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
          2 "physical id"s (chips)
          72 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 18
          siblings  : 36
          physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
          physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
       cache size : 46080 KB

    From /proc/meminfo
       MemTotal:       527930448 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.2 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.2"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

    uname -a:
       Linux DL160Gen9 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
       x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 May 17 17:50

    SPEC is set to: /cpu2006-HP
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda4      xfs   368G  167G  201G  46% /
```

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## Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP U20 02/22/2016
   Memory:
    16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

 (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
OMP_NUM_THREADS = "36"
LD_LIBRARY_PATH = "/cpu2006-HP/libs/32:/cpu2006-HP/libs/64:/cpu2006-HP/sh"

 Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
 memory using RedHat EL 7.2
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
```

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## Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

Continued on next page

## Peak Optimization Flags (Continued)

465.tonto (continued):
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml


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