



# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp®2006 = **95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

SPECfp\_base2006 = **89.9**

CPU2006 license: 9017

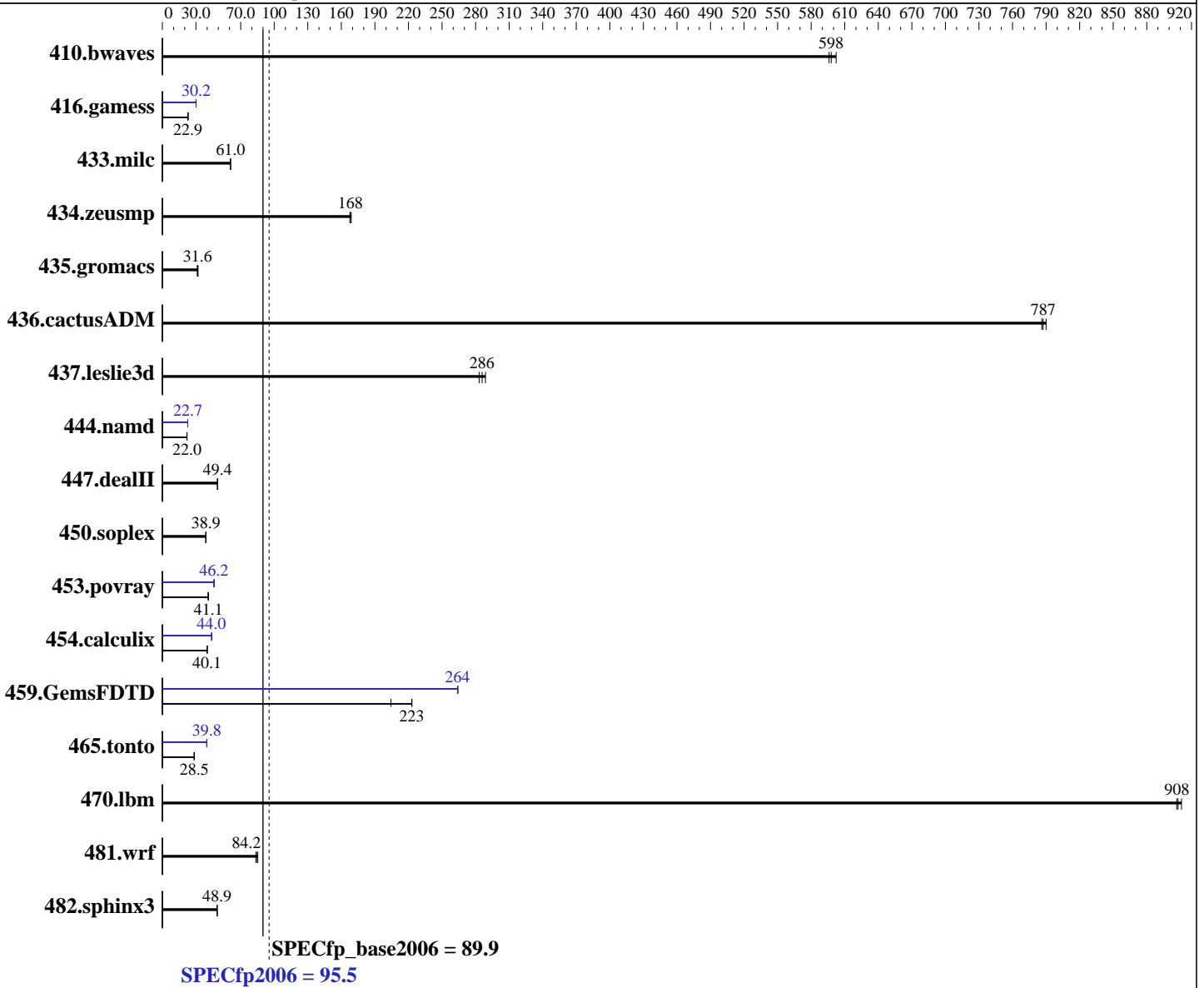
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2650L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp2006 = **95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

SPECfp\_base2006 = **89.9**

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>22.7</u>	<u>598</u>	22.8	596	22.6	602	<u>22.7</u>	<u>598</u>	22.8	596	22.6	602
416.gamess	853	23.0	854	22.9	<u>853</u>	<u>22.9</u>	<u>649</u>	<u>30.2</u>	649	30.2	651	30.1
433.milc	151	61.0	151	60.9	<u>151</u>	<u>61.0</u>	151	61.0	151	60.9	<u>151</u>	<u>61.0</u>
434.zeusmp	54.3	168	<u>54.2</u>	<u>168</u>	53.9	169	54.3	168	<u>54.2</u>	<u>168</u>	53.9	169
435.gromacs	224	31.9	<u>226</u>	<u>31.6</u>	229	31.2	224	31.9	<u>226</u>	<u>31.6</u>	229	31.2
436.cactusADM	<u>15.2</u>	<u>787</u>	15.1	790	15.2	786	<u>15.2</u>	<u>787</u>	15.1	790	15.2	786
437.leslie3d	32.5	289	<u>32.9</u>	<u>286</u>	33.2	283	32.5	289	<u>32.9</u>	<u>286</u>	33.2	283
444.namd	<u>364</u>	<u>22.0</u>	365	21.9	364	22.0	353	22.7	<u>353</u>	<u>22.7</u>	353	22.7
447.dealII	<u>232</u>	<u>49.4</u>	232	49.2	232	49.4	<u>232</u>	<u>49.4</u>	232	49.2	232	49.4
450.soplex	214	38.9	215	38.9	<u>214</u>	<u>38.9</u>	214	38.9	215	38.9	<u>214</u>	<u>38.9</u>
453.povray	<u>130</u>	<u>41.1</u>	129	41.2	130	40.9	115	46.4	<u>115</u>	<u>46.2</u>	116	46.0
454.calculix	206	40.1	<u>206</u>	<u>40.1</u>	206	40.1	188	43.9	<u>187</u>	<u>44.0</u>	187	44.0
459.GemsFDTD	<u>47.6</u>	<u>223</u>	51.9	204	47.6	223	40.2	264	<u>40.2</u>	<u>264</u>	40.1	264
465.tonto	344	28.6	<u>345</u>	<u>28.5</u>	347	28.4	247	39.8	<u>247</u>	<u>39.8</u>	248	39.7
470.lbm	<u>15.1</u>	<u>908</u>	15.1	911	15.2	907	<u>15.1</u>	<u>908</u>	15.1	911	15.2	907
481.wrf	134	83.6	<u>133</u>	<u>84.2</u>	131	85.3	134	83.6	<u>133</u>	<u>84.2</u>	131	85.3
482.sphinx3	397	49.1	399	48.9	<u>398</u>	<u>48.9</u>	397	49.1	399	48.9	<u>398</u>	<u>48.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Intel Hyperthreading set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on DaAn-02 Thu May 26 00:36:03 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp2006 = 95.5

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

SPECfp\_base2006 = 89.9

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

### Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
 2 "physical id"s (chips)
28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

```

```

From /proc/meminfo
MemTotal:      263829056 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux DaAn-02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 25 17:45

```

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   688G   28G  661G   5% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp2006 = 95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

**SPECfp\_base2006 = 89.9**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Platform Notes (Continued)

BIOS LENOVO -[TBE124K-2.10]- 05/10/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP\_NUM\_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp2006 = 95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

**SPECfp\_base2006 = 89.9**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Base Portability Flags (Continued)

```

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

**SPECfp2006 = 95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

**SPECfp\_base2006 = 89.9**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp2006 = 95.5**

Lenovo System x3550 M5  
(1.70 GHz, Intel Xeon E5-2650L v4)

**SPECfp\_base2006 = 89.9**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jun 30 14:07:48 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 June 2016.