



SPEC® CFP2006 Result

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Lenovo Group Limited

SPECfp®2006 = 112

Lenovo System x3650 M5
(2.20 GHz, Intel Xeon E5-2650 v4)

SPECfp_base2006 = 107

CPU2006 license: 9017

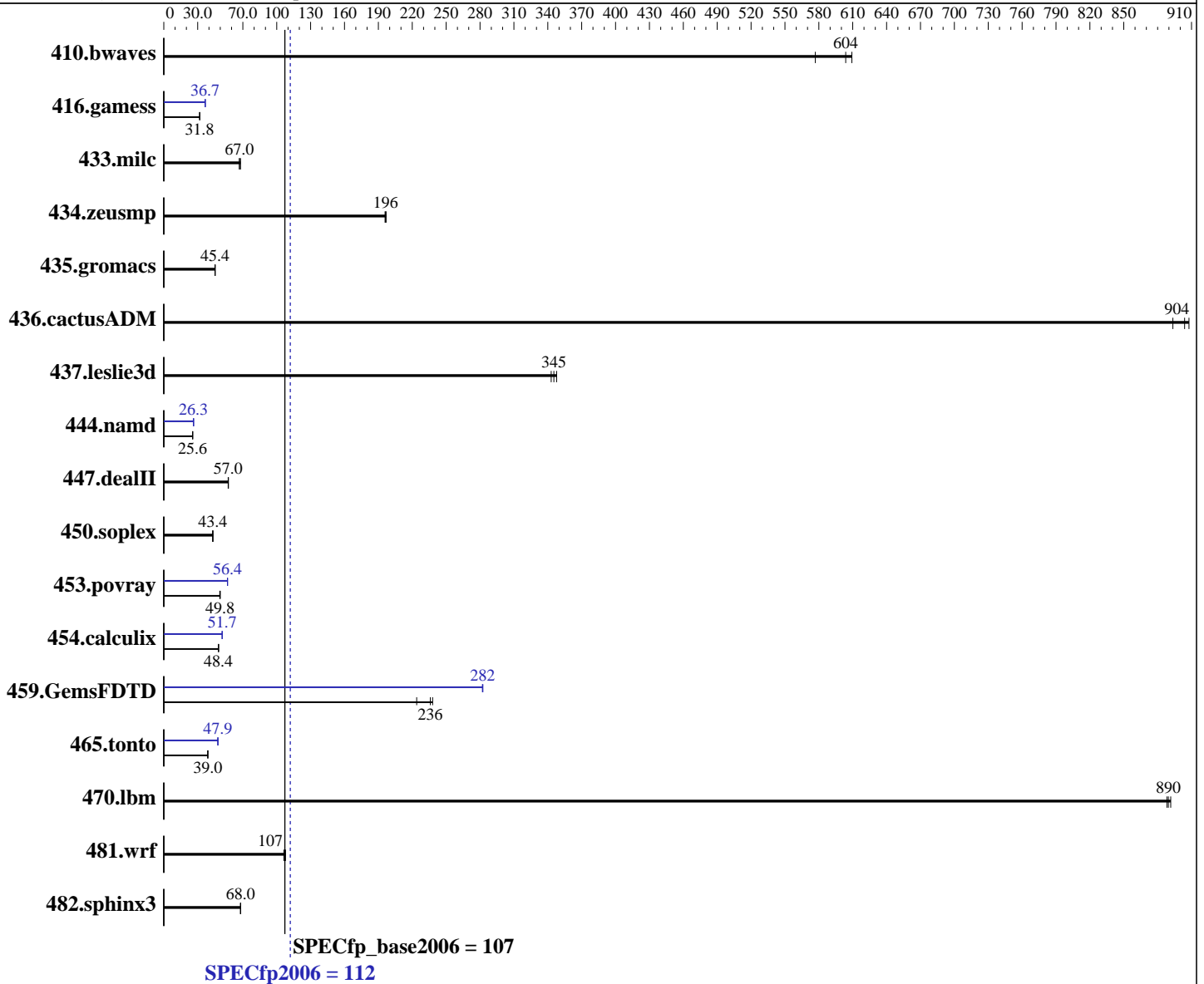
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2650 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>22.5</u>	604	22.3	609	23.6	577	<u>22.5</u>	604	22.3	609	23.6	577
416.gamess	616	<u>31.8</u>	614	31.9	617	31.7	533	36.7	534	36.7	<u>534</u>	36.7
433.milc	135	68.0	137	<u>67.0</u>	137	66.9	135	68.0	137	<u>67.0</u>	137	66.9
434.zeusmp	46.5	196	46.2	197	46.3	<u>196</u>	46.5	196	46.2	197	46.3	<u>196</u>
435.gromacs	157	45.4	157	45.5	157	<u>45.4</u>	157	45.4	157	45.5	157	<u>45.4</u>
436.cactusADM	13.2	908	13.2	<u>904</u>	13.4	893	13.2	908	13.2	<u>904</u>	13.4	893
437.leslie3d	27.4	343	27.0	348	27.2	<u>345</u>	27.4	343	27.0	348	27.2	<u>345</u>
444.namd	314	25.6	314	<u>25.6</u>	314	25.5	305	<u>26.3</u>	304	26.4	305	26.3
447.dealII	201	<u>57.0</u>	201	57.0	200	57.3	201	<u>57.0</u>	201	57.0	200	57.3
450.soplex	192	<u>43.4</u>	193	43.2	192	43.5	192	<u>43.4</u>	193	43.2	192	43.5
453.povray	107	<u>49.8</u>	107	50.0	107	49.6	94.4	56.4	94.2	56.5	94.4	<u>56.4</u>
454.calculix	170	<u>48.4</u>	170	48.5	170	48.4	159	51.7	160	<u>51.7</u>	160	51.6
459.GemsFDTD	45.0	<u>236</u>	47.4	224	44.6	238	37.6	282	37.6	283	37.6	<u>282</u>
465.tonto	253	38.9	252	<u>39.0</u>	252	39.0	205	<u>47.9</u>	205	47.9	206	47.8
470.lbm	15.4	892	15.4	<u>890</u>	15.5	888	15.4	892	15.4	<u>890</u>	15.5	888
481.wrf	105	106	104	<u>107</u>	104	108	105	106	104	<u>107</u>	104	108
482.sphinx3	287	<u>68.0</u>	286	68.0	287	67.9	287	<u>68.0</u>	286	68.0	287	67.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Hyper-Threading set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on XinYi-02 Wed May 11 08:15:05 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page

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Page 2



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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

From /proc/meminfo
MemTotal:      263960580 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux XinYi-02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 11 02:15

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   688G  7.6G  681G  2% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

```

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Platform Notes (Continued)

BIOS LENOVO -[TCE124I-2.10]- 04/27/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

Continued on next page

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Page 4



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Base Portability Flags (Continued)

```

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page



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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

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