



# SPEC<sup>®</sup> CFP2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL580 Gen9  
(2.80 GHz, Intel Xeon E7-8891 v4)

**SPECfp<sup>®</sup>2006 = Not Run**

**SPECfp\_base2006 = 132**

CPU2006 license: 3

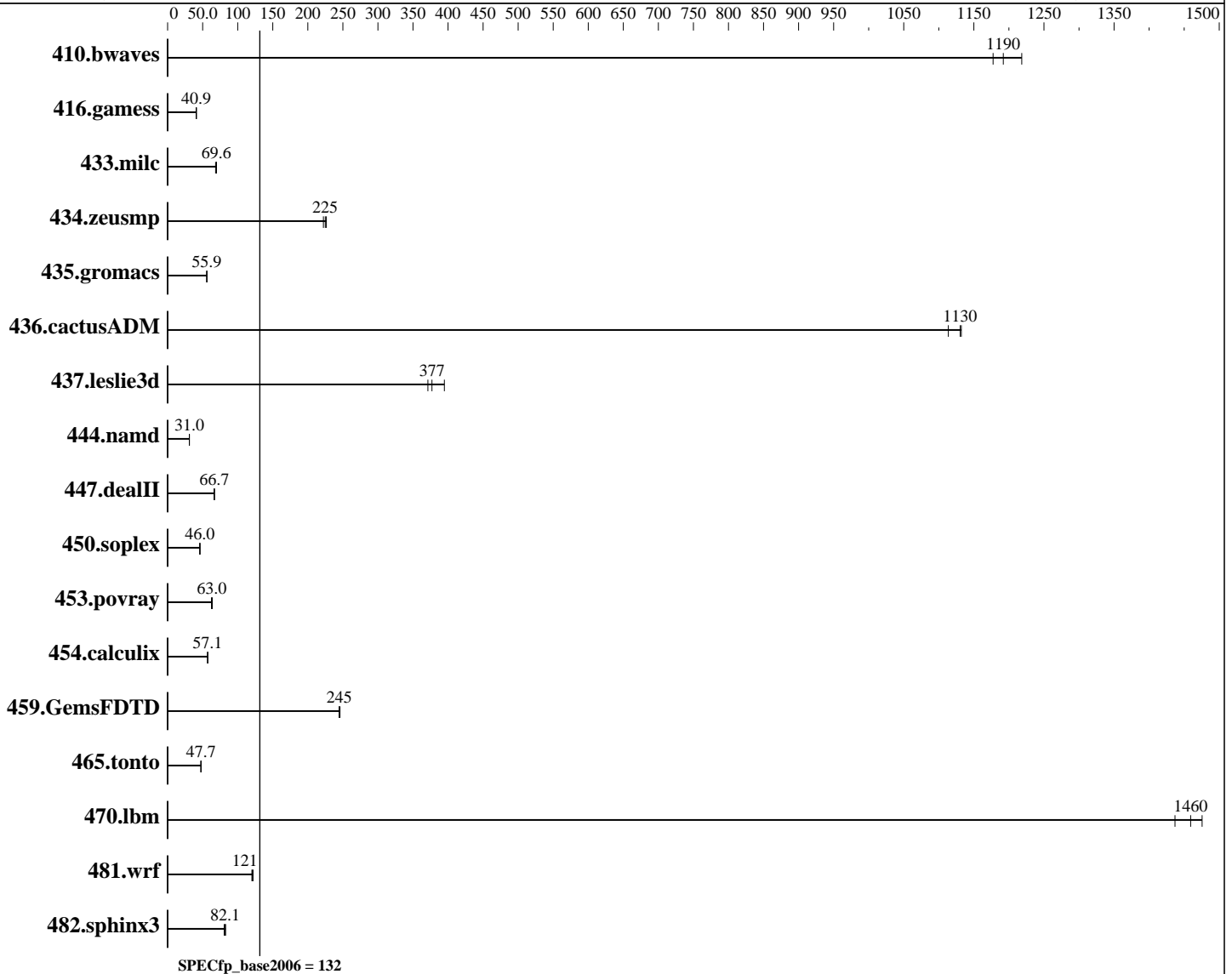
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015



## Hardware

CPU Name: Intel Xeon E7-8891 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 60 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)  
Disk Subsystem: 1 x 800 GB NVMe PCIe SSD, RAID 0  
Other Hardware: DL580 Gen9 NVMe SSD Express Bay Enablement Kit

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	11.5	1180	<b><u>11.4</u></b>	<b><u>1190</u></b>	11.2	1220						
416.gamess	<b><u>478</u></b>	<b><u>40.9</u></b>	481	40.7	477	41.0						
433.milc	134	68.4	132	69.7	<b><u>132</u></b>	<b><u>69.6</u></b>						
434.zeusmp	40.9	222	40.2	226	<b><u>40.5</u></b>	<b><u>225</u></b>						
435.gromacs	<b><u>128</u></b>	<b><u>55.9</u></b>	128	56.0	128	55.9						
436.cactusADM	<b><u>10.6</u></b>	<b><u>1130</u></b>	10.7	1110	10.6	1130						
437.leslie3d	<b><u>24.9</u></b>	<b><u>377</u></b>	25.3	371	23.8	395						
444.namd	258	31.0	<b><u>258</u></b>	<b><u>31.0</u></b>	259	31.0						
447.dealII	<b><u>172</u></b>	<b><u>66.7</u></b>	171	66.8	172	66.5						
450.soplex	180	46.3	182	45.8	<b><u>181</u></b>	<b><u>46.0</u></b>						
453.povray	<b><u>84.4</u></b>	<b><u>63.0</u></b>	83.5	63.7	84.9	62.6						
454.calculix	<b><u>144</u></b>	<b><u>57.1</u></b>	144	57.2	144	57.1						
459.GemsFDTD	<b><u>43.3</u></b>	<b><u>245</u></b>	43.4	245	43.1	246						
465.tonto	207	47.4	<b><u>206</u></b>	<b><u>47.7</u></b>	206	47.7						
470.lbm	9.56	1440	9.31	1480	<b><u>9.42</u></b>	<b><u>1460</u></b>						
481.wrf	91.7	122	92.9	120	<b><u>92.3</u></b>	<b><u>121</u></b>						
482.sphinx3	236	82.7	<b><u>237</u></b>	<b><u>82.1</u></b>	242	80.7						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C6 State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

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## Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Intel Hyperthreading set to Disabled

Sysinfo program /home/experiment/fp/new/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-vi0i Mon May 16 23:12:40 2016

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E7-8891 v4 @ 2.80GHz
 4 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 10
  physical 0: cores 5 9 10 11 13 18 24 26 28 29
  physical 1: cores 5 9 10 11 13 18 24 26 28 29
  physical 2: cores 5 9 10 11 13 18 24 26 28 29
  physical 3: cores 5 9 10 11 13 18 24 26 28 29
cache size : 61440 KB
```

From /proc/meminfo

```
MemTotal:      529321304 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-vi0i 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

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## Platform Notes (Continued)

run-level 3 May 16 22:44

SPEC is set to: /home/experiment/fp/new/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p4	xfs	703G	34G	669G	5%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 04/26/2016

Memory:

64x UNKNOWN NOT AVAILABLE

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

OMP\_NUM\_THREADS = "40"

LD\_LIBRARY\_PATH = "/home/experiment/fp/new/cpu2006/libs/32:/home/experiment/fp/new/cpu2006/libs/64:/home/experiment/fp/new/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint -auto-ilp32

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-qopt-calloc

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint -auto-ilp32
-fp-model fast=2

```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jun 30 13:53:37 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 6 June 2016.