



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

**SPECfp<sup>®</sup>2006 = 112**

Express5800/R120g-2M (Intel Xeon E5-2630 v4)

**SPECfp\_base2006 = 106**

CPU2006 license: 9006

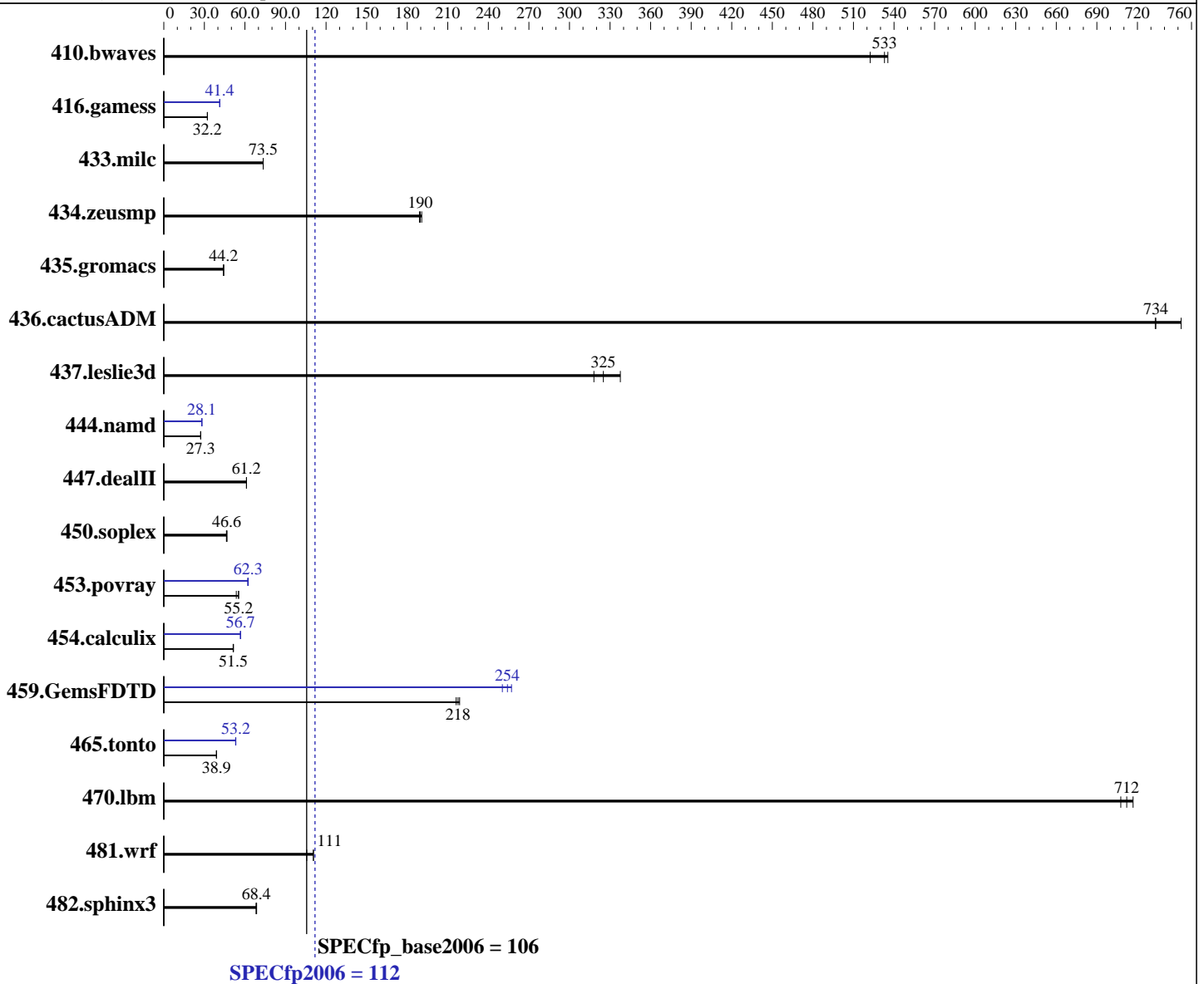
Test date: Apr-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



### Hardware

CPU Name: Intel Xeon E5-2630 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.0	522	<b>25.5</b>	<b>533</b>	25.4	535	26.0	522	<b>25.5</b>	<b>533</b>	25.4	535
416.gamess	<b>607</b>	<b>32.2</b>	608	32.2	607	32.2	473	41.4	<b>473</b>	<b>41.4</b>	473	41.4
433.milc	<b>125</b>	<b>73.5</b>	125	73.5	125	73.5	<b>125</b>	<b>73.5</b>	125	73.5	125	73.5
434.zeusmp	<b>48.0</b>	<b>190</b>	47.7	191	48.1	189	<b>48.0</b>	<b>190</b>	47.7	191	48.1	189
435.gromacs	162	44.2	161	44.3	<b>162</b>	<b>44.2</b>	162	44.2	161	44.3	<b>162</b>	<b>44.2</b>
436.cactusADM	<b>16.3</b>	<b>734</b>	15.9	752	16.3	733	<b>16.3</b>	<b>734</b>	15.9	752	16.3	733
437.leslie3d	29.6	318	<b>28.9</b>	<b>325</b>	27.8	338	29.6	318	<b>28.9</b>	<b>325</b>	27.8	338
444.namd	294	27.3	294	27.3	<b>294</b>	<b>27.3</b>	285	28.1	285	28.1	<b>285</b>	<b>28.1</b>
447.dealII	187	61.1	187	61.2	<b>187</b>	<b>61.2</b>	187	61.1	187	61.2	<b>187</b>	<b>61.2</b>
450.soplex	179	46.7	<b>179</b>	<b>46.6</b>	180	46.3	179	46.7	<b>179</b>	<b>46.6</b>	180	46.3
453.povray	<b>96.3</b>	<b>55.2</b>	96.1	55.3	99.2	53.6	<b>85.4</b>	<b>62.3</b>	85.7	62.1	85.1	62.5
454.calculix	<b>160</b>	<b>51.5</b>	160	51.5	161	51.3	<b>145</b>	<b>56.7</b>	146	56.7	145	56.8
459.GemsFDTD	49.1	216	48.5	219	<b>48.8</b>	<b>218</b>	<b>41.8</b>	<b>254</b>	42.4	250	41.3	257
465.tonto	253	38.9	<b>253</b>	<b>38.9</b>	252	39.1	185	53.1	<b>185</b>	<b>53.2</b>	185	53.3
470.lbm	<b>19.3</b>	<b>712</b>	19.4	708	19.2	717	<b>19.3</b>	<b>712</b>	19.4	708	19.2	717
481.wrf	<b>101</b>	<b>111</b>	106	106	101	111	<b>101</b>	<b>111</b>	106	106	101	111
482.sphinx3	<b>285</b>	<b>68.4</b>	285	68.3	284	68.7	<b>285</b>	<b>68.4</b>	285	68.3	284	68.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Power Management Policy: Custom  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Snoop Mode: Home Snoop with Directory  
 Hyper-Threading: Disabled



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

The Express5800/R120g-1M (Intel Xeon E5-2630 v4) and the Express5800/R120g-2M (Intel Xeon E5-2630 v4) models are electronically equivalent. The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2630 v4) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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