



# SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant XL420 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

SPECfp®2006 =

117

SPECfp\_base2006 =

111

CPU2006 license: 3

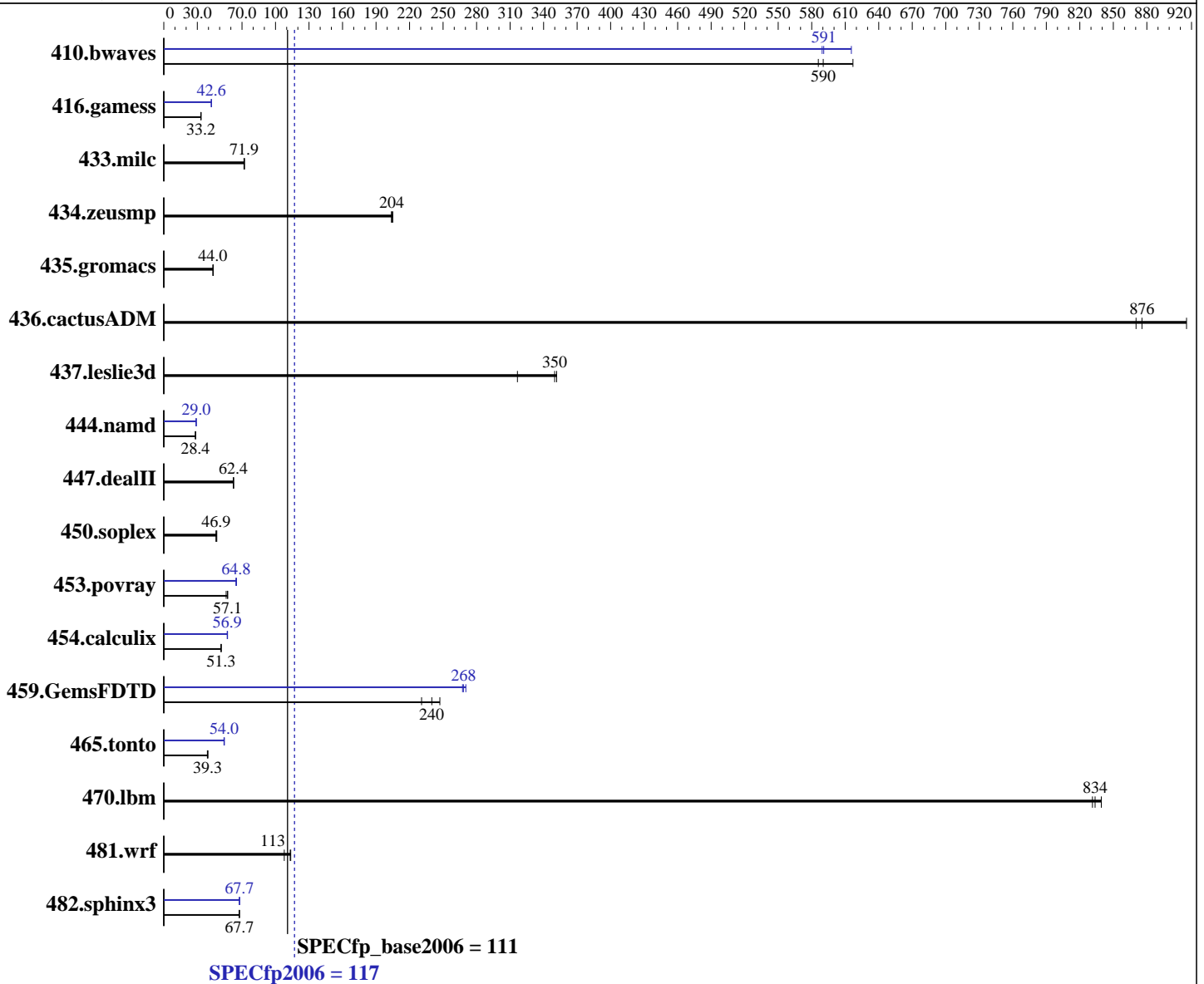
Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2660 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo), Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>23.0</u>	<u>590</u>	23.2	586	22.0	617	23.1	589	<u>23.0</u>	<u>591</u>	22.1	615
416.gamess	587	33.3	589	33.2	<u>589</u>	<u>33.2</u>	<u>460</u>	<u>42.6</u>	459	42.6	460	42.6
433.milc	128	71.8	127	72.3	<u>128</u>	<u>71.9</u>	128	71.8	127	72.3	<u>128</u>	<u>71.9</u>
434.zeusmp	44.7	204	44.4	205	<u>44.6</u>	<u>204</u>	44.7	204	44.4	205	<u>44.6</u>	<u>204</u>
435.gromacs	161	44.3	162	44.0	<u>162</u>	<u>44.0</u>	161	44.3	162	44.0	<u>162</u>	<u>44.0</u>
436.cactusADM	13.7	870	<u>13.6</u>	<u>876</u>	13.1	916	13.7	870	<u>13.6</u>	<u>876</u>	13.1	916
437.leslie3d	29.7	317	26.7	352	<u>26.9</u>	<u>350</u>	29.7	317	26.7	352	<u>26.9</u>	<u>350</u>
444.namd	283	28.4	<u>283</u>	<u>28.4</u>	283	28.4	277	29.0	276	29.0	<u>277</u>	<u>29.0</u>
447.dealII	<u>183</u>	<u>62.4</u>	183	62.6	184	62.2	<u>183</u>	<u>62.4</u>	183	62.6	184	62.2
450.soplex	<u>178</u>	<u>46.9</u>	176	47.5	178	46.8	<u>178</u>	<u>46.9</u>	176	47.5	178	46.8
453.povray	93.1	57.2	<u>93.2</u>	<u>57.1</u>	95.3	55.8	81.7	65.1	<u>82.1</u>	<u>64.8</u>	82.4	64.6
454.calculix	161	51.3	<u>161</u>	<u>51.3</u>	161	51.3	145	57.1	<u>145</u>	<u>56.9</u>	145	56.8
459.GemsFDTD	<u>44.2</u>	<u>240</u>	42.9	247	46.0	231	39.2	270	<u>39.6</u>	<u>268</u>	39.7	267
465.tonto	<u>250</u>	<u>39.3</u>	249	39.5	252	39.0	181	54.3	<u>182</u>	<u>54.0</u>	183	53.9
470.lbm	16.5	831	16.4	839	<u>16.5</u>	<u>834</u>	16.5	831	16.4	839	<u>16.5</u>	<u>834</u>
481.wrf	104	108	98.3	114	<u>98.7</u>	<u>113</u>	104	108	98.3	114	<u>98.7</u>	<u>113</u>
482.sphinx3	<u>288</u>	<u>67.7</u>	288	67.7	289	67.5	<u>288</u>	<u>67.7</u>	288	67.6	287	67.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Platform Notes

BIOS Configuration:  
Intel Hyperthreading set to Disabled  
Power Profile set to Custom  
Power Regulator set to HP Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C6 State  
Minimum Processor Idle Power Package C-State set to No Package State  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Home Snoop

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## Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on Pilot-XL420-G9 Wed Apr 27 12:09:17 2016

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
 2 "physical id"s (chips)
 28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 14
  siblings      : 14
  physical 0    : cores 0 2 4 5 6 8 9 10 11 12 13 14
  physical 1    : cores 0 2 4 5 6 8 9 10 11 12 13 14
  cache size    : 35840 KB
```

From /proc/meminfo

```
MemTotal:      263891508 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

uname -a:

```
Linux Pilot-XL420-G9 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 27 11:55

SPEC is set to: /home/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
Continued on next page
```



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## Platform Notes (Continued)

/dev/sda3 ext4 361G 21G 322G 7% /  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U19 04/12/2016

Memory:

8x UNKNOWN NOT AVAILABLE

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:  
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "28"

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel  
-opt-prefetch -ansi-alias  
-fp-model fast=2  
-qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel  
-opt-prefetch  
-fp-model fast=2  
-qopt-prefetch-issue-excl-hint -funroll-all-loops

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## Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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