



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp®2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

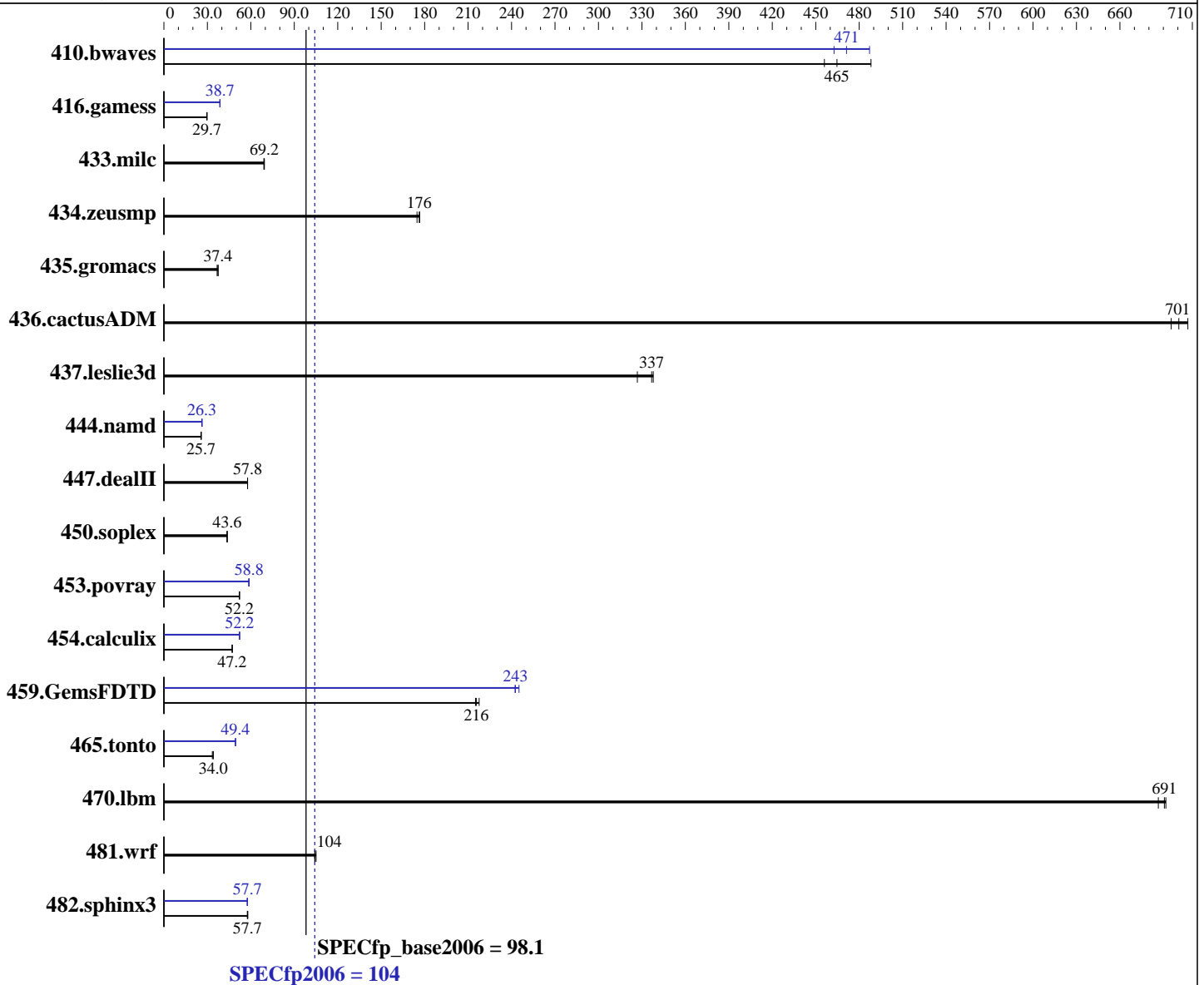
Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2630L v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
 Kernel 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>29.2</u>	<u>465</u>	27.8	488	29.8	456	29.4	463	27.9	487	<u>28.8</u>	<u>471</u>
416.gamess	<u>659</u>	<u>29.7</u>	659	29.7	660	29.7	506	38.7	506	38.7	<u>506</u>	<u>38.7</u>
433.milc	132	69.4	<u>133</u>	<u>69.2</u>	133	69.0	132	69.4	<u>133</u>	<u>69.2</u>	133	69.0
434.zeusmp	<u>51.6</u>	<u>176</u>	52.1	175	51.5	177	<u>51.6</u>	<u>176</u>	52.1	175	51.5	177
435.gromacs	191	37.4	<u>191</u>	<u>37.4</u>	194	36.7	191	37.4	<u>191</u>	<u>37.4</u>	194	36.7
436.cactusADM	17.2	695	<u>17.1</u>	<u>701</u>	16.9	707	17.2	695	<u>17.1</u>	<u>701</u>	16.9	707
437.leslie3d	27.8	338	28.8	327	<u>27.9</u>	<u>337</u>	27.8	338	28.8	327	<u>27.9</u>	<u>337</u>
444.namd	<u>312</u>	<u>25.7</u>	312	25.7	312	25.7	305	26.3	305	26.3	<u>305</u>	<u>26.3</u>
447.dealII	198	57.7	<u>198</u>	<u>57.8</u>	198	57.8	198	57.7	<u>198</u>	<u>57.8</u>	198	57.8
450.soplex	191	43.7	<u>191</u>	<u>43.6</u>	191	43.6	191	43.7	<u>191</u>	<u>43.6</u>	191	43.6
453.povray	<u>102</u>	<u>52.2</u>	102	52.2	102	52.1	91.0	58.5	<u>90.5</u>	<u>58.8</u>	90.4	58.9
454.calculix	<u>175</u>	<u>47.2</u>	175	47.1	175	47.2	<u>158</u>	<u>52.2</u>	158	52.3	158	52.2
459.GemsFDTD	49.3	215	<u>49.2</u>	<u>216</u>	48.8	218	43.8	242	<u>43.7</u>	<u>243</u>	43.3	245
465.tonto	<u>289</u>	<u>34.0</u>	289	34.1	294	33.4	199	49.4	199	49.4	<u>199</u>	<u>49.4</u>
470.lbm	20.0	687	<u>19.9</u>	<u>691</u>	19.9	692	20.0	687	<u>19.9</u>	<u>691</u>	19.9	692
481.wrf	107	104	106	105	<u>107</u>	<u>104</u>	107	104	106	105	<u>107</u>	<u>104</u>
482.sphinx3	336	57.9	<u>338</u>	<u>57.7</u>	338	57.6	339	57.5	<u>338</u>	<u>57.7</u>	338	57.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 2



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9
(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/new_fp/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
running on DL380Gen9allbin Sun Apr 17 21:37:01 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 10
  siblings    : 20
  physical 0   : cores 0 1 2 3 4 8 9 10 11 12
  physical 1   : cores 0 1 2 3 4 8 9 10 11 12
cache size    : 25600 KB
```

From /proc/meminfo

```
MemTotal:      528065708 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/*release* /etc/*version*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

uname -a:

```
Linux DL380Gen9allbin 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 17 21:25

SPEC is set to: /home/new_fp/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
Continued on next page
```



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

Platform Notes (Continued)

/dev/sda5 xfs 318G 88G 231G 28% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 03/23/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

OMP_NUM_THREADS = "20"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 5



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 =

104

SPECfp_base2006 =

98.1

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Apr-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue May 17 16:51:09 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 May 2016.