



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY TX1330 M2, Intel Xeon E3-1270 v5, 3.60 GHz

SPECfp®2006 = 100

SPECfp_base2006 = 98.0

CPU2006 license: 19

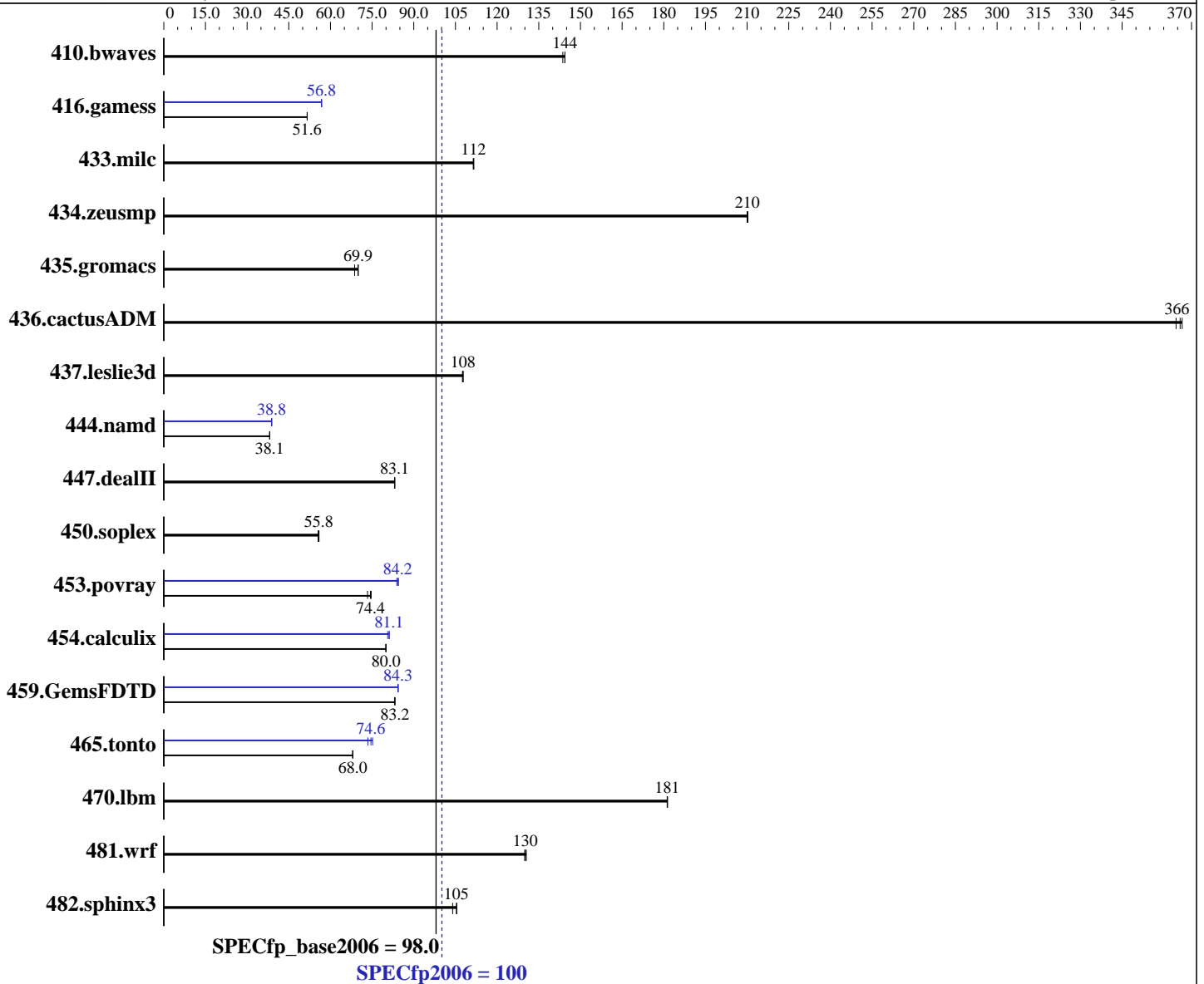
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2015

Hardware Availability: Feb-2016

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E3-1270 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.48-52.27-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2133P-E)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	94.1	144	94.6	144	<u>94.1</u>	<u>144</u>	94.1	144	94.6	144	<u>94.1</u>	<u>144</u>
416.gamess	379	51.6	379	51.6	<u>379</u>	<u>51.6</u>	345	56.8	345	56.8	<u>345</u>	<u>56.8</u>
433.milc	82.3	112	<u>82.3</u>	<u>112</u>	82.4	111	82.3	112	<u>82.3</u>	<u>112</u>	82.4	111
434.zeusmp	43.3	210	<u>43.3</u>	<u>210</u>	43.3	210	43.3	210	<u>43.3</u>	<u>210</u>	43.3	210
435.gromacs	104	68.7	102	70.0	<u>102</u>	<u>69.9</u>	104	68.7	102	70.0	<u>102</u>	<u>69.9</u>
436.cactusADM	32.6	367	32.8	364	<u>32.7</u>	<u>366</u>	32.6	367	32.8	364	<u>32.7</u>	<u>366</u>
437.leslie3d	<u>87.3</u>	<u>108</u>	87.4	108	87.2	108	<u>87.3</u>	<u>108</u>	87.4	108	87.2	108
444.namd	<u>210</u>	<u>38.1</u>	210	38.1	210	38.1	<u>206</u>	<u>38.8</u>	207	38.8	206	38.9
447.dealII	138	83.2	138	83.1	<u>138</u>	<u>83.1</u>	138	83.2	138	83.1	<u>138</u>	<u>83.1</u>
450.soplex	150	55.6	<u>149</u>	<u>55.8</u>	149	55.9	150	55.6	<u>149</u>	<u>55.8</u>	149	55.9
453.povray	<u>71.5</u>	<u>74.4</u>	71.3	74.7	72.5	73.4	63.4	83.9	63.0	84.5	<u>63.2</u>	<u>84.2</u>
454.calculix	103	79.9	103	80.0	<u>103</u>	<u>80.0</u>	102	81.1	<u>102</u>	<u>81.1</u>	102	80.6
459.GemsFDTD	128	83.2	128	83.2	<u>128</u>	<u>83.2</u>	126	84.3	<u>126</u>	<u>84.3</u>	126	84.4
465.tonto	145	68.0	<u>145</u>	<u>68.0</u>	145	68.1	131	75.2	134	73.5	<u>132</u>	<u>74.6</u>
470.lbm	75.8	181	75.8	181	<u>75.8</u>	<u>181</u>	75.8	181	75.8	181	<u>75.8</u>	<u>181</u>
481.wrf	<u>85.8</u>	<u>130</u>	85.6	131	86.0	130	<u>85.8</u>	<u>130</u>	85.6	131	86.0	130
482.sphinx3	187	104	185	105	<u>185</u>	<u>105</u>	187	104	185	105	<u>185</u>	<u>105</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on TX1330M2 Sat Dec 12 16:42:31 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz

1 "physical id"s (chips)
8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo

MemTotal: 32806336 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0

This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:

Linux TX1330M2 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Dec 12 12:37

SPEC is set to: /home/SPECcpu2006

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 237G 15G 223G 7% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.1.0 for D3373-A1x 10/30/2015

Memory:

2x Not Specified Not Specified

2x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -noFOR_main

436.cactusADM: -DSPEC_CPU_LP64 -noFOR_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact webmaster@spec.org.

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