



# SPEC® CFP2006 Result

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## Supermicro

Supermicro C7H170-M motherboard  
(C7H170-M , Intel Core i5-6500)

SPECfp®2006 = 88.0

SPECfp\_base2006 = 86.5

CPU2006 license: 001176

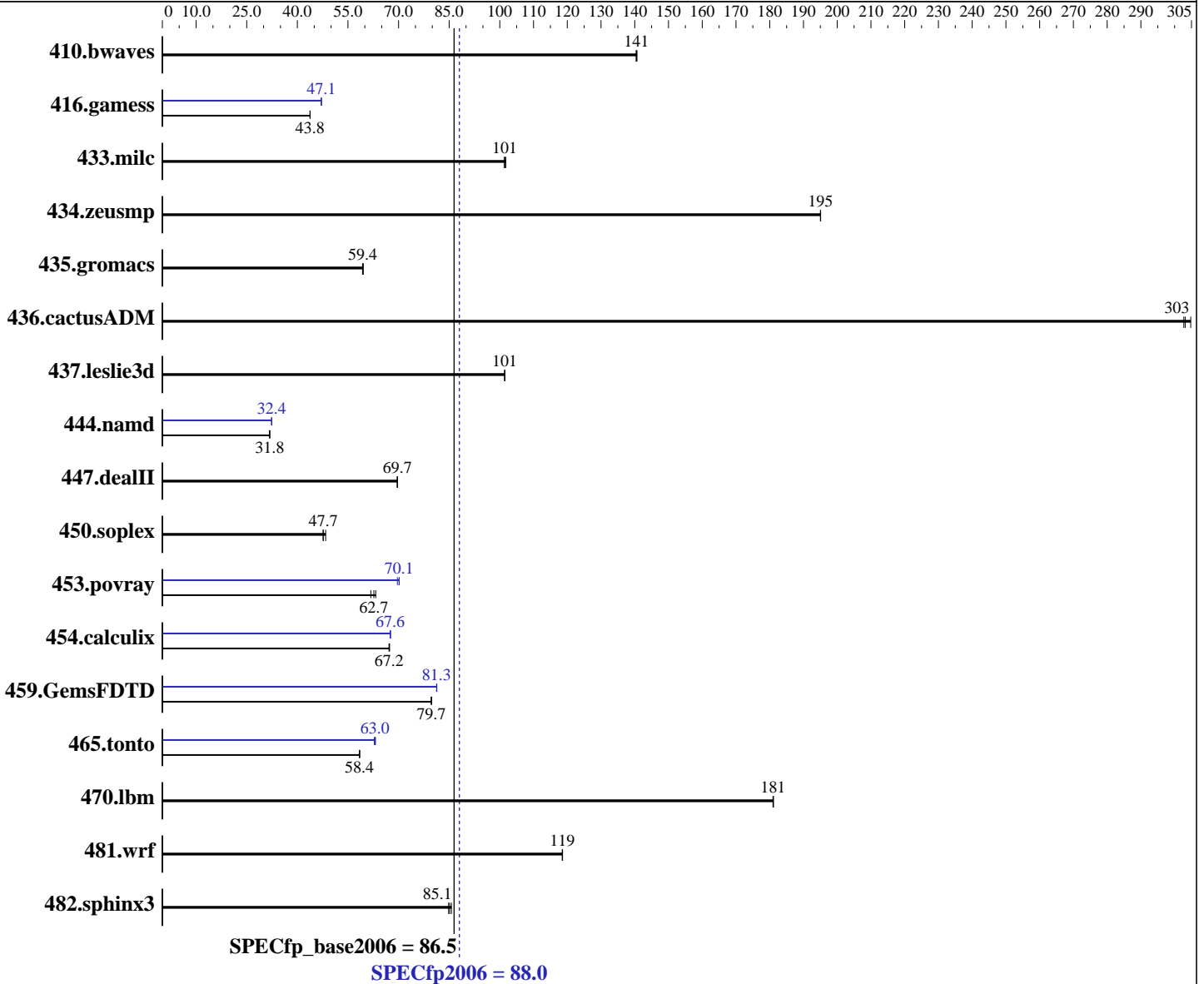
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Core i5-6500  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 6 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2666P-U, running at 2133 MHz)  
Disk Subsystem: 1 x 200 GB SATA III SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>96.7</b>	<b>141</b>	96.6	141	96.8	140	<b>96.7</b>	<b>141</b>	96.6	141	96.8	140
416.gamess	447	43.8	447	43.8	<b>447</b>	<b>43.8</b>	415	47.1	<b>416</b>	<b>47.1</b>	416	47.1
433.milc	90.6	101	90.2	102	<b>90.5</b>	<b>101</b>	90.6	101	90.2	102	<b>90.5</b>	<b>101</b>
434.zeusmp	46.6	195	<b>46.7</b>	<b>195</b>	46.7	195	46.6	195	<b>46.7</b>	<b>195</b>	46.7	195
435.gromacs	120	59.3	120	59.5	<b>120</b>	<b>59.4</b>	120	59.3	120	59.5	<b>120</b>	<b>59.4</b>
436.cactusADM	39.5	303	39.2	305	<b>39.4</b>	<b>303</b>	39.5	303	39.2	305	<b>39.4</b>	<b>303</b>
437.leslie3d	92.6	102	92.7	101	<b>92.6</b>	<b>101</b>	92.6	102	92.7	101	<b>92.6</b>	<b>101</b>
444.namd	252	31.8	252	31.8	<b>252</b>	<b>31.8</b>	247	32.4	248	32.3	<b>247</b>	<b>32.4</b>
447.dealII	<b>164</b>	<b>69.7</b>	164	69.6	164	69.7	<b>164</b>	<b>69.7</b>	164	69.6	164	69.7
450.soplex	<b>175</b>	<b>47.7</b>	172	48.4	175	47.6	<b>175</b>	<b>47.7</b>	172	48.4	175	47.6
453.povray	86.1	61.8	<b>84.9</b>	<b>62.7</b>	84.1	63.2	<b>75.9</b>	<b>70.1</b>	75.8	70.2	76.4	69.6
454.calculix	123	67.3	<b>123</b>	<b>67.2</b>	123	67.2	122	67.6	<b>122</b>	<b>67.6</b>	122	67.5
459.GemsFDTD	133	79.8	<b>133</b>	<b>79.7</b>	133	79.7	131	81.3	<b>131</b>	<b>81.3</b>	130	81.3
465.tonto	169	58.4	168	58.5	<b>168</b>	<b>58.4</b>	<b>156</b>	<b>63.0</b>	156	63.2	157	62.8
470.lbm	<b>75.9</b>	<b>181</b>	75.9	181	75.9	181	<b>75.9</b>	<b>181</b>	75.9	181	75.9	181
481.wrf	94.2	119	94.3	119	<b>94.2</b>	<b>119</b>	94.2	119	94.3	119	<b>94.2</b>	<b>119</b>
482.sphinx3	<b>229</b>	<b>85.1</b>	228	85.7	230	84.8	<b>229</b>	<b>85.1</b>	228	85.7	230	84.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-743TQ-865B-SQ chassis.  
The chassis is configured with a PWS-865-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0103L4 rear fan and 2 FAN-0104L4 chassis fan.  
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on C7H170-01 Tue Dec 15 03:05:22 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Software Availability: Sep-2015

## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6500 CPU @ 3.20GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
cache size : 6144 KB

```

```

From /proc/meminfo
MemTotal:      16209832 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

```

```

uname -a:
Linux C7H170-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 14 22:12

```

SPEC is set to: /usr/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       xfs       183G      44G  139G  25% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0c 12/09/2015

Memory:  
4x 0420 F4-2666C15-4GRR 4 GB 1 rank 2133 MHz

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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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**Tested by:** Supermicro

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**Hardware Availability:** Sep-2015

**Software Availability:** Sep-2015

## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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