



# SPEC<sup>®</sup> CFP2006 Result

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## Supermicro

SuperServer 5019S-M  
(X11SSH-F, Intel Xeon E3-1260L v5)

SPECfp<sup>®</sup>2006 = 99.2

SPECfp\_base2006 = 96.9

CPU2006 license: 001176

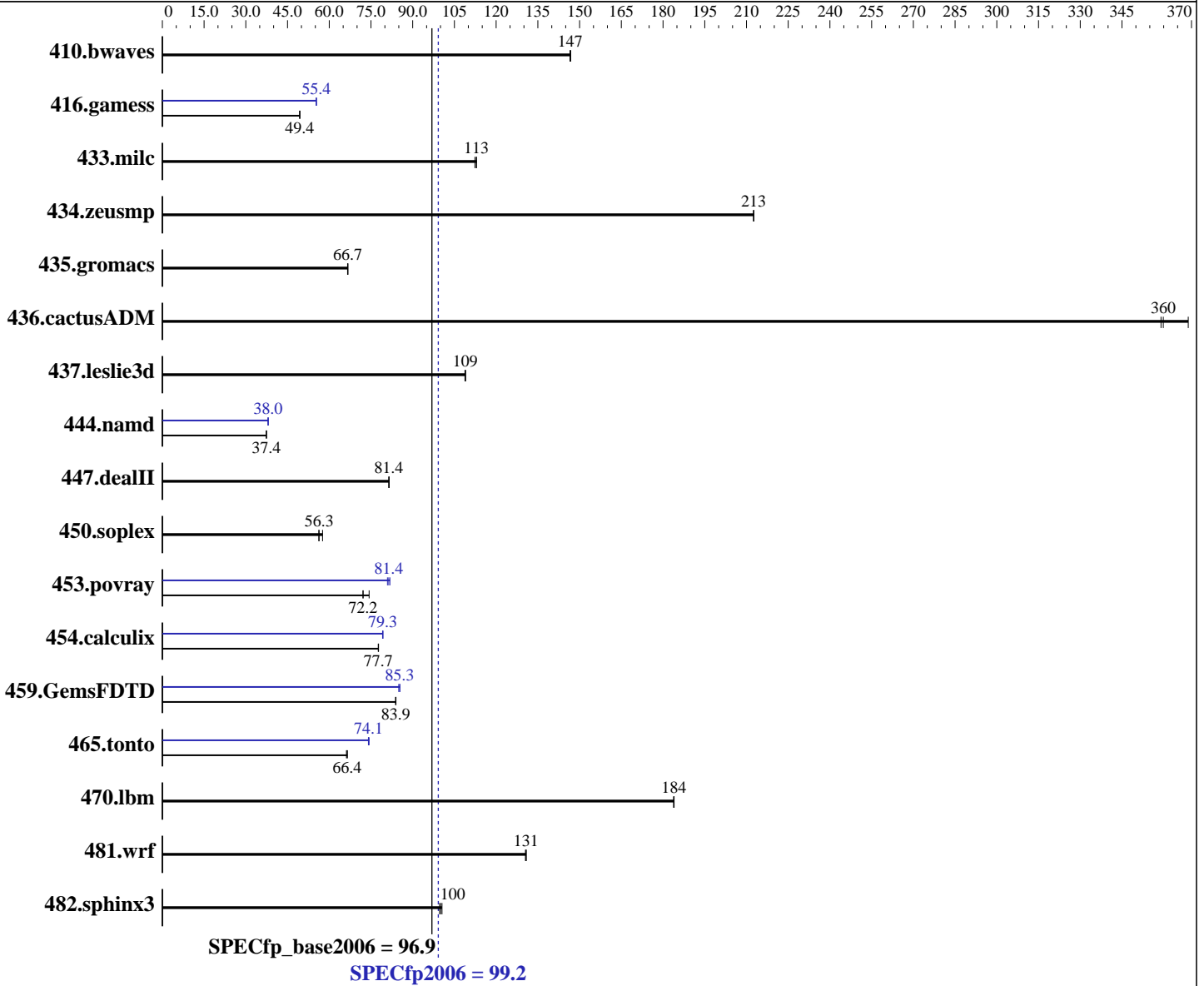
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E3-1260L v5  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 2900  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.2  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-U)  
Disk Subsystem: 1 x 400 GB SATA III SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base        |             |             |             |             |             | Peak        |             |             |             |             |             |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
|               | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       |
| 410.bwaves    | 92.6        | 147         | <b>92.7</b> | <b>147</b>  | 92.7        | 147         | 92.6        | 147         | <b>92.7</b> | <b>147</b>  | 92.7        | 147         |
| 416.gamess    | 396         | 49.4        | <b>396</b>  | <b>49.4</b> | 396         | 49.4        | 354         | 55.4        | <b>354</b>  | <b>55.4</b> | 354         | 55.3        |
| 433.milc      | 81.7        | 112         | 81.3        | 113         | <b>81.3</b> | <b>113</b>  | 81.7        | 112         | 81.3        | 113         | <b>81.3</b> | <b>113</b>  |
| 434.zeusmp    | 42.8        | 213         | <b>42.8</b> | <b>213</b>  | 42.8        | 213         | 42.8        | 213         | <b>42.8</b> | <b>213</b>  | 42.8        | 213         |
| 435.gromacs   | 107         | 66.6        | <b>107</b>  | <b>66.7</b> | 107         | 66.7        | 107         | 66.6        | <b>107</b>  | <b>66.7</b> | 107         | 66.7        |
| 436.cactusADM | 33.3        | 359         | <b>33.2</b> | <b>360</b>  | 32.4        | 369         | 33.3        | 359         | <b>33.2</b> | <b>360</b>  | 32.4        | 369         |
| 437.leslie3d  | <b>86.3</b> | <b>109</b>  | 86.2        | 109         | 86.4        | 109         | <b>86.3</b> | <b>109</b>  | 86.2        | 109         | 86.4        | 109         |
| 444.namd      | <b>214</b>  | <b>37.4</b> | 214         | 37.4        | 214         | 37.4        | 211         | 38.0        | 211         | 38.1        | <b>211</b>  | <b>38.0</b> |
| 447.dealII    | 141         | 81.4        | 140         | 81.6        | <b>140</b>  | <b>81.4</b> | 141         | 81.4        | 140         | 81.6        | <b>140</b>  | <b>81.4</b> |
| 450.soplex    | 148         | 56.2        | 145         | 57.6        | <b>148</b>  | <b>56.3</b> | 148         | 56.2        | 145         | 57.6        | <b>148</b>  | <b>56.3</b> |
| 453.povray    | 71.6        | 74.3        | 73.8        | 72.1        | <b>73.7</b> | <b>72.2</b> | <b>65.4</b> | <b>81.4</b> | 65.7        | 80.9        | 65.0        | 81.8        |
| 454.calculix  | 106         | 77.7        | <b>106</b>  | <b>77.7</b> | 106         | 77.6        | 104         | 79.3        | <b>104</b>  | <b>79.3</b> | 104         | 79.2        |
| 459.GemsFDTD  | 126         | 84.0        | 127         | 83.8        | <b>126</b>  | <b>83.9</b> | 124         | 85.4        | <b>124</b>  | <b>85.3</b> | 125         | 85.0        |
| 465.tonto     | 148         | 66.5        | 149         | 66.2        | <b>148</b>  | <b>66.4</b> | <b>133</b>  | <b>74.1</b> | 132         | 74.3        | 133         | 74.1        |
| 470.lbm       | 74.7        | 184         | 74.8        | 184         | <b>74.7</b> | <b>184</b>  | 74.7        | 184         | 74.8        | 184         | <b>74.7</b> | <b>184</b>  |
| 481.wrf       | 85.4        | 131         | 85.6        | 131         | <b>85.4</b> | <b>131</b>  | 85.4        | 131         | 85.6        | 131         | <b>85.4</b> | <b>131</b>  |
| 482.sphinx3   | <b>195</b>  | <b>100</b>  | 194         | 101         | 196         | 99.7        | <b>195</b>  | <b>100</b>  | 194         | 101         | 196         | 99.7        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Hyper-threading = Disabled  
Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Mon Nov 30 17:34:18 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

**Test date:** Nov-2015  
**Hardware Availability:** Oct-2015  
**Software Availability:** Sep-2015

### Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1260L v5 @ 2.90GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
 cache size : 8192 KB

```

```

From /proc/meminfo
MemTotal:      16086916 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 30 13:18

```

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs   280G  8.3G  271G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS American Megatrends Inc. 1.0a 11/13/2015
Memory:
 2x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz
 2x Not Specified Not Specified

```

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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "4"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias  
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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