



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 92.4

PowerEdge T330 (Intel Xeon E3-1220 v5, 3.00 GHz)

SPECfp_base2006 = 90.4

CPU2006 license: 55

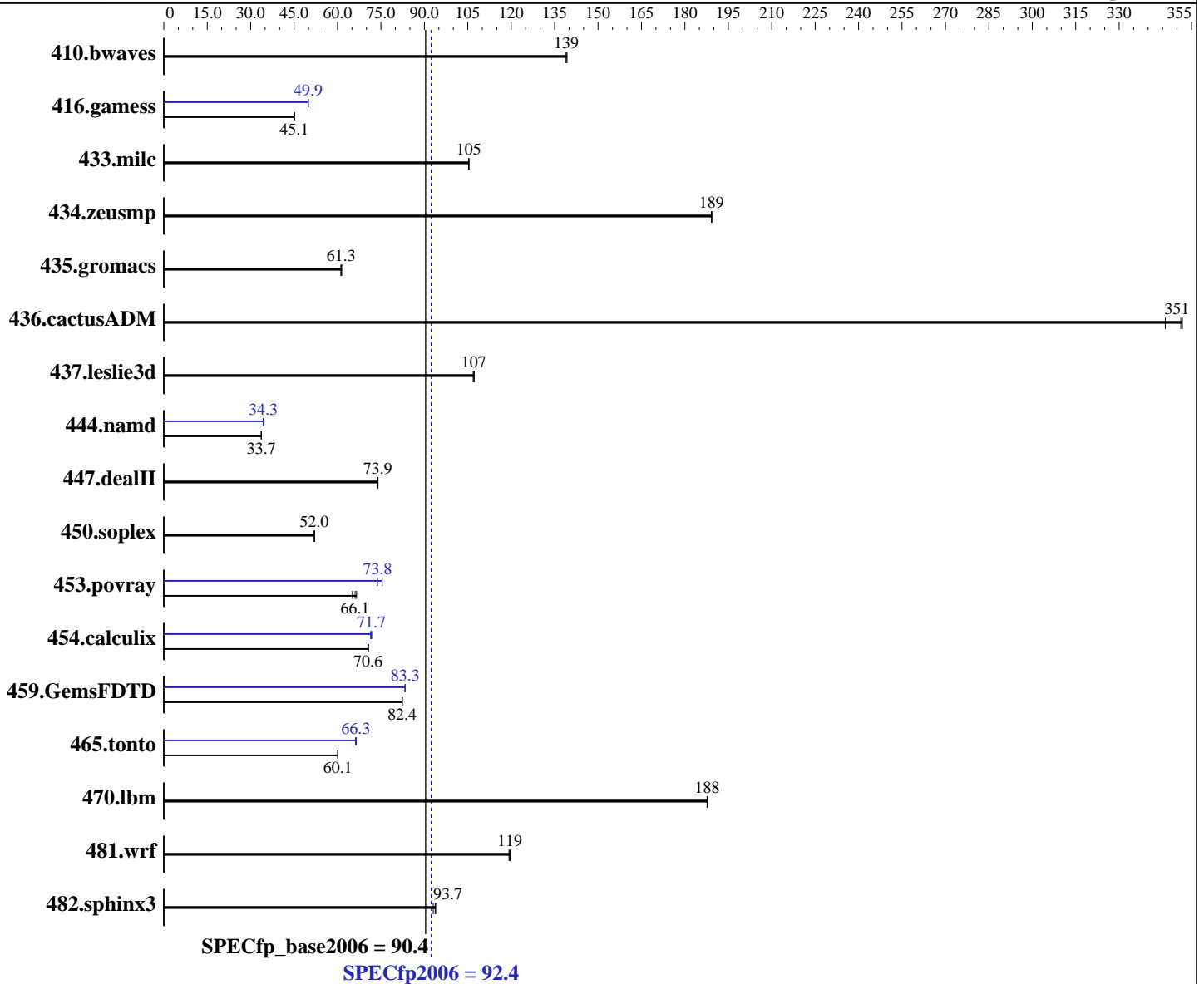
Test date: Sep-2015

Test sponsor: Dell Inc.

Hardware Availability: Nov-2015

Tested by: Dell Inc.

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E3-1220 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12
 3.12.28-4-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE
 for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran
 Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 multi-user

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 500 GB 7200 RPM SATA
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>97.7</u>	<u>139</u>	97.9	139	97.6	139	<u>97.7</u>	<u>139</u>	97.9	139	97.6	139
416.gamess	<u>434</u>	<u>45.1</u>	434	45.1	434	45.1	<u>392</u>	<u>49.9</u>	392	49.9	392	49.9
433.milc	87.0	105	<u>87.1</u>	<u>105</u>	87.2	105	87.0	105	<u>87.1</u>	<u>105</u>	87.2	105
434.zeusmp	48.1	189	48.1	189	<u>48.1</u>	<u>189</u>	48.1	189	48.1	189	<u>48.1</u>	<u>189</u>
435.gromacs	<u>116</u>	<u>61.3</u>	117	61.2	116	61.5	<u>116</u>	<u>61.3</u>	117	61.2	116	61.5
436.cactusADM	34.0	352	<u>34.0</u>	<u>351</u>	34.5	346	34.0	352	<u>34.0</u>	<u>351</u>	34.5	346
437.leslie3d	88.0	107	<u>87.8</u>	<u>107</u>	87.7	107	88.0	107	<u>87.8</u>	<u>107</u>	87.7	107
444.namd	238	33.7	<u>238</u>	<u>33.7</u>	238	33.7	234	34.3	233	34.4	<u>234</u>	<u>34.3</u>
447.dealII	155	73.9	155	74.0	<u>155</u>	<u>73.9</u>	155	73.9	155	74.0	<u>155</u>	<u>73.9</u>
450.soplex	160	52.1	<u>160</u>	<u>52.0</u>	161	51.8	160	52.1	<u>160</u>	<u>52.0</u>	161	51.8
453.povray	<u>80.5</u>	<u>66.1</u>	81.6	65.2	79.9	66.6	72.2	73.7	<u>72.1</u>	<u>73.8</u>	70.5	75.5
454.calculix	<u>117</u>	<u>70.6</u>	117	70.7	117	70.5	<u>115</u>	<u>71.7</u>	115	71.8	116	71.4
459.GemsFDTD	129	82.4	<u>129</u>	<u>82.4</u>	129	82.3	127	83.4	127	83.3	<u>127</u>	<u>83.3</u>
465.tonto	<u>164</u>	<u>60.1</u>	164	60.1	164	60.1	<u>148</u>	<u>66.3</u>	148	66.5	149	66.2
470.lbm	<u>73.2</u>	<u>188</u>	73.2	188	73.2	188	<u>73.2</u>	<u>188</u>	73.2	188	73.2	188
481.wrf	93.4	120	93.7	119	<u>93.5</u>	<u>119</u>	93.4	120	93.7	119	<u>93.5</u>	<u>119</u>
482.sphinx3	207	93.9	209	93.1	<u>208</u>	<u>93.7</u>	207	93.9	209	93.1	<u>208</u>	<u>93.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-oe1 Tue Sep 22 11:27:59 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1220 v5 @ 3.00GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
cache size : 8192 KB

```

```

From /proc/meminfo
MemTotal:      66066084 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

```

```

uname -a:
Linux linux-oc1 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 22 06:47

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  451G   10G  440G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.16 09/09/2015

Memory:

1x 00AD00000000 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

2x 00AD0000020B HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

1x 00AD00000800 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.lelie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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