



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant BL460c Gen9
(3.40 GHz, Intel Xeon E5-2643 v3)

SPECfp®2006 = 113

SPECfp_base2006 = 109

CPU2006 license: 3

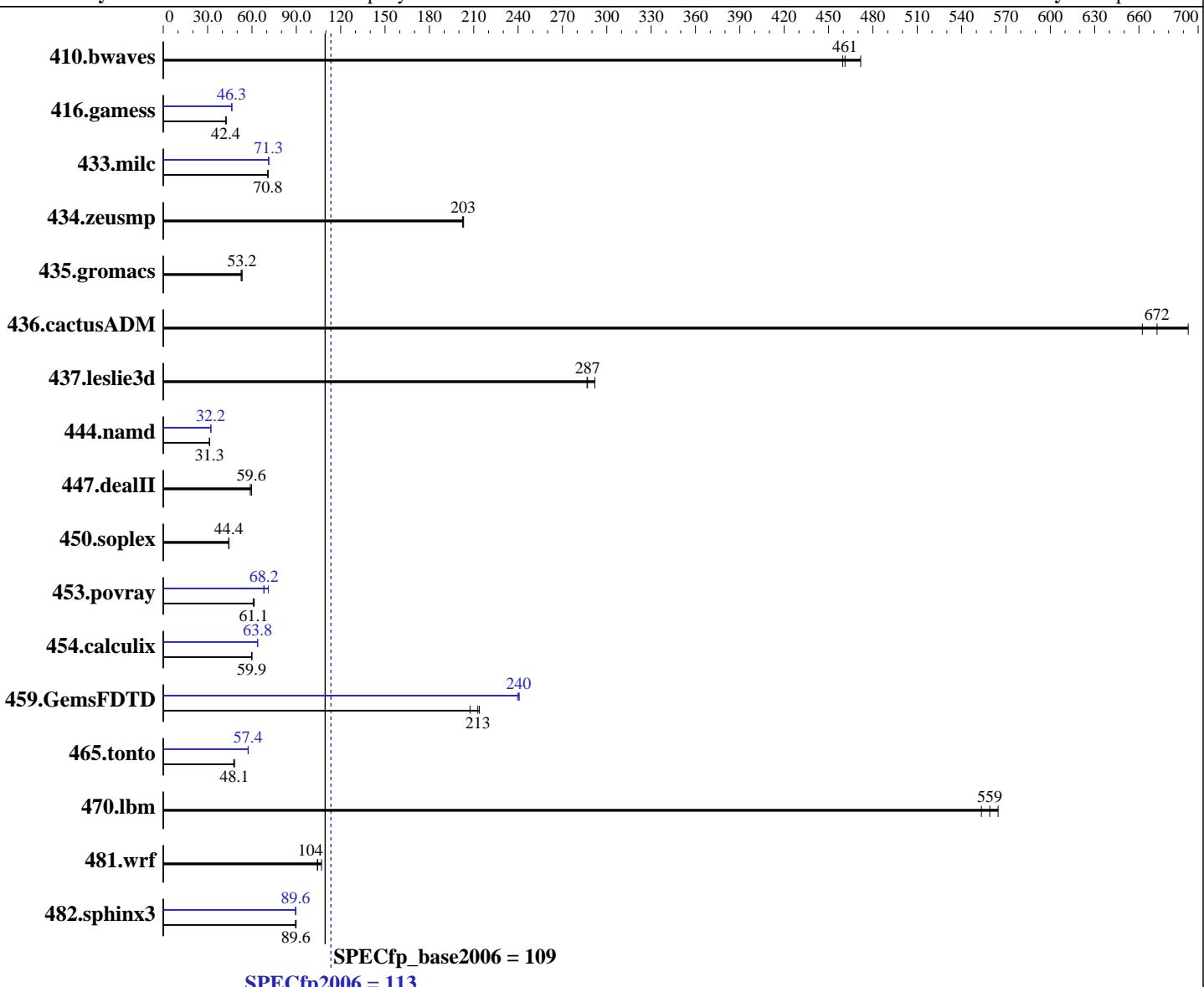
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Sep-2015

Hardware Availability: May-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2643 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: Kernel 3.10.0-123.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	29.5	461	29.6	460	28.8	472	29.5	461	29.6	460	28.8	472
416.gamess	462	42.4	460	42.6	462	42.4	422	46.4	423	46.3	423	46.3
433.milc	130	70.7	130	70.8	130	70.8	129	71.2	129	71.3	129	71.4
434.zeusmp	44.9	203	45.0	202	44.8	203	44.9	203	45.0	202	44.8	203
435.gromacs	134	53.3	134	53.2	136	52.7	134	53.3	134	53.2	136	52.7
436.cactusADM	17.8	672	18.0	662	17.2	693	17.8	672	18.0	662	17.2	693
437.leslie3d	32.2	292	32.8	287	32.8	287	32.2	292	32.8	287	32.8	287
444.namd	256	31.3	256	31.3	256	31.3	249	32.2	249	32.2	249	32.2
447.dealII	192	59.7	194	58.8	192	59.6	192	59.7	194	58.8	192	59.6
450.soplex	188	44.4	188	44.3	188	44.4	188	44.4	188	44.3	188	44.4
453.povray	86.4	61.5	87.5	60.8	87.0	61.1	74.9	71.1	78.2	68.0	78.0	68.2
454.calculix	137	60.0	138	59.7	138	59.9	129	63.9	129	63.8	129	63.7
459.GemsFDTD	49.6	214	51.1	208	49.9	213	44.3	240	44.1	240	44.1	241
465.tonto	206	47.7	204	48.1	204	48.1	171	57.4	172	57.4	171	57.5
470.lbm	24.6	559	24.3	565	24.8	553	24.6	559	24.3	565	24.8	553
481.wrf	104	107	107	104	107	104	104	107	107	104	107	104
482.sphinx3	218	89.5	217	89.6	217	89.9	218	89.6	218	89.6	218	89.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled

HP Power Profile set to Custom

HP Power Regulator set to HP Static High Performance Mode

Minimum Processor Idle Power Core State set to C6 State

Minimum Processor Idle Power Package State set to No Package State

Energy/Performance Bias set to Maximum Performance

Collaborative Power Control set to Disabled

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Platform Notes (Continued)

QPI Snoop Configuration set to Home Snoop
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on BL460c.Gen9-CPU2006 Thu Sep 24 03:56:44 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      263847176 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux BL460c.Gen9-CPU2006 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT
2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 24 03:49

SPEC is set to: /cpu2006

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Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-root	xfs	368G	6.6G	361G	2%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 05/06/2015

Memory:

```
8x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2133 MHz
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:

```
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2133 MHz
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/libs/32:/cpu2006/lib64:/cpu2006/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

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Sep-2015

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Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias
              -parallel
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
             -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
              -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -inline-calloc -opt-malloc-options=3 -auto -unroll14
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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