



# SPEC® CFP2006 Result

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## Huawei

**SPECfp®2006 = 109**

Huawei RH2288 V3 (Intel Xeon E5-2698 v3)

**SPECfp\_base2006 = 103**

CPU2006 license: 3175

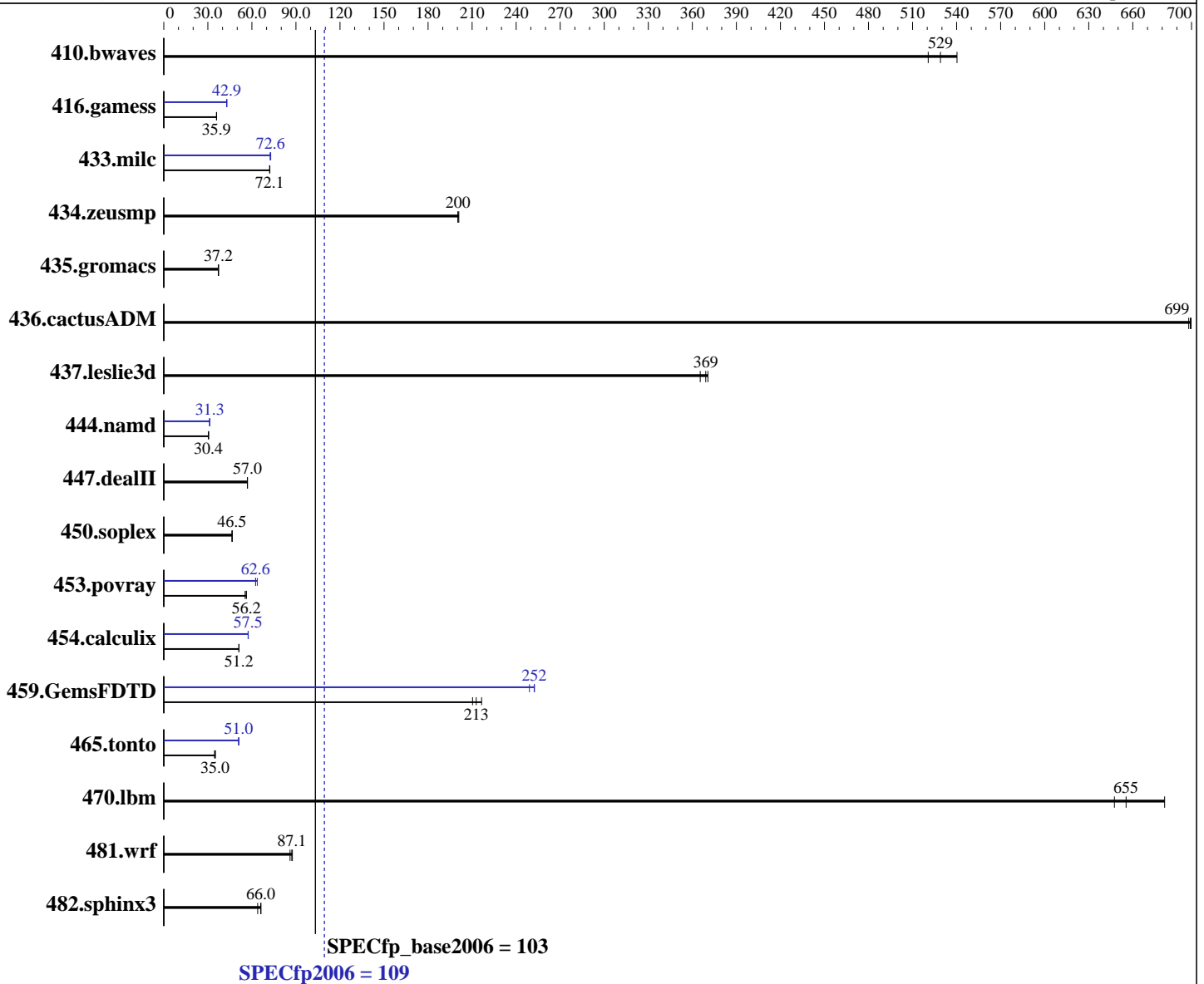
Test date: Aug-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014



**Hardware**

CPU Name: Intel Xeon E5-2698 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 40 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.2	540	26.1	521	<u>25.7</u>	<u>529</u>	25.2	540	26.1	521	<u>25.7</u>	<u>529</u>
416.gamess	<u>545</u>	<u>35.9</u>	545	35.9	545	35.9	457	42.8	456	42.9	<u>457</u>	<u>42.9</u>
433.milc	127	72.1	<u>127</u>	<u>72.1</u>	127	72.2	<u>126</u>	<u>72.6</u>	127	72.5	126	72.8
434.zeusmp	<u>45.4</u>	<u>200</u>	45.4	200	45.3	201	<u>45.4</u>	<u>200</u>	45.4	200	45.3	201
435.gromacs	<u>192</u>	<u>37.2</u>	192	37.1	192	37.3	<u>192</u>	<u>37.2</u>	192	37.1	192	37.3
436.cactusADM	<u>17.1</u>	<u>699</u>	17.1	698	17.1	700	<u>17.1</u>	<u>699</u>	17.1	698	17.1	700
437.leslie3d	25.7	365	<u>25.5</u>	<u>369</u>	25.4	371	25.7	365	<u>25.5</u>	<u>369</u>	25.4	371
444.namd	<u>264</u>	<u>30.4</u>	264	30.4	263	30.5	256	31.3	257	31.3	<u>256</u>	<u>31.3</u>
447.dealII	201	56.9	<u>201</u>	<u>57.0</u>	201	57.0	201	56.9	<u>201</u>	<u>57.0</u>	201	57.0
450.soplex	179	46.7	180	46.4	<u>179</u>	<u>46.5</u>	179	46.7	180	46.4	<u>179</u>	<u>46.5</u>
453.povray	<u>94.7</u>	<u>56.2</u>	94.6	56.2	96.1	55.4	83.6	63.7	85.1	62.5	<u>85.0</u>	<u>62.6</u>
454.calculix	161	51.2	<u>161</u>	<u>51.2</u>	161	51.2	144	57.4	143	57.5	<u>144</u>	<u>57.5</u>
459.GemsFDTD	50.5	210	49.0	216	<u>49.9</u>	<u>213</u>	42.6	249	<u>42.1</u>	<u>252</u>	42.0	252
465.tonto	<u>281</u>	<u>35.0</u>	280	35.1	285	34.6	192	51.1	193	51.0	<u>193</u>	<u>51.0</u>
470.lbm	<u>21.0</u>	<u>655</u>	20.2	682	21.2	647	<u>21.0</u>	<u>655</u>	20.2	682	21.2	647
481.wrf	128	87.6	<u>128</u>	<u>87.1</u>	130	86.0	128	87.6	<u>128</u>	<u>87.1</u>	130	86.0
482.sphinx3	294	66.2	305	63.9	<u>295</u>	<u>66.0</u>	294	66.2	305	63.9	<u>295</u>	<u>66.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to ES  
 Set Hyper-Threading to Disabled  
 Set Patrol Scrub to Disable  
 Baseboard Management Controller used to adjust the fan speed to 100%  
 Sysinfo program /spec15/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on localhost.localdomain Mon Aug 3 17:48:31 2015

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Test date: Aug-2015  
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Software Availability: Sep-2014

## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2698 v3 @ 2.30GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 16
  siblings       : 16
  physical 0:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1:    cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size      : 40960 KB
```

```
From /proc/meminfo
MemTotal:        263577516 kB
HugePages_Total: 0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 3 12:55
```

```
SPEC is set to: /spec15
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  448G  177G  249G  42% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS Insyde Corp. 1.26 12/22/2014

Memory:

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz

8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

OMP\_NUM\_THREADS = "32"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

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Software Availability: Sep-2014

## Base Portability Flags (Continued)

```

450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

## Peak Portability Flags

Same as Base Portability Flags



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Tested by: Huawei

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Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>

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