



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp®2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

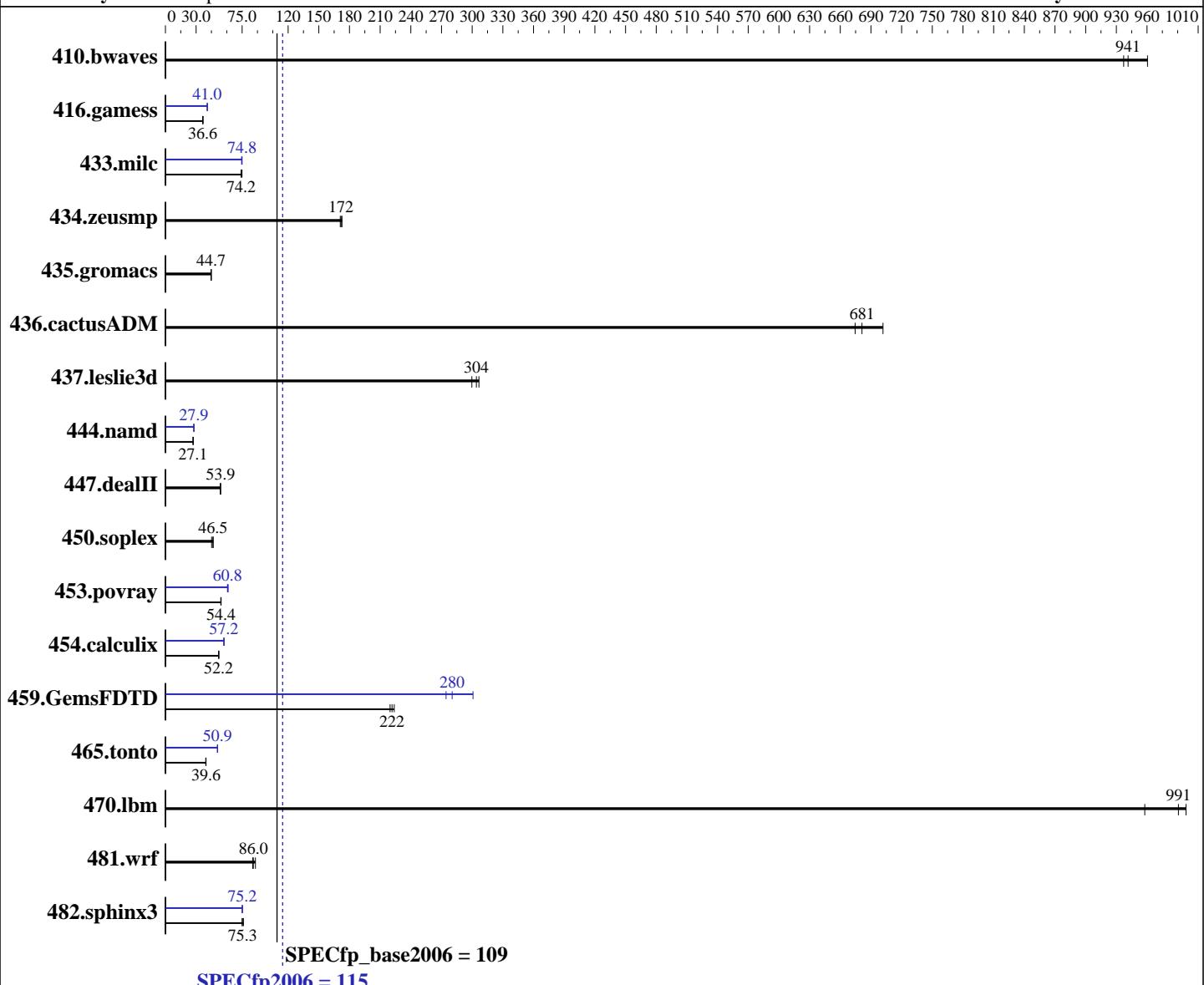
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-4655 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2900
FPU: Integrated
CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12, Kernel 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 512 GB SATA III, SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio								
410.bwaves	14.5	937	14.4	941	14.1	960	14.5	937	14.4	941	14.1	960
416.gamess	537	36.4	532	36.8	535	36.6	479	40.8	478	41.0	478	41.0
433.milc	123	74.9	124	74.0	124	74.2	122	74.9	123	74.8	123	74.5
434.zeusmp	53.2	171	53.0	172	52.7	173	53.2	171	53.0	172	52.7	173
435.gromacs	159	44.8	160	44.7	160	44.7	159	44.8	160	44.7	160	44.7
436.cactusADM	17.5	681	17.7	675	17.0	702	17.5	681	17.7	675	17.0	702
437.leslie3d	30.9	304	31.4	300	30.7	307	30.9	304	31.4	300	30.7	307
444.namd	296	27.1	296	27.1	296	27.1	288	27.9	289	27.7	288	27.9
447.dealII	212	53.9	213	53.7	212	54.0	212	53.9	213	53.7	212	54.0
450.soplex	179	46.5	185	45.2	179	46.7	179	46.5	185	45.2	179	46.7
453.povray	97.8	54.4	98.3	54.1	97.8	54.4	86.7	61.4	87.5	60.8	87.6	60.7
454.calculix	158	52.2	158	52.2	159	52.0	144	57.2	144	57.2	144	57.2
459.GemsFDTD	47.9	222	48.3	220	47.4	224	35.3	301	37.8	280	38.7	274
465.tonto	250	39.4	248	39.6	248	39.7	193	50.9	193	50.9	193	51.1
470.lbm	13.8	998	13.9	991	14.3	958	13.8	998	13.9	991	14.3	958
481.wrf	127	88.0	131	85.4	130	86.0	127	88.0	131	85.4	130	86.0
482.sphinx3	260	75.0	259	75.3	255	76.3	259	75.2	259	75.1	259	75.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Hyper-Threading (ALL) = Disable

Early Snoop = Disable

Enforce POR = Disabled

Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fb8667b5a285932ceab81e28219e1

running on linux-rrui Tue Jun 16 12:37:10 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 =

115

SPECfp_base2006 =

109

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4655 v3 @ 2.90GHz
        4 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings : 6
    physical 0: cores 1 3 5 9 11 12
    physical 1: cores 1 3 5 9 11 12
    physical 2: cores 1 3 5 9 11 12
    physical 3: cores 1 3 5 9 11 12
    cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      529339400 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 0
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
```

```
Linux linux-rrui 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Jun 16 07:09
```

```
SPEC is set to: /home/SPEC2K6/SPEC2006-V12
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        ext4  458G  8.8G  448G   2%  /home
Additional information from dmidecode:
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.00 06/01/2015

Memory:

```
16x NO DIMM NO DIMM
1x Samsung(data:13/44) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:13/48) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
3x Samsung(data:13/51) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
2x Samsung(data:14/13) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:14/16) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
5x Samsung(data:14/17) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
1x Samsung(data:14/25) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
14x Samsung(data:14/26) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
4x Samsung(data:14/47) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,0,1"

LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4655 v3)

SPECfp2006 = 115

SPECfp_base2006 = 109

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Peak Optimization Flags (Continued)

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Aug 6 13:25:31 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 August 2015.