



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL560 Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECfp®2006 = 102

SPECfp_base2006 = 96.2

CPU2006 license: 3

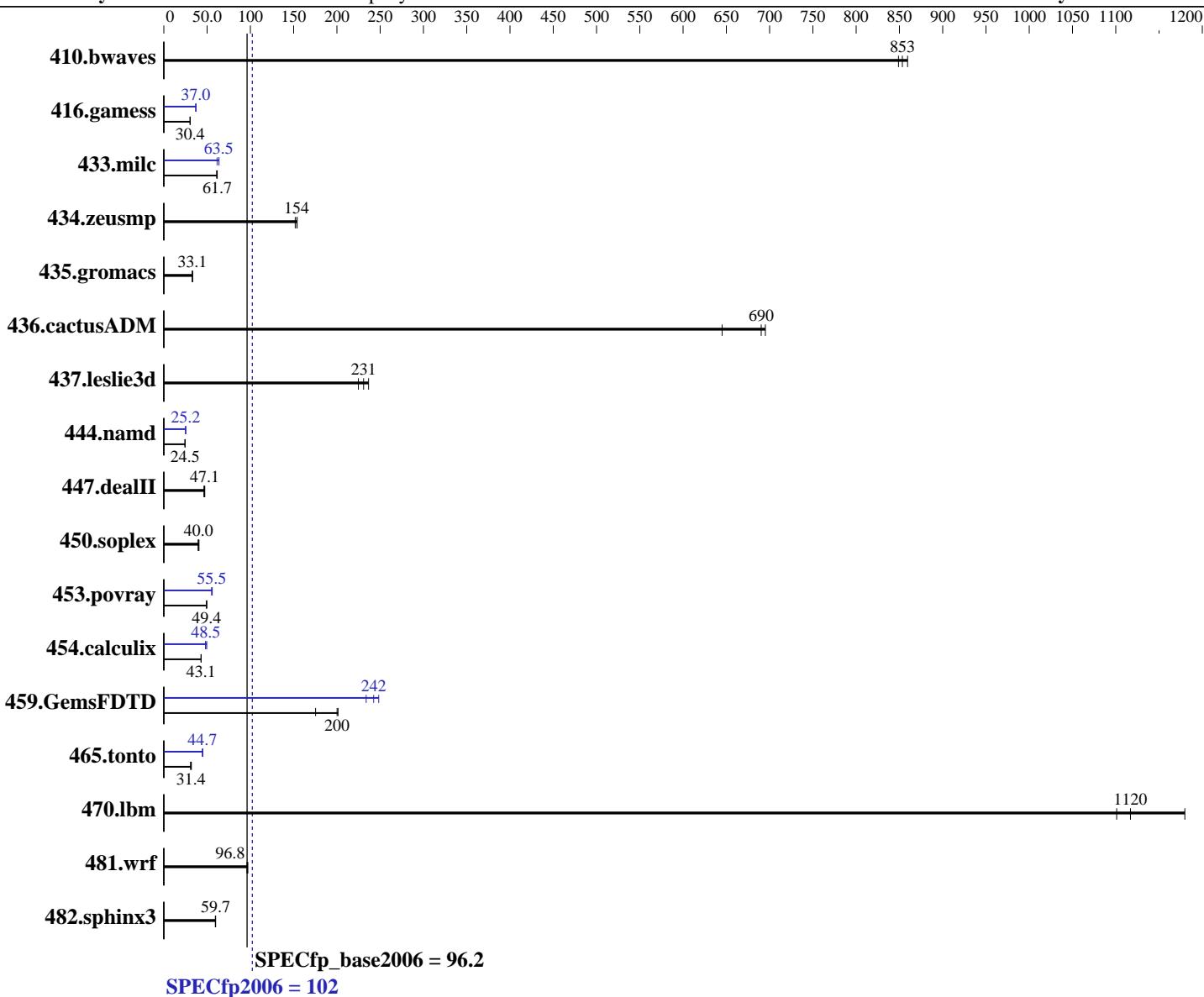
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-4660 v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SuSE Linux Enterprise Server 12 (x86_64)
Kernel 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	15.8	859	16.0	849	<u>15.9</u>	<u>853</u>	15.8	859	16.0	849	<u>15.9</u>	<u>853</u>
416.gamess	645	30.3	643	30.5	645	30.4	529	37.0	529	37.0	530	36.9
433.milc	149	61.7	151	60.9	149	61.7	144	63.7	144	63.5	149	61.5
434.zeusmp	59.9	152	59.1	154	59.2	154	59.9	152	59.1	154	59.2	154
435.gromacs	217	32.9	216	33.1	216	33.1	217	32.9	216	33.1	216	33.1
436.cactusADM	17.3	690	18.5	645	17.2	695	17.3	690	18.5	645	17.2	695
437.leslie3d	40.7	231	39.7	237	41.8	225	40.7	231	39.7	237	41.8	225
444.namd	327	24.5	327	24.5	327	24.5	318	25.2	319	25.1	318	25.2
447.dealII	242	47.3	243	47.1	247	46.3	242	47.3	243	47.1	247	46.3
450.soplex	208	40.0	210	39.6	205	40.6	208	40.0	210	39.6	205	40.6
453.povray	108	49.4	107	49.5	108	49.4	95.0	56.0	95.9	55.5	96.2	55.3
454.calculix	192	43.0	191	43.1	191	43.1	172	48.1	170	48.5	166	49.6
459.GemsFDTD	52.7	201	60.5	175	53.0	200	45.4	234	42.7	248	43.8	242
465.tonto	309	31.8	314	31.4	319	30.9	220	44.7	219	45.0	221	44.6
470.lbm	11.6	1180	12.5	1100	12.3	1120	11.6	1180	12.5	1100	12.3	1120
481.wrf	115	97.0	116	96.0	115	96.8	115	97.0	116	96.0	115	96.8
482.sphinx3	325	59.9	326	59.7	328	59.5	325	59.9	326	59.7	328	59.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core State set to C6 State

Energy/Performance Bias set to Maximum Performance

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

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Platform Notes (Continued)

Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on dl560gen9jks Wed May 20 12:31:48 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4660 v3 @ 2.10GHz
        4 "physical id"s (chips)
        56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 14
        siblings : 14
        physical 0: cores 0 2 4 5 6 8 9 10 11 12 13 14
        physical 1: cores 0 2 4 5 6 8 9 10 11 12 13 14
        physical 2: cores 0 2 4 5 6 8 9 10 11 12 13 14
        physical 3: cores 0 2 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      529311072 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
        NAME="SLES"
        VERSION="12"
        VERSION_ID="12"
        PRETTY_NAME="SUSE Linux Enterprise Server 12"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux dl560gen9jks 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
Continued on next page
```



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Platform Notes (Continued)

(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 19 13:18

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	331G	6.1G	325G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P85 03/05/2015

Memory:

24x	HP	752369-081	16 GB	2 rank	2133 MHz
16x	UNKNOWN	NOT AVAILABLE			
8x	UNKNOWN	NOT AVAILABLE	16 GB	2 rank	2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:

24x	HP	752369-081	16 GB	2 rank	2133 MHz
8x	UNKNOWN	NOT AVAILABLE	16 GB	2 rank	2133 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Software Availability:

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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.deallII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

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Peak Optimization Flags (Continued)

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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