



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 109

Huawei CH242 V3 (Intel Xeon E7-8891 v2)

SPECfp_base2006 = 103

CPU2006 license: 3175

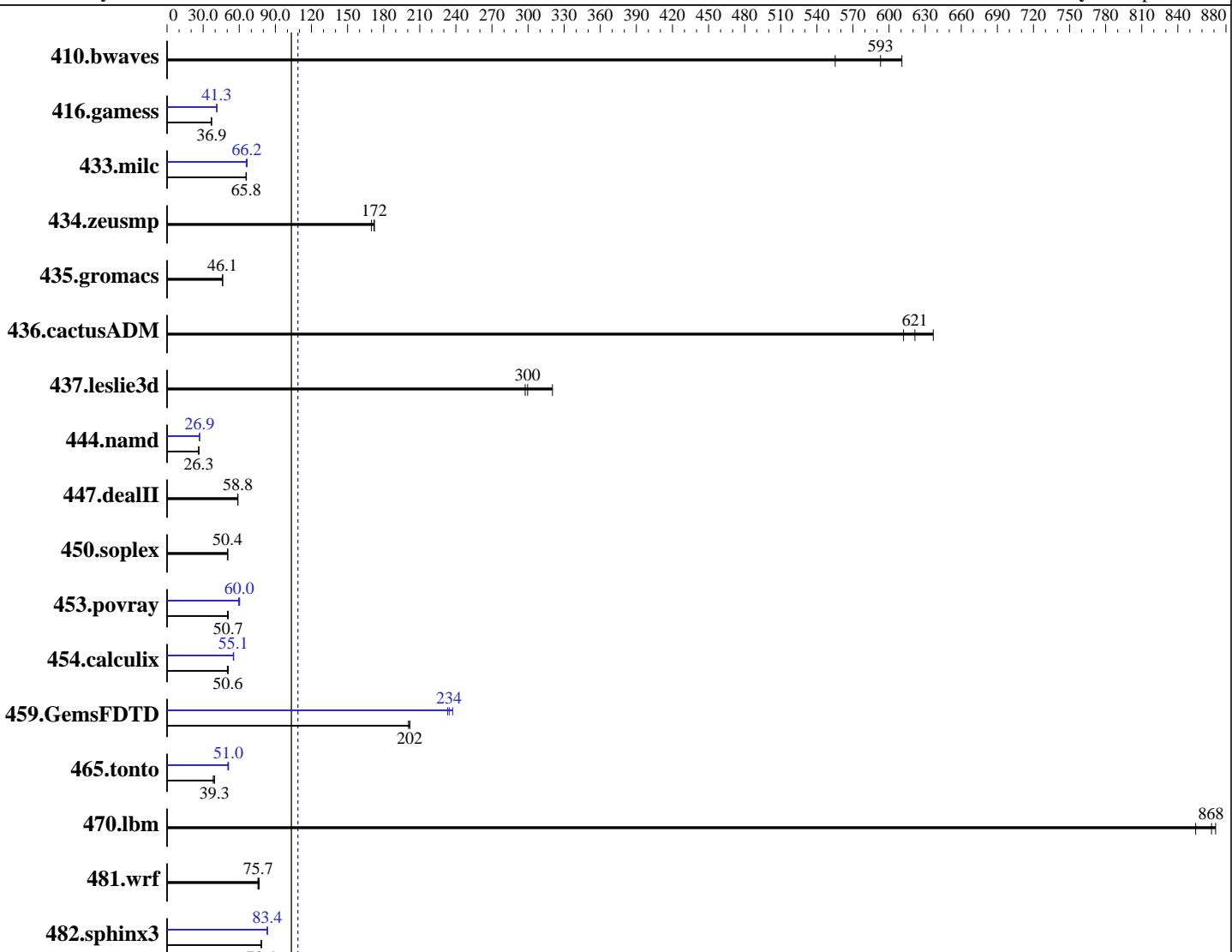
Test date: Jan-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014



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Hardware

CPU Name: Intel Xeon E7-8891 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 Compiler: 3.10.0-123.el7.x86_64
 C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs

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L3 Cache: 37.5 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.3	611	24.5	555	<u>22.9</u>	<u>593</u>	22.3	611	24.5	555	<u>22.9</u>	<u>593</u>
416.gamess	531	36.9	<u>530</u>	<u>36.9</u>	530	37.0	<u>473</u>	<u>41.4</u>	474	41.3	<u>474</u>	<u>41.3</u>
433.milc	139	65.9	<u>139</u>	<u>65.8</u>	140	65.6	<u>139</u>	<u>66.2</u>	140	65.7	138	66.5
434.zeusmp	52.8	172	53.6	170	<u>52.9</u>	<u>172</u>	52.8	172	53.6	170	<u>52.9</u>	<u>172</u>
435.gromacs	<u>155</u>	<u>46.1</u>	155	46.1	155	46.1	<u>155</u>	<u>46.1</u>	155	46.1	155	46.1
436.cactusADM	18.8	637	19.5	612	<u>19.2</u>	<u>621</u>	18.8	637	19.5	612	<u>19.2</u>	<u>621</u>
437.leslie3d	29.3	320	<u>31.4</u>	<u>300</u>	31.6	298	<u>29.3</u>	<u>320</u>	<u>31.4</u>	<u>300</u>	31.6	298
444.namd	<u>305</u>	<u>26.3</u>	305	26.3	305	26.3	298	27.0	<u>298</u>	<u>26.9</u>	298	26.9
447.dealII	<u>194</u>	<u>58.8</u>	194	58.9	195	58.7	<u>194</u>	<u>58.8</u>	194	58.9	195	58.7
450.soplex	165	50.4	<u>165</u>	<u>50.4</u>	166	50.3	<u>165</u>	<u>50.4</u>	<u>165</u>	<u>50.4</u>	166	50.3
453.povray	105	50.7	<u>105</u>	<u>50.7</u>	106	50.2	88.2	60.3	<u>88.7</u>	<u>60.0</u>	89.3	59.6
454.calculix	<u>163</u>	<u>50.6</u>	165	50.1	163	50.8	150	55.1	<u>150</u>	<u>55.1</u>	149	55.4
459.GemsFDTD	52.6	202	<u>52.6</u>	<u>202</u>	52.9	201	44.7	237	<u>45.3</u>	<u>234</u>	45.5	233
465.tonto	250	39.4	<u>251</u>	<u>39.3</u>	256	38.4	<u>195</u>	<u>50.5</u>	<u>193</u>	<u>51.0</u>	193	51.0
470.lbm	<u>15.8</u>	<u>868</u>	16.1	855	15.8	871	<u>15.8</u>	<u>868</u>	16.1	855	15.8	871
481.wrf	148	75.6	<u>148</u>	<u>75.7</u>	146	76.6	148	75.6	<u>148</u>	<u>75.7</u>	146	76.6
482.sphinx3	<u>249</u>	<u>78.4</u>	249	78.2	249	78.4	234	83.2	234	83.4	<u>234</u>	<u>83.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Baseboard Management Controller used to adjust the fan speed to 100%

Set Hyper Threading to Disabled

Sysinfo program /spec/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Jan 29 18:33:36 2015

This section contains SUT (System Under Test) info as seen by

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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7-8891 v2 @ 3.20GHz
        4 "physical id"s (chips)
        40 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 10
        siblings : 10
        physical 0: cores 2 3 4 5 6 7 8 10 11 12
        physical 1: cores 2 3 4 5 6 7 8 10 11 12
        physical 2: cores 2 3 4 5 6 7 8 10 11 12
        physical 3: cores 2 3 4 5 6 7 8 10 11 12
    cache size : 38400 KB
```

```
From /proc/meminfo
MemTotal:       263802256 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 29 13:28

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   445G  163G  282G  37% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS American Megatrends Inc. BLISV308 11/28/2014

Memory:

32x Micron 36KSF1G72PZ-1G6K1 8 GB 2 rank 1600 MHz, configured at 1333 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
           -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
              -parallel
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
              -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
             -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

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Peak Optimization Flags (Continued)

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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