



SPEC® CFP2006 Result

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Lenovo Group Limited

System Model Name
Lenovo System x3650 M5
(Intel Xeon E5-2608L v3, 2.00 GHz)

SPECfp®2006 = 70.9

SPECfp_base2006 = 68.8

CPU2006 license: 9017

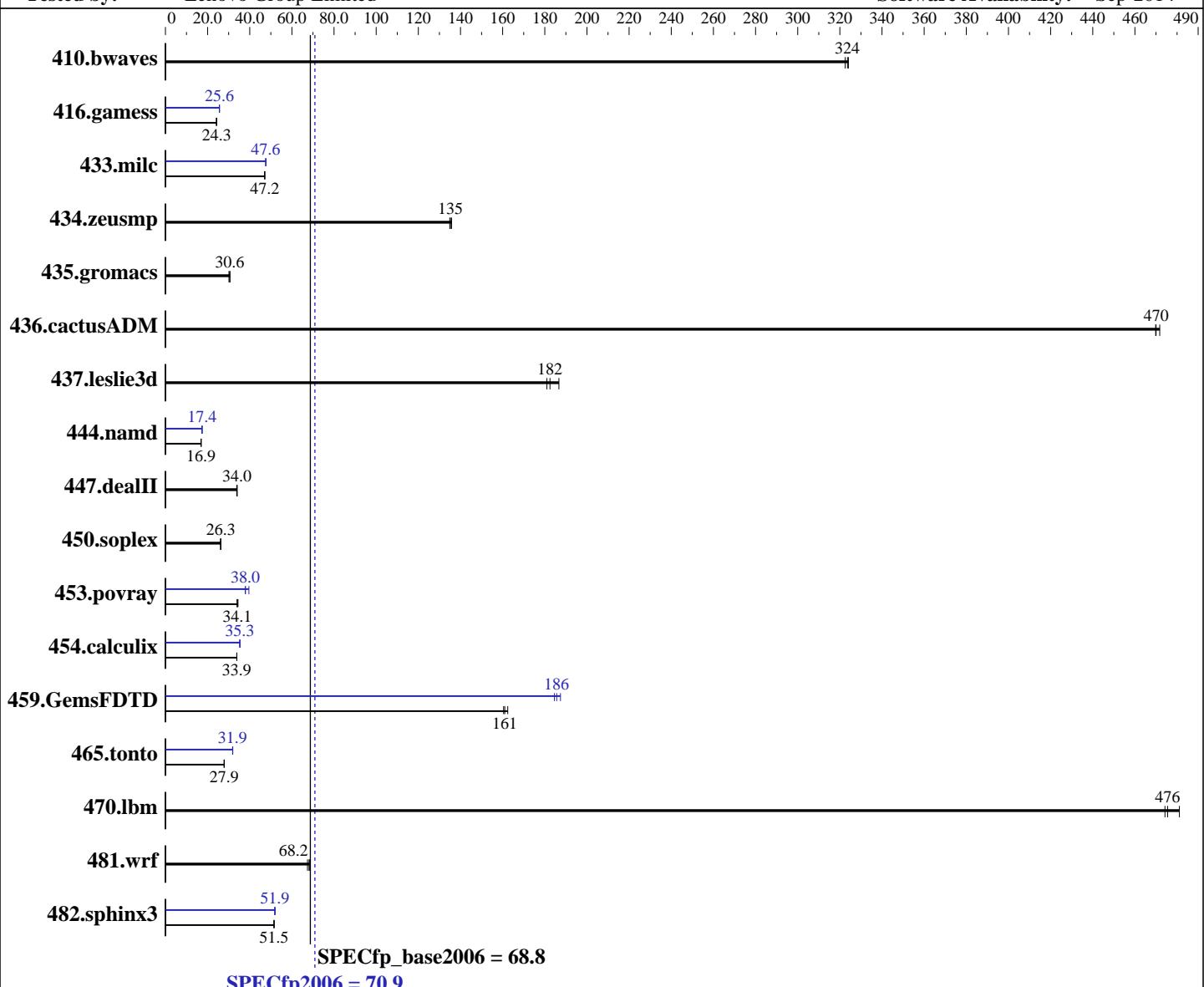
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Apr-2015

Hardware Availability: Oct-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2608L v3
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: 3.10.0-123.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs

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| | |
|-----------------|---|
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz) |
| Disk Subsystem: | 1 x 1000 GB SATA, 7200 RPM |
| Other Hardware: | None |

| | |
|-----------------|--------------------------|
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio |
| 410.bwaves | 42.0 | 324 | 42.1 | 323 | 41.9 | 324 | 42.0 | 324 | 42.1 | 323 | 41.9 | 324 |
| 416.gamess | 805 | 24.3 | 810 | 24.2 | 807 | 24.3 | 763 | 25.7 | 765 | 25.6 | 765 | 25.6 |
| 433.milc | 195 | 47.2 | 195 | 47.0 | 195 | 47.2 | 193 | 47.6 | 193 | 47.6 | 193 | 47.6 |
| 434.zeusmp | 67.0 | 136 | 67.3 | 135 | 67.5 | 135 | 67.0 | 136 | 67.3 | 135 | 67.5 | 135 |
| 435.gromacs | 237 | 30.1 | 234 | 30.6 | 234 | 30.6 | 237 | 30.1 | 234 | 30.6 | 234 | 30.6 |
| 436.cactusADM | 25.3 | 472 | 25.4 | 470 | 25.4 | 470 | 25.3 | 472 | 25.4 | 470 | 25.4 | 470 |
| 437.leslie3d | 50.3 | 187 | 51.9 | 181 | 51.5 | 182 | 50.3 | 187 | 51.9 | 181 | 51.5 | 182 |
| 444.namd | 474 | 16.9 | 474 | 16.9 | 474 | 16.9 | 461 | 17.4 | 461 | 17.4 | 461 | 17.4 |
| 447.dealII | 337 | 34.0 | 336 | 34.0 | 338 | 33.8 | 337 | 34.0 | 336 | 34.0 | 338 | 33.8 |
| 450.soplex | 317 | 26.3 | 318 | 26.3 | 319 | 26.2 | 317 | 26.3 | 318 | 26.3 | 319 | 26.2 |
| 453.povray | 156 | 34.0 | 155 | 34.4 | 156 | 34.1 | 140 | 38.0 | 141 | 37.9 | 134 | 39.6 |
| 454.calculix | 243 | 33.9 | 244 | 33.9 | 244 | 33.9 | 233 | 35.4 | 234 | 35.3 | 234 | 35.3 |
| 459.GemsFDTD | 66.1 | 160 | 65.9 | 161 | 65.3 | 162 | 57.2 | 186 | 57.5 | 185 | 56.6 | 187 |
| 465.tonto | 353 | 27.9 | 353 | 27.9 | 352 | 27.9 | 309 | 31.9 | 308 | 32.0 | 308 | 31.9 |
| 470.lbm | 28.6 | 481 | 29.0 | 474 | 28.9 | 476 | 28.6 | 481 | 29.0 | 474 | 28.9 | 476 |
| 481.wrf | 164 | 68.3 | 164 | 68.2 | 166 | 67.4 | 164 | 68.3 | 164 | 68.2 | 166 | 67.4 |
| 482.sphinx3 | 378 | 51.5 | 379 | 51.5 | 378 | 51.6 | 374 | 52.1 | 375 | 51.9 | 376 | 51.9 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to "Efficiency-Favor Performance"

Sysinfo program /SPECcpu/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fbb8667b5a285932ceab81e28219e1

running on x3650M5 Wed Apr 22 14:35:38 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page



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Platform Notes (Continued)

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2608L v3 @ 2.00GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      263584992 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux x3650M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux
```

```
SPEC is set to: /SPECcpu
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   239G   18G  221G   8% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TCE103EUS-1.01]- 10/21/2014

Memory:

16x Hynix 484D4134324752374D4652344E2D54462020 16 GB 2 rank 2133 MHz,
configured at 1866 MHz

Continued on next page



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Platform Notes (Continued)

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/SPECcpu/libs/32:/SPECcpu/libs/64:/SPECcpu/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64

```

Continued on next page



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Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.xml>

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