



SPEC[®] CFP2006 Result

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Supermicro

SuperServer 1028TR-T
(X10DRT-L, Intel Xeon E5-2697 v3)

SPECfp[®]2006 = 116

SPECfp_base2006 = 111

CPU2006 license: 001176

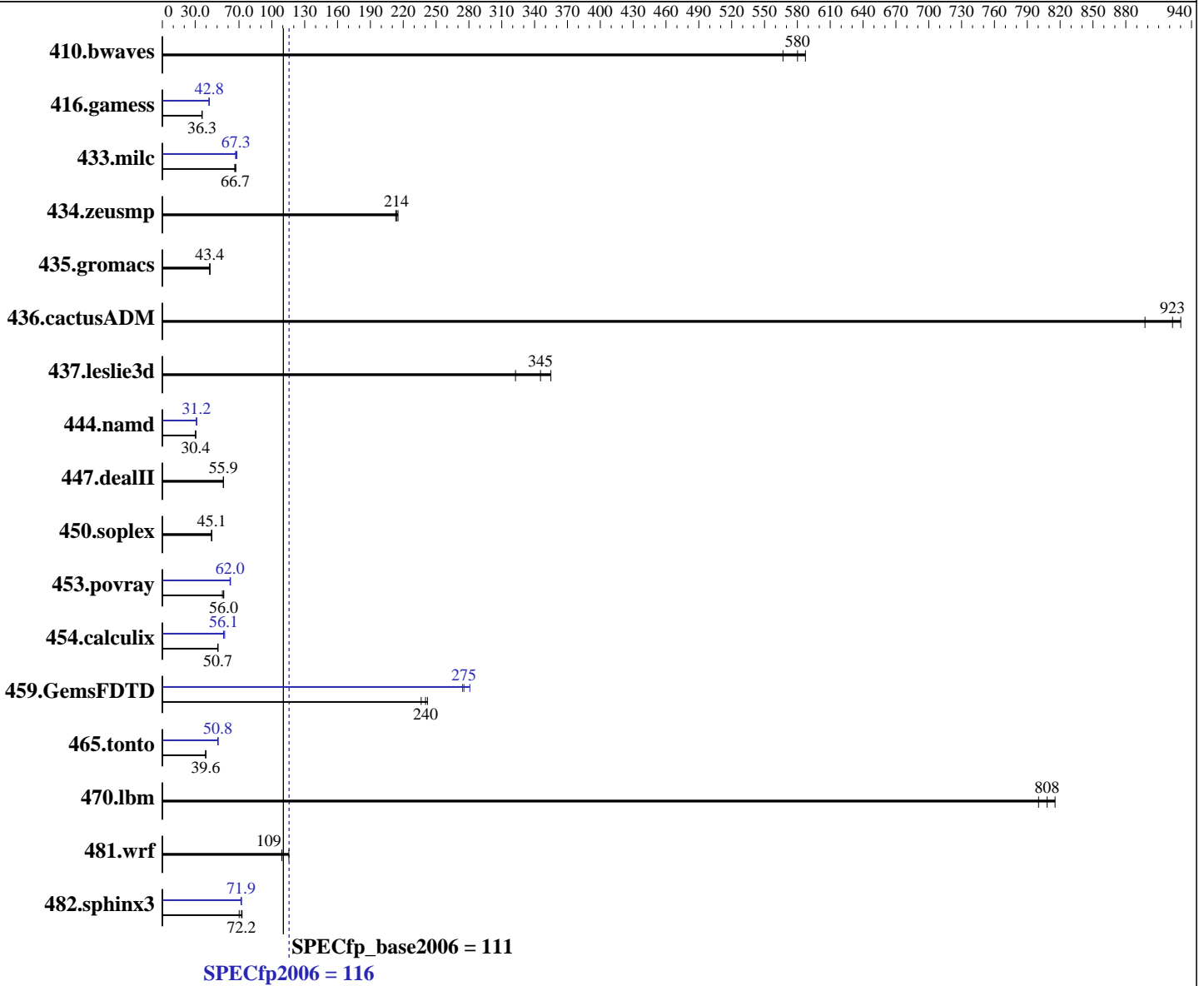
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2697 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0, Kernel 3.10.0-123.9.3.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 200 GB SATA III SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	23.1	587	<u>23.4</u>	<u>580</u>	24.0	567	23.1	587	<u>23.4</u>	<u>580</u>	24.0	567
416.gamess	539	36.3	541	36.2	<u>540</u>	<u>36.3</u>	460	42.6	<u>457</u>	<u>42.8</u>	457	42.8
433.milc	<u>138</u>	<u>66.7</u>	139	66.1	137	67.1	137	66.9	135	68.1	<u>136</u>	<u>67.3</u>
434.zeusmp	42.7	213	42.3	215	<u>42.5</u>	<u>214</u>	42.7	213	42.3	215	<u>42.5</u>	<u>214</u>
435.gromacs	<u>165</u>	<u>43.4</u>	165	43.3	164	43.4	<u>165</u>	<u>43.4</u>	165	43.3	164	43.4
436.cactusADM	<u>13.0</u>	<u>923</u>	12.8	930	13.3	898	<u>13.0</u>	<u>923</u>	12.8	930	13.3	898
437.leslie3d	26.5	355	<u>27.2</u>	<u>345</u>	29.1	323	26.5	355	<u>27.2</u>	<u>345</u>	29.1	323
444.namd	264	30.4	264	30.4	<u>264</u>	<u>30.4</u>	<u>257</u>	<u>31.2</u>	258	31.1	256	31.3
447.dealII	206	55.6	<u>205</u>	<u>55.9</u>	205	55.9	206	55.6	<u>205</u>	<u>55.9</u>	205	55.9
450.soplex	186	44.8	184	45.2	<u>185</u>	<u>45.1</u>	186	44.8	184	45.2	<u>185</u>	<u>45.1</u>
453.povray	<u>95.0</u>	<u>56.0</u>	97.0	54.8	94.9	56.1	85.4	62.3	86.0	61.8	<u>85.9</u>	<u>62.0</u>
454.calculix	162	50.8	<u>163</u>	<u>50.7</u>	163	50.7	148	55.9	<u>147</u>	<u>56.1</u>	145	56.9
459.GemsFDTD	43.8	242	44.9	236	<u>44.1</u>	<u>240</u>	<u>38.5</u>	<u>275</u>	37.8	281	38.7	274
465.tonto	<u>248</u>	<u>39.6</u>	248	39.7	249	39.5	<u>194</u>	<u>50.8</u>	193	50.9	194	50.7
470.lbm	<u>17.0</u>	<u>808</u>	16.9	815	17.2	800	<u>17.0</u>	<u>808</u>	16.9	815	17.2	800
481.wrf	96.7	116	102	109	<u>102</u>	<u>109</u>	96.7	116	102	109	<u>102</u>	<u>109</u>
482.sphinx3	<u>270</u>	<u>72.2</u>	278	70.2	268	72.8	269	72.3	271	71.8	<u>271</u>	<u>71.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Enforce POR = Disabled
Memory Frequency = 2133
Early Snoop = Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on X10DRT-01 Thu Apr 9 19:11:36 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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Software Availability: Sep-2014

Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697 v3 @ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

```

```

From /proc/meminfo
MemTotal:      131752440 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux X10DRT-01 3.10.0-123.9.3.el7.x86_64 #1 SMP Thu Oct 30 00:16:40 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Apr 9 19:08

```

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   183G  109G   75G  60% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0 01/29/2015
Memory:

Continued on next page



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Platform Notes (Continued)

8x Samsung (date:14/5q) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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